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Variable DC voltage based reactive power enhancement scheme for MMC-STATCOM

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Abstract

Constrained by the AC voltage amplitude modulated by a modular multilevel converterbased static synchronous compensator (MMC-STATCOM), its reactive power output may be subject to oscillations under grid contingencies, posing a threat to the grid stable operation. To solve this problem, this paper proposes a variable DC voltage (VDCV)based reactive power enhancement scheme for MMC-STATCOM. In this scheme, a novel variable DC voltage control is designed, which can increase the DC voltage in a transient state for relaxing the constraint of the AC voltage amplitude modulated by MMC-STATCOM and improving its reactive power output capability (RPC). At the same time, to make full use of the improved RPC of MMC-STATCOM, a VDCV scheme also proposes an optimisation algorithm of its reactive current-AC voltage droop coefficient using the established reactive power model of the MMC-STATCOM. Based on small signal modelling and analysis, the key parameters of the proposed VDCV scheme are optimised. The performance and reactive power enhancement of the VDCV scheme is evaluated through the hardware-in-the-loop experiment under grid disturbances.

KEYWORDS

overvoltage, power system stability, reactive power control, voltage-source convertors

1 **INTRODUCTION**

The modular multilevel converter-based static synchronous compensator (MMC-STATCOM) possesses the advantages of fast reactive power regulation speed, wide operation range and low harmonic content [1-4], being proven to be one of the important technologies to guarantee the grid voltage dynamic stability. Especially in the case of grid voltage sags, MMC-STATCOM can provide a certain level of short-term overload capability, output additional reactive power and improve the support effect for the grid voltage in transient states [5, 6]. According to STATCOM design standards and engineering application examples [7, 8], the overload operation time for a STATCOM is allowed in maximum 2s at 1.5 times of its rated current. However, the stability constraints of MMC-STATCOM (e.g., modulation index, submodule capacitor and voltage ripple) may restrict its additional reactive power output. Among them, the modulation index constraint limits the MMC-STATCOM reactive power output to the greatest extent [9-12]. As the MMC-STATCOM enters the overmodulation region, the voltage at the point of common coupling (PCC) can be subject to the high total harmonic distortion (THD) for its distorted modulated AC voltage waveform [13, 14], which may not fulfil the grid-connection codes and other requirements [15–17] (e.g., IEEE Standard 519 recommends that the THD of the PCC voltage should not be more than 3%).

To ensure grid voltage stability, it is necessary to improve the reactive power output capability (RPC) of MMC-STATCOM. The existing research studies focus on increasing the modulation index to expand the amplitude of the modulated AC voltage [18-22]. Directly increasing the modulation index may lead to the overmodulation of the MMC converter station. Moreover, when a modulation index limiter is added to avoid overmodulation [18], the control system may be saturated at its limit, causing the reactive power oscillations of MMC-STATCOM. The modulation strategy of MMC based

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on third-order harmonic voltage injection can effectively expand the modulation index range to increase the amplitude of the modulated AC voltage [19, 20], but its improvement effect can be sensitively affected by the circulating current suppression control [21], and also, the calculation of the thirdorder harmonic voltage amplitude additionally takes up controller computation resources [22]. In fact, the amplitude of the modulated AC voltage is not only subject to the modulation index but also influenced by its DC voltage [14]. According to the insulation requirements of the MMC-STATCOM DC system, DC voltage can vary within a small range (such as $\pm 10\%$) around its rated value [10]. Therefore, increasing the DC voltage within an allowable range to increase the modulated AC voltage amplitude can also be an effective manner to improve the RPC of MMC-STATCOM. In this regard, rare relevant work is reported.

The reactive current-AC voltage droop coefficient of MMC-STATCOM is determined by the ratio of the allowable variation range of PCC voltage to the maximum reactive current output [23, 24]. The limited RPC of MMC-STATCOM constrains its maximum reactive current output, resulting in a large droop coefficient and consequently an underutilised regulation on grid voltage. Therefore, with an improved RPC of MMC-STATCOM, its droop coefficient can be further optimised to enhance its regulation effect on the grid voltage. However, the existing literature on improving the RPC of MMC-STATCOM rarely involves the droop coefficient optimisation.

Therefore, this paper proposes a variable DC voltage (VDCV)-based reactive power enhancement scheme for MMC-STATCOM. The main features of the proposed VDCV scheme are summarised as follows:

- (1) VDCV scheme designs a novel variable DC voltage control, which can extend the operating range of the modulated AC voltage and improve the RPC of MMC-STATCOM by temporarily raising the DC voltage. Different from third-order harmonic voltage injection schemes in Refs. [19, 20], the VDCV scheme does not require complex calculations, and it also avoid the adverse control impact for improving RPC.
- (2) VDCV scheme designs a droop coefficient optimisation algorithm based on an established MMC-STATCOM reactive power model, effectively lowering the droop coefficient and improving its grid voltage regulation effect.

The rest of the paper is as follows: Section 2 introduces the topology and traditional control of MMC-STATCOM. Section 3 establishes the reactive power model of MMC-STATCOM and analyses the limiting factors and potential improvement manners of the MMC-STATCOM RPC. Section 4 proposes the specific VDCV design, including variable DC voltage control and droop coefficient optimizsation. Section 5 optimises the key parameters of the VDCV scheme based on small signal analysis. Section 6 verifies the effectiveness of the VDCV scheme via hardware-in-the-loop experiments. Section 7 summarises the full text.

2 | MMC-STATCOM TOPOLOGY AND TRADITIONAL CONTROL

This section introduces the topology and traditional control of MMC-STATCOM, which provides a theoretical basis to analyse the RPC of MMC-STATCOM and design the novel control.

2.1 | MMC-STATCOM topology

The topology of a three-phase MMC-STATCOM is shown in Figure 1. Each phase of MMC contains the upper and lower arms, and each arm includes an arm inductor (L_0) and N cascaded half-bridge sub-modules (SMs). Each sub-module consists of a DC side capacitor (C) and four semiconductor switching devices $(D_1, D_2, S_1, \text{ and } S_2)$. The coupling point of the upper and lower arms is connected to PCC via a three-phase transformer (L_i) . In Figure 1, u_{pcc} and i are the voltage and current at the PCC, respectively; u_v is the MMC modulated AC voltage; and U_{dc} is the DC voltage. MMC-STATCOM maintains DC voltage stability and realises the modulated voltage output by adjusting the switching state of submodules.

2.2 | MMC-STATCOM traditional control

The MMC-STATCOM traditional control is presented in Figure 2. The outer controller employs constant DC voltage control and reactive current-AC voltage droop control to ensure the stable operation of DC voltage and the PCC voltage, respectively. The inner current controller generates modulation signals for MMC according to the current reference values generated by the outer controller, and adopts a modulation index limiter to avoid MMC from overmodulation. The control system of MMC-STATCOM also includes capacitor voltage balancing control (VBC) and circulating current suppression control (CCSC), which are reported in Ref. [25].

3 | MMC-STATCOM REACTIVE POWER MODEL AND RPC ANALYSIS

This section establishes the MMC-STATCOM reactive power model, analyses its reactive power output constraints and discusses the feasible improvement methods of its RPC.

3.1 | MMC-STATCOM reactive power model and constraints

The equivalent AC circuit of MMC is shown in the grey box of Figure 1. The reactive power flow Q between the MMC and the AC system can be written as:

FIGURE 1 MMC-STATCOM topology.

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where U_{pcc} and U_v are the RMS value of u_{pcc} and u_v , respectively; X_t and X_0 are the reactances of the transformer and the arm inductor, respectively; X_{eq} is the equivalent reactance between u_{pcc} and u_v ; and δ is the phase angle difference between u_{pcc} and u_v ($\delta \approx 0$ as MMC-STATCOM has no active power interaction). To simplify the analysis, all variables in this paper adopt per unit values.

The droop control of MMC-STATCOM characterises the relationship between the PCC voltage reference value U_{pcc}^* and the reactive current reference value i_q^* [23]:

$$U_{pcc}^{*} = U_{pcc0} + k_d i_q^{*} \tag{2}$$

where U_{pcc0} is the rated value of U_{pcc} , and k_d is the droop coefficient. In a practical system, the PCC voltage is allowed to fluctuate in a certain range, and the reactive power compensation device can participate in the regulation of the PCC voltage by using a simple reactive current-AC voltage droop control. Meanwhile, some grid connection regulations specify the range of the droop coefficient value for the reactive current-AC voltage droop control [17]. In order to ensure the studied system is consistent with an actual system, this paper adopts reactive current-AC voltage droop control to regulate the PCC voltage. The droop coefficient can be determined by the following equation:

$$k_d = \frac{\Delta U_{\text{max}}}{I_v \text{ max}} \tag{3}$$

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The reactive current i_q is given as follows:

$$i_q = -\frac{U_v - U_{pcc}}{X_{eq}} \tag{4}$$

Via proportional-integral (PI) control, the actual values of the aforementioned variables are equal to their reference values in a steady state, thus $U_{pcc} = U_{pcc}^*$ and $i_q = i_q^*$. The relationship between U_{pcc} and U_v can be obtained by combining (2) and (4):

$$U_{pcc} = \frac{X_{eq}U_{pcc0} - k_d U_v}{X_{eq} - k_d}$$
(5)

Furthermore, by combining (1) and (5), Q can be written as follows:

$$Q = \frac{-k_d U_v^2 + (X_{eq} + k_d) U_{pcc0} U_v - X_{eq} U_{pcc0}^2}{(X_{eq} - k_d)^2}$$
(6)

According to (6), Q is mainly restricted by U_v when the system parameters X_{eq} , U_{pcc0} and k_d are given, and Q increases with the increase of U_v . The modulated AC voltage limit U_{vmax} is determined by the modulation index limit m_{max} and DC voltage limit U_{dcmax} :

$$U_{v\max} = k_v \frac{m_{\max} U_{dc\max}}{2\sqrt{2}} \tag{7}$$

where k_v is a constant given by U_{dc0}/U_{v0} , where U_{dc0} and U_{v0} are the rated values of DC voltage and the modulated AC voltage, respectively.

Substituting (7) into (6), the maximum reactive power output of MMC-STATCOM under the modulated AC voltage constraint Q_{vmax} can be obtained as follows:

$$Q_{v\max} = \frac{1}{8(X_{eq} - k_d)^2} [-k_d (k_v m_{\max} U_{dcmax})^2 + 2\sqrt{2} U_{pcc0} (X_{eq} + k_d) k_v m_{\max} U_{dcmax} - 8X_{eq} U_{pcc0}^2]$$
(8)

Substituting (7) into (5), the maximum reactive power output of MMC-STATCOM under the overload current constraint Q_{imax} can be obtained as follows:

$$Q_{imax} = \frac{2\sqrt{2}X_{eq}U_{pcc0} - k_d k_v m_{max} U_{dcmax}}{2\sqrt{2}(X_{eq} - k_d)} I_{max}$$
(9)

where I_{max} is the MMC overload current limit.

Considering the two comprehensive RPC constraints of the modulated AC voltage and the overload current limit above, the maximum reactive power output of MMC- STATCOM Q_{max} is eventually constrained by the smaller value in (8) and (9) as follows:

$$Q_{\max} = \min\{Q_{v\max}, Q_{i\max}\}$$
(10)

3.2 | RPC analysis of MMC-STATCOM

Based on the established reactive power model of MMC-STATCOM, this sub-section analyses the RPC of MMC-STATCOM and its possible improvement methods.

The selected parameters k_d , k_v , X_{eq} , U_{pcc0} , and I_{max} (1.5 times the rated current is adopted in this paper) in (8) and (9) are presented in Table A1. Under the traditional control, U_{dcmax} is normally fixed at 1pu and m_{max} fixed at 1. Figure 3 shows the three-dimensional relationships of Q_{vmax} or Q_{imax} , m_{max} and U_{dcmax} . It can be observed that Q_{vmax} is far lower than Q_{imax} , indicating that the modulated AC voltage constraint is the main impact factor limiting the RPC of MMC-STATCOM and resulting in the underutilisation of MMC-STATCOM overload capability.

In order to break through the modulated AC voltage constraint and improve the RPC of MMC-STATCOM, it can be seen from Figure 3 and (8) that Q_{vmax} can be significantly improved by increasing m_{max} or U_{dcmax} . The traditional thirdorder harmonic voltage injection strategy of increasing m_{max} has complex calculations and is subject to the influence of the circulating current suppression control [19–22], so the VDCV scheme which improves the RPC by increasing the DC voltage is introduced in the following section.

4 | VDCV SCHEME FOR ENHANCING REACTIVE POWER OF MMC-STATCOM

This section proposes a VDCV scheme which includes variable DC voltage control and droop coefficient optimisation. Variable DC voltage control improves the RPC of MMC-



FIGURE 3 Relationships between Q_{vmax} , Q_{imax} and m_{max} , U_{dcmax}

STATCOM by flexibly adjusting the DC voltage, while droop coefficient optimisation reduces the droop coefficient in (3) for a better grid voltage support effect.

4.1 | Variable DC voltage control

To expand the adjustable range of the modulated AC voltage and improve the RPC of MMC-STATCOM, variable DC voltage control adds a modulation index control to adjust the DC voltage reference value U_{dc}^* when modulation index *m* exceeds its limit m_{max} , which can be expressed as follows:

$$\begin{cases} U_{dc}^* = U_{dc0} + \Delta U_{dc} \\ \Delta U_{dc} = \left(k_p + \frac{k_i}{s}\right)(m - m_{\max}), 0 \le \Delta U_{dc} \le \Delta U_{dc\max} \end{cases}$$
(11)

where ΔU_{dc} is the correction value of U_{dc}^* ; k_p and k_i are the proportional coefficient and integral coefficient of the

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modulation index control, respectively; and ΔU_{dcmax} is the upper limit of ΔU_{dc} , which is set as 0.1pu in this paper. The lower limit of ΔU_{dc} is set at 0 to prevent the modulation index control being active when unnecessary. The control loop of variable DC voltage control is shown in the red line in Figure 4, and CPS-SPWM generates switching signals to control the switching of sub-modules according to the modulated AC voltage reference signal (the number of MMC-STATCOM sub-modules in this paper is moderate, so CPS-SPWM can be adopted without causing significant loss increase).

4.2 | RPC improvement effect analysis of variable DC voltage control

To evaluate the RPC improvement effect of variable DC voltage control, this section sets up an MMC-STATCOM connected system, as illustrated in Figure 5, and establishes its mathematical model to quantify the RPC of MMC-STATCOM under the proposed control.

The power exchange between the MMC-STATCOM and its connected system is as follows:



FIGURE 4 Variable DC voltage control.

FIGURE 5 Diagram of the MMC-STATCOM connected system.

$$\begin{cases}
P_L = P_s = \frac{U_s U_{pcc} \sin \theta}{X_s} \\
Q_L = Q_s + Q = \frac{U_{pcc} (U_s \cos \theta - U_{pcc})}{X_s} + \frac{U_{pcc} (U_v - U_{pcc})}{X_{eq}} \\
\end{cases}$$
(12)

where U_s is the AC gird voltage; θ is the phase angle difference between U_s and U_{pcc} ; X_s is the equivalent reactance of the AC system; $P_L + jQ_L$ is the active and reactive load power; and $P_s + jQ_s$ is the active and reactive power from the AC system.

By combining (5) and (12), the modulated AC voltage U_v can be obtained under specific U_s and $P_L + jQ_L$. Then substituting U_v into (6), the reactive power Q can be obtained accordingly. It is worth stressing that U_v should not exceed its limit $U_{v\text{max}}$ during the substitution to get the correct reactive power output of MMC-STATCOM.

Figure 6 shows the three-dimensional relationship of Q, U_s and Q_L . When U_s is decreased or Q_L is increased within region I, the reactive power output from the MMC-STATCOM Q will be increased with the proposed control, the same as the performances with traditional control. When U_s further decreases or Q_L further increases to Region II, under the traditional control, the reactive power output from the MMC-STATCOM Q reaches its limit and cannot increase as wanted. However, under the proposed variable DC voltage control, Q can further increase since it effectively breaks through the constraint of the modulated AC voltage by increasing the MMC-STATCOM DC voltage.

4.3 | Droop coefficient optimization of MMC-STATCOM

After improving the RPC of MMC-STATCOM by the

proposed variable DC voltage control, the optimisation of the droop coefficient k_d in (3) makes it possible to further



FIGURE 6 Relationship between Q and Q_L , U_s .

enhance the voltage regulation effect of MMC-STATCOM. Curve ① in Figure 7 shows the reactive current regulation of MMC-STATCOM under traditional control. The reactive current is regulated following line AB during the change of U_{pcc} and is limited at the maximum reactive current I_{vmax} as in line BC. With the analysis in Section 3, it can be found that I_{vmax} is smaller than the rated reactive current, resulting in a large droop coefficient k_d and weak voltage regulation effect. On the other hand, under variable DC voltage control, the maximum reactive current can be increased to I_{vmax}' as in line ② according to Section 3, so k_d can be correspondingly reduced to fully utilise the improved RPC and enhance the grid voltage regulation effect. Therefore, in this section, the droop coefficient k_d is optimised to identify the slope of line ③.

According to (4), (5), (7), the maximum reactive current I_{vmax} ' can be derived as follows:

$$I_{vmax}' = \frac{k_v m_{max} U_{dcmax} \left(2k_d - X_{eq}\right)}{2\sqrt{2}X_{eq} \left(X_{eq} - k_d\right)} - \frac{U_{pcc0}}{\left(X_{eq} - k_d\right)}$$
(13)

It can be observed from (3) and (13) that k_d and I_{omax}' are mutually effected. Therefore, an iterative method is adopted in this paper to finalise droop coefficient k_d . The optimisation flow chart is presented in Figure 8, and the specific optimisation steps are as follows:

Step 1 Obtain the parameters related to the calculation of droop coefficient k_d , including ΔU_{\max} in (3), k_v , m_{\max} , U_{dcmax} , X_{eq} and U_{pcc0} in (13).

Step 2 Solve the optimised droop coefficient through iteration. Firstly, given an initial droop coefficient k_{d0} , obtain I_{vmax} through (13). Then, substitute I_{vmax} into (3) to obtain the optimised droop coefficient k_d . Finally, determine whether the



FIGURE 7 Comparison of the MMC-STATCOM droop coefficient before and after optimisation.

absolute value of the difference between k_{d0} and k_d meets the accuracy requirements.

Figure 9 shows the three-dimensional relationship of k_{ds} ΔU_{max} and $U_{d\text{cmax}}$. It can be seen from Figure 9 that the droop coefficient k_d under variable DC voltage control (that is, when $U_{d\text{cmax}} > 1$) is lower than that under traditional control (i.e., when $U_{d\text{cmax}} = 1$). In addition, as seen in Figure 9, with the increase of $U_{d\text{cmax}}$, the droop coefficient correspondingly decreases as the RPC of MMC-STATCOM is improved, thereby reducing the drop of the grid voltage.

5 | SMALL SIGNAL ANALYSIS AND PARAMETERS OPTIMIZATION OF VDCV SCHEME

To optimise the key control parameters of the VDCV scheme, a small signal model (SSM) of the MMC-STATCOM connected system as shown in Figure 5 is established in this section. The 47.5 MVar-rated MMC-STATCOM is connected to the AC grid (SCR = 4) through a 32 kV/230 kV step-up transformer. The fixed static load L1 is $S_{L1} = P_{L1} + jQ_{L1} = 28.5$ MW + j47.5MVar. The switchable load L2 is $S_{L2} = jQ_{L2} = j16.63$ MVar and is



FIGURE 8 The flow chart of droop coefficient optimisation.

switched out in the initial state. The state space equations and specific parameters of the system can be seen in Tables A1 and A2 and Appendix B.

Linearising the state space equations using the linear analysis tool in the Matlab/Simulink platform [26]. The SSM obtained by the linearisation can be expressed as follows:

$$\frac{d}{dt}\Delta \boldsymbol{X} = \boldsymbol{A}\Delta \boldsymbol{X} + \boldsymbol{B}\Delta \boldsymbol{U}$$
(14)

where Δ is the small disturbance variation; X is the state column vector; A is the state matrix; B is the input matrix; and U is the input column vector.

To verify the accuracy of SSM, its dynamic responses are compared with those of its time-domain electromagnetic transient (EMT) model in Figure 10. At t = 0.1s, the reactive load step increases by 16.63MVar, and the responses of SSM are accurately aligned with those from the EMT model, which verifies the correctness of SSM.

The eigenvalues of the MMC-STATCOM connected system are listed in Table C1 in which modes $\lambda_{7,8}$ and $\lambda_{11,12}$ with low damping ratio are identified as the dominant modes to be focused.

The parameters k_p and k_i of the modulation index control are optimised in this paper. When k_p increases from 0 to 10, as shown in Figure 11a, $\lambda_{7,8}$ move away from the imaginary axis, which indicates the increase of the damping ratio and the improvement of the system stability. $\lambda_{11,12}$ firstly move away from the imaginary axis and then approach it with the increase of k_p and enter the right half plane after k_p exceeds 2.5, which indicates the unstable of the system. Considering the comprehensive influence of k_p on $\lambda_{7,8}$ and $\lambda_{11,12}$, the optimal $k_p = 0.5$ is selected, for the damping ratio of $\lambda_{11,12}$ is the largest and $\lambda_{7,8}$ is far away from the imaginary axis. With the optimal k_p , the oscillation and overshoot of the modulation index response can be significantly suppressed under the step increase of reactive load power, as shown in Figure 11b.



FIGURE 9 Relationship between k_d and ΔU_{max} , $U_{d\text{cmax}}$.



FIGURE 11 Dominant eigenvalue loci and modulation index response under different k_p and k_i : (a) k_p varies from 0 to 10, (b) Modulation index response, (c) k_i varies from 0 to 400, (d) Modulation index response.

When k_i increases from 0 to 400, as shown in Figure 11c, $\lambda_{11,12}$ firstly move away from the imaginary axis and then approach it, resulting in a continuous decrease in a damping ratio. $\lambda_{11,12}$ cross the imaginary axis when k_i increases to 320, leading to system instability. The optimal $k_i = 40$ is selected as its damping ratio is the largest. With the optimal k_i , the modulation index oscillation can be effectively suppressed, and the response speed can be ensured under the step increase of reactive load power, as shown in Figure 11d.

HARDWARE-IN-THE-LOOP 6 **EXPERIMENTAL RESULTS**

To verify the effectiveness of the VDCV scheme, a hardwarein-the-loop experimental platform of the MMC-STATCOM connected system as shown in Figure 12 is set up in this section. The experimental platform consists of a dSPACE SCA-LAXIO processing system for MMC-STATCOM control and an RT-LAB OP5600 simulator for the real-time simulation of



FIGURE 12 MMC-STATCOM connected system and controller-hardware-in-the-loop experimental platform.

the MMC-STATCOM connected system. The performances of the following three schemes on the grid voltage support is compared and analysed under the scenarios of the reactive load step and AC grid three-phase ground fault. The three schemes consist of

- Traditional control scheme 1 (TC1): The traditional control introduced in Section 2, which has the modulation index limiter and k_d is set as 0.045;
- 2) Traditional control scheme 2 (TC2): The traditional control without the modulation index limiter and k_d is set as 0.045;
- Proposed VDCV scheme, and k_d is set as 0.032 according to the optimisation algorithm in Section 4.

6.1 | Scenario 1: reactive load step

Reactive load L2 (as seen in Figure 12) is switched in at t = 0.1s, and the system response is illustrated in Figure 13. The increase of the reactive load causes the PCC voltage to decrease, as shown in Figure 13a. It can be observed from Figure 13b that TC1 limits the modulation index at its limit through the modulation index limiter, thus triggering the oscillation of the DC voltage, as shown in Figure 13c. The modulation index limiter of TC1 also causes the low-frequency oscillation and even divergence of the reactive power and the instability of the PCC voltage, as shown in Figures 13a,f. The modulation index exceeds its limit in TC2, and MMC-



FIGURE 13 Simulation comparison of reactive power load step increase: (a) PCC voltage U_{pcc} , (b) Modulation index m, (c) DC voltage U_{dc} , (d) Modulated AC voltage u_{va} , (e) PCC voltage THD and (f) Reactive power output Q.

STATCOM enters the overmodulation region, as shown in Figure 13b. The overmodulation results in the oscillation of DC voltage and the distortion of the modulated AC voltage, as shown in Figures 13c,d, thus dramatically increasing the THD of the PCC voltage, as shown in Figure 13e. In addition, the overmodulation causes the high-frequency oscillation of the reactive power and the PCC voltage, as shown in Figures 13a,f.

Compared with TC1 and TC2, the VDCV scheme effectively avoids DC voltage oscillation and overmodulation by raising the DC voltage, as shown in Figures 13b,c, thus ensuring the stability of the reactive power and PCC voltage and significantly improving the reactive power output and reducing the PCC voltage drop based on the optimised droop coefficient, as shown in Figures 13a,f.

6.2 | Scenario 2: AC grid three phase ground fault

A temporary 300 ms three-phase fault occurs at the AC grid bus at t = 0.1s, which results in a 5% voltage drop of the PCC voltage, as shown in Figure 14a. Compared with TC1 and TC2, VDCV controls the modulation index at its limit by increasing the DC voltage, avoids the overmodulation and the triggering of the modulation index limiter and ensures the stability of the modulation index and DC voltage, as shown in Figures 14b,c. Figures 14d,e show that VDCV expands the adjustable range of the modulated AC voltage, which avoids the distortion of the PCC voltage and reduces its THD to around 0. As seen in Figures 14a,f, VDCV ensures the stability of the reactive power



FIGURE 14 Simulation comparison of AC grid three phase transient fault: (a) PCC voltage U_{pcc} (b) Modulation index m, (c) DC voltage U_{dc} (d) Modulated AC voltage u_{vas} (e) PCC voltage THD, (f) Reactive power output Q.

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output and the PCC voltage, and with the optimised droop coefficient, the reactive power output and the PCC voltage performance are significantly improved.

7 | CONCLUSION

Considering the constraint of the modulated AC voltage, the reactive power output of MMC-STATCOM is potentially unstable under grid contingencies. This paper proposes a variable DC voltage (VDCV)-based reactive power enhancement scheme for MMC-STATCOM. With the designed variable DC voltage control, the DC voltage is effectively increased in a transient state to diminish the constraint of the modulated AC voltage amplitude, providing the advantages of RPC improvement and overmodulation avoidance. The optimisation algorithm adjusts the reactive current-AC voltage droop coefficient, enabling the MMC-STATCOM to make full use of its improved RPC. Small signal analysis conducted obtains the suitable parameters for the proposed VDCV scheme. The theoretical analysis and hardware-in-the-loop results verify the effectiveness of the proposed VDCV scheme in improving the RPC and the voltage support of MMC-STATCOM.

AUTHOR CONTRIBUTIONS

Lujie Yu: Project administration; conceptualisation; writing – review and editing. Guoyan Wang: Formal analysis; roles/ writing – original draft. Tong Wu: Conceptualisation; roles/ writing – original draft. Jiebei Zhu: Writing–review and editing; supervision. Campbell D. Booth: Project administration.

CONFLICT OF INTEREST STATEMENT

The authors declare no conflicts of interest.

DATA AVAILABILITY STATEMENT

The data that support the findings of this study are available from the corresponding author upon reasonable request.

PERMISSION TO REPRODUCE MATERIALS FROM OTHER SOURCES

None.

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APPENDIX A

SIMULATION AND CONTROL SYSTEM PARAMETERS

See Tables A1 and A2.

TABLE A1 Simulation system parameters.

Module	Item	Value
MMC-STATCOM	SM number per arm N	32
	SM capacitance C	4.95 mF
	Arm inductance L ₀	20 mH
	Carrier frequency f_c	1.05 kHz
	Rated dc voltage U_{dc0}	64 kV
	Rated output ac voltage U_{v0}	32 kV
	Rated PCC voltage U_{pcc0}	230 kV
	Transformer ratio	32/230 kV
	Transformer inductance L_t	10 mH
	Rated Apparent power S	47.5 MVar
	Maximum ac current $I_{\rm max}$	1286 A
	Droop coefficient k_d	0.045
AC grid system	AC grid voltage	230 kV
	AC equivalent inductance L_s	250 mH
	SCR	4

T	A	B	L	\mathbf{E}	Aź	2	Control	system	parameters
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Parameter	Value
DC voltage outer controller loop (k_{p1}, k_{i1})	(8, 400)
Droop controller loop (k_{p2}, k_{i2})	(1, 100)
Inner current controller loop (k_{p3}, k_{i3})	(0.5, 50)
Circulating current suppression controller loop (k_{P4}, k_{i4})	(0.5, 50)
PLL (k_{p5}, k_{i5})	(60, 1400)
Modulation index controller loop (k_p, k_i)	(0.5, 40)

APPENDIX B

MMC-STATCOM STATE SPACE EQUATIONS

The small signal modelling of MMC-STATCOM mainly includes four parts: MMC main circuit, MMC control system, transformer and coordinate transformation. The state-space equations of each part is as follows:

- 1) State space equations of MMC main circuit:
- ① Sub-module capacitor voltage fluctuation dynamic DC component:

$$\frac{d\overline{u_c}}{dt} = \frac{u_{vq}i_q}{4CU_{dc}} + \frac{u_{vd}i_d}{4CU_{dc}} + \frac{u_{cirq}i_{cirq}}{2CU_{dc}} + \frac{u_{cird}i_{cird}}{2CU_{dc}}$$
(B1)

where $\overline{u_c}$ is the DC component of capacitor voltage; u_{vdq} is the modulated AC voltage in dq-axis; i_{dq} is the PCC current in dq-axis; u_{cirdq} is the output voltage of the circulating current suppression controller in dq-axis; i_{cirdq} is the circulating current in *dq*-axis.

Fundamental component:

$$\begin{pmatrix}
\frac{du_{c_{-1d}}}{dt} = -\omega u_{c_{-1q}} - \frac{i_d}{4C} - \frac{u_{vq}i_{cirq}}{2CU_{dc}} - \frac{u_{vd}i_{cird}}{2CU_{dc}} \\
\frac{u_{cirq}i_q}{4CU_{dc}} - \frac{u_{cird}i_d}{4CU_{dc}} \\
\frac{du_{c_{-1q}}}{dt} = -\omega u_{c_{-1d}} - \frac{i_q}{4C} - \frac{u_{vd}i_{cirq}}{2CU_{dc}} + \frac{u_{vq}i_{cird}}{2CU_{dc}} - \\
\frac{u_{cirq}i_d}{4CU_{dc}} + \frac{u_{cird}i_q}{4CU_{dc}}
\end{cases}$$
(B2)

where $u_{c \ 1dq}$ is the fundamental frequency component of capacitor voltage in *dq*-axis.

Second harmonic component:

$$\begin{cases} \frac{du_{c_{-2d}}}{dt} = -2\omega u_{c_{-2q}} + \frac{i_{cird}}{2C} - \frac{u_{vq}i_q}{4CU_{dc}} + \frac{u_{vd}i_d}{4CU_{dc}} \\ \frac{du_{c_{-2q}}}{dt} = 2\omega u_{c_{-2d}} + \frac{i_{cirq}}{2C} + \frac{u_{vd}i_d}{4CU_{dc}} + \frac{u_{vq}i_q}{4CU_{dc}} \end{cases}$$
(B3)

where u_{c_2dq} is the second harmonic component of capacitor voltage in dq-axis. Third harmonic component:

$$\frac{du_{c_{-3x}}}{dt} = 3\omega u_{c_{-3y}} - \frac{i_{cirq}u_{vd}}{2CU_{dc}} - \frac{i_{cird}u_{vq}}{2CU_{dc}} - \frac{u_{cird}i_q}{4CU_{dc}} - \frac{u_{cirq}i_d}{4CU_{dc}} = -\frac{u_{cirq}i_d}{4CU_{dc}} + \frac{du_{c_{-3y}}}{2CU_{dc}} - \frac{3\omega u_{c_{-3x}}}{2CU_{dc}} - \frac{i_{cird}u_{vd}}{2CU_{dc}} + \frac{u_{cird}i_q}{4CU_{dc}} - \frac{u_{cird}i_d}{4CU_{dc}} + \frac{u_{cird}i_q}{4CU_{dc}} + \frac{u_{cird}i_q}{4CU_{dc}} - \frac{u_{cird}i_d}{4CU_{dc}} + \frac{u_{cird}i_q}{4CU_{dc}} - \frac{u_{cird}i_d}{4CU_{dc}} + \frac{u_{cird}i_d}{4CU_{dc}} + \frac{u_{cird}i_q}{4CU_{dc}} - \frac{u_{cird}i_d}{4CU_{dc}} + \frac{u_{cird}i_q}{4CU_{dc}} - \frac{u_{cird}i_d}{4CU_{dc}} + \frac{u_{cird}i_q}{4CU_{dc}} - \frac{u_{cird}i_d}{4CU_{dc}} + \frac{u_{cird}i_d}{4CU_{dc}} + \frac{u_{cird}i_d}{4CU_{dc}} + \frac{u_{cird}i_q}{4CU_{dc}} - \frac{u_{cird}i_d}{4CU_{dc}} + \frac{u_{cird}i_d}{4CU_$$

where $u_{c_{3xy}}$ is the third harmonic component of capacitor voltage in dq-axis.

② DC voltage dynamic:

$$U_{dc} = N\overline{u_c} - \frac{Nu_{cq}u_{c_1q}}{U_{dc}} - \frac{Nu_{cd}u_{c_1d}}{U_{dc}} + \frac{Nu_{cirq}u_{c_2q}}{U_{dc}} + \frac{Nu_{cird}u_{c_2d}}{U_{dc}}$$
(B5)

③ AC current dynamic:

$$\begin{cases} \frac{di_d}{dt} = \frac{u_{sd}}{L_{eq}} + \frac{Nu_{c_{-1}d}}{2L_{eq}} - N \frac{2\overline{u_c}u_{vd} + u_{vq}u_{c_{-2}q} + u_{vd}u_{c_{-2}q}}{2L_{eq}U_{dc}} + \\ N \frac{u_{cirq}u_{c_{-1}q} + u_{cird}u_{c_{-1}d} + u_{c_{-3}x}u_{cirq} + u_{c_{-3}y}u_{cird}}{2L_{eq}U_{dc}} - \omega i_q \\ \frac{di_q}{dt} = \frac{u_{sq}}{L_{eq}} + \frac{Nu_{c_{-1}q}}{2L_{eq}} - N \frac{2\overline{u_c}u_{vq} + u_{vd}u_{c_{-2}q} - u_{vq}u_{c_{-2}d}}{2L_{eq}U_{dc}} + \\ N \frac{u_{cirq}u_{c_{-1}d} - u_{cird}u_{c_{-1}q} + u_{c_{-3}x}u_{cird} - u_{c_{-3}y}u_{cirq}}{2L_{eq}U_{dc}} + \omega i_d \\ \end{cases}$$
(B6)

④ Circulating current dynamic:

$$\frac{di_{cird}}{dt} = -2\omega i_{cirq} - \frac{Nu_{c_2d}}{2L_{arm}} - \frac{N\overline{u_c}u_{cird}}{L_{arm}U_{dc}} + \frac{Nu_{vd}u_{c_{-1d}} - Nu_{vq}u_{c_{-1q}} + Nu_{c_{-3x}}u_{vq} + Nu_{c_{-3y}}u_{vd}}{2L_{arm}U_{dc}}$$

$$\frac{di_{cirq}}{dt} = -2\omega i_{cird} - \frac{Nu_{c_2q}}{2L_{arm}} - \frac{N\overline{u_c}u_{cirq}}{L_{arm}U_{dc}} + \frac{Nu_{vd}u_{c_{-1q}} + Nu_{vq}u_{c_{-1d}} + Nu_{c_{-3x}}u_{vd} + Nu_{c_{-3y}}u_{vq}}{2L_{arm}U_{dc}}$$
(B7)

The small signal model of MMC uses u_{vdq} , ω , u_{cirdq} as its input variables, which provides interface for inner current controller loop, PLL controller and circulating current suppression controller loop respectively; u_{sdq} provides the interface with AC system; U_{dc} provides the interface with DC system.

- 2) State space equations of MMC control system:
- ① Outer controller loop

$$\begin{cases}
i_{d}^{*} = \left(k_{p1} + \frac{k_{i1}}{s}\right) \left(U_{dc} - U_{dc}^{*}\right) \\
i_{q}^{*} = \left(k_{p2} + \frac{k_{i2}}{s}\right) \left(U_{pcc} - U_{pcc}^{*}\right) \\
U_{pcc}^{*} = 1 + k_{d}i_{q}^{*}
\end{cases}$$
(B8)

where i_{dq} * is the PCC current reference in dq-axis. ② Inner current controller loop

$$\begin{cases} u_{vd} = \left(k_{p3} + \frac{k_{i3}}{s}\right)\left(i_d^* - i_d\right) - \omega L_{eq}i_q + u_{pccd} \\ u_{vq} = \left(k_{p3} + \frac{k_{i3}}{s}\right)\left(i_q^* - i_q\right) + \omega L_{eq}i_d + u_{pccq} \end{cases}$$
(B9)

where u_{vdq}* is the modulated AC voltage reference in dq-axis. u_{pccdq}* is the PCC voltage reference in dq-axis.
③ Circulating current suppression controller loop

$$\begin{cases} i_{cird}^{*} = 0, i_{cirq}^{*} = 0\\ u_{cird}^{*} = -(k_{p4} + k_{i4}/s)(i_{cird}^{*} - i_{cird}) - 2\omega L_{0}i_{cirq} \\ u_{cirq}^{*} = (k_{p4} + k_{i4}/s)(i_{cirq}^{*} - i_{cirq}) + 2\omega L_{0}i_{cird} \end{cases}$$
(B10)

where i_{cirdq}^* is the circulating current reference in dq-axis; u_{cirdq}^* is the output voltage reference of the circulating current suppression controller in dq-axis.

④ PLL controller

$$\begin{cases} \theta = \frac{\omega}{s} \\ \omega = \left(k_{p5} + \frac{k_{i5}}{s}\right) u_{pccq} \end{cases}$$
(B11)

3) State space equations of the transformer:

$$\begin{cases} \frac{di_{d}}{dt} = \omega i_{q} - \frac{R_{t}}{L_{t}} i_{d} + \frac{u_{td} - u_{t1d}}{L_{t}} \\ \frac{di_{q}}{dt} = -\omega i_{d} - \frac{R_{t}}{L_{t}} i_{q} + \frac{u_{tq} - u_{t1q}}{L_{t}} \end{cases}$$
(B12)

APPENDIX C

MAIN EIGENVALUES OF MMC-STATCOM CONNECTED SYSTEM

See Table C1.

TABLE C1	Main eigenvalues of the MMC-STATCOM connected
system.	

λ	Eigenvalue	Damping ratio	Frequency (Hz)
λ_1	-185,840	1	0
λ_2	-17,670	1	0
$\lambda_{3,4}$	-15620±j632.89	0.9992	100.7280
$\lambda_{5,6}$	-875.02±j2341.8	0.35	372.7135
$\lambda_{7,8}$	-209.78±j1919.3	0.1087	305.4703
λ _{9,10}	-275.46±j122.03	0.9143	19.421
λ _{11,12}	-31.740±j115.58	0.2648	18.3950
$\lambda_{13,14}$	-26.62±j20.83	0.7877	3.3146
$\lambda_{15,16}$	-35.95±j6.9	0.9821	1.0988
λ_{17}	-64.05	1	0
$\lambda_{18,19}$	$-119.19 \pm j24.4$	0.9797	3.8841
λ_{20}	-127.21	1	0
λ_{21}	-96.7	1	0
λ_{22}	-103.24	1	0