

# A new fast-acting backup protection strategy for embedded MVDC links in future distribution networks

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**Abstract** — This paper presents a new fast-acting backup protection strategy for future hybrid ac-dc distribution networks. By examining the impedance measured by a distance protection relay measuring from the “ac-side” of the network, a unique characteristic is established for faults occurring on the “dc-side” of an embedded medium-voltage dc (MVDC) link, interconnecting two 33 kV distribution network sections. Based on the identified impedance characteristic, appropriate settings are developed and deployed on a verified software model of a commercially available distance protection relay. To remain stable for ac-side faults, it is found that the tripping logic of the device must be altered to provide correct time grading between standard, ac, protection zones and the fast-acting dc region, which can identify faults on the dc system within 40 ms. An additional confirmatory check is also employed to reduce the likelihood of mal-operation. Trials on a test system derived from an actual distribution network, which employs distance protection, are shown to provide stable operation for both ac-side and dc-side pole-pole and pole-pole-ground faults.

**Index Terms**— Distance protection, Medium-voltage direct-current (MVDC), Power system protection, Smart grids.

## I. INTRODUCTION

**D**ISTRIBUTION Network Operators (DNOs), as they evolve to become Distribution System Operators (DSOs) [1], are under increasing pressure to find new methods to accommodate growing levels of both embedded generation and new demand while using existing system assets as far as possible. Several regulator-funded projects in Great Britain (GB) have been tasked with investigating pioneering approaches to increase system flexibility within parts of their electrical distribution networks [2] [3].

Embedded medium-voltage direct current (MVDC) links provide DSOs with the capability to control network sections dynamically thus allowing substantial increases in power flows while remaining within the thermal ratings of transformers and conductors [4] [5]. This is being validated through demonstration projects notably in GB, where two DNOs have received regulator funding to deploy multi-megawatt dc demonstrators on their system [6] [7], and in Zhuhai, China [8]. Several vendors have recently begun offering MVDC solutions for power distribution applications [9] [10]. Internationally,

MVDC technologies have been proposed for shipboard [11], rail traction [12], mining [13], rural low-power/long-distance electrification [14] and for generation collection network [15] [16] applications.

The protection required to safeguard controlled MVDC links from a dc-side fault needs to be fast acting otherwise the power electronic devices are likely to be irreversibly damaged [17] [18]. Generally, dc system protection requires faster fault clearance than ac systems [19].

The primary aim of this work is to determine whether conventional ac-side distance protection remains a valid and appropriate approach for the protection of hybrid ac-dc distribution networks. In particular, the studies conducted assess whether a suitably configured distance relay can provide sufficiently fast, backup protection for dc events should the converter station ac-side circuit breaker (CB) fail.

This paper examines the impedance, under faulted conditions, of an MVDC link as calculated by an ac-connected distance protection relay. No literature has been found which examines the ac-side impedance during a dc-side fault on power electronic links, however studies examining the impact of high-voltage dc (HVDC) and embedded generation (with ac fault ride through capability) are more readily available [20] [21].

The proposed backup protection scheme reported in this paper is assessed using a numerical distance protection relay model developed at the University of Strathclyde and corroborated by National Grid [22]. Studies conducted in the paper examine a section of actual distribution network in Scotland with protection settings derived from the local DNO’s long term development statement.

Section II presents a brief overview of distance protection relaying and converter protection strategies. Section III introduces the study methodology and discusses system modelling parameters. Section IV examines the apparent impedance measured during a dc-side fault and implements the proposed fast-acting backup protection methodology. Discussion surrounding reported results is carried out in Section V followed by concluding remarks in Section VI.

In summary, this paper proposes a remote, fast-acting, backup protection strategy for dc-links embedded in distribution networks. The solution employs existing distance

Manuscript originally received 22<sup>nd</sup> July 2019; revised 6<sup>th</sup> December 2019 and 10<sup>th</sup> March 2020; accepted 25<sup>th</sup> April 2020.

This work has been supported through the Engineering and Physical Sciences Research Council (EPSRC) Centre for Doctoral Training in Future Power Networks and Smart Grids (EP/L015471/1).

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protection and measurement equipment and does not require communications.

## II. BACKGROUND

This section reviews conventional relay setting guidance for distance protection of 33 kV distribution networks. The protection strategy for a dc-side fault on an MVDC link is outlined.

### A. Distance Protection Operation

The decision to trip or not (and whether the trip is instantaneous or occurs after a time delay) within distance protection relays is proportional to the impedance between the measurement point and the fault location [23]. This principle allows operational zones, typically three (Z1, Z2, and Z3), to be defined as presented in Fig. 1 [24]. Zone 1 generally reaches to approximately 80% of the first protected feeder (Line RS in Fig. 1) to prevent overreach into Zone 2 which would be caused by errors in the transducers, relay and estimation of line parameters. Zone 2 is typically set to 120% impedance of the protected line (120% of Line RS). Zone 3 generally reaches to 120% of the combined impedance of lines RS and ST [25]. Maximum distance protection clearance times adopted by a particular DNO are presented in Table I [26]. The delayed trip times for Z2 and Z3 are designed to ensure that in the event of failure of the main protection to operate (i.e. Z1) then other distance relays will break in the vicinity.

Distance protection is used commonly in transmission and primary distribution systems to provide both fast acting main protection in addition to backup functionality [25].

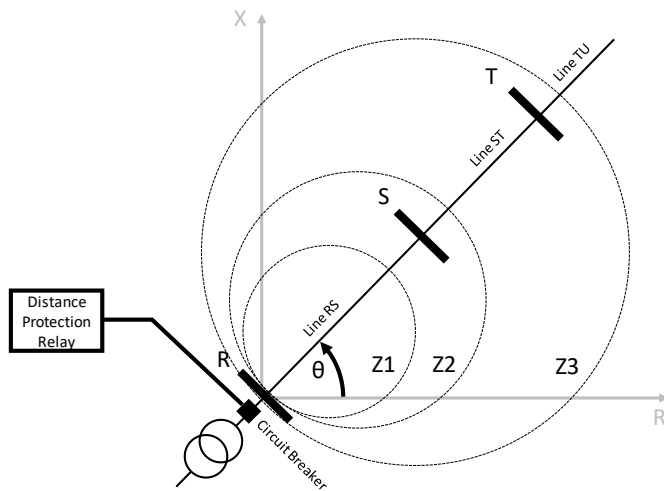


Fig. 1. Distance protection zone diagram with Mho characteristic

TABLE I  
MAXIMUM CLEARANCE TIMES FOR DISTANCE PROTECTION SCHEMES AS SPECIFIED BY A SCOTTISH DNO

Zone	Maximum permissible clearance time (s)
Z1	0.150
Z2	0.500
Z3	1.300

### B. Converter Protection Strategy for DC Faults

While grid-connected converters can provide limited and controlled fault current contribution to ac-side faults, via the converter’s IGBTs (insulated-gate bipolar transistor) [27], their performance during a dc fault is effectively uncontrolled [17] for most converter topologies and can only be overcome at the expense of significant power losses [28]. When a dc-side fault occurs on a dc link and is detected by the converter controller, the first step taken by the protection system is to block commutation of the switching devices. This prevents the power electronic devices being subjected to high fault currents which would only be limited by system and converter filter impedances [17]. The dc-side capacitors discharge forcing the freewheel diodes, placed across the power switches, into conduction as shown in Fig. 2 for a two-level topology. The converter essentially becomes an uncontrolled rectifier with current only being limited by the ac-side inductance between the source and the converter [29]. Bypass devices (not shown in Fig. 2), often high capacity thyristors, may be installed to divert current away from the diodes allowing time for protection to operate [30] [31].

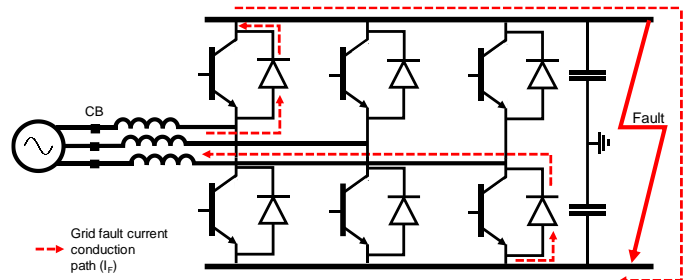


Fig. 2. Example of converter fault path through freewheel diode for a pole to pole (PP) fault

The ac-side CB of the converter should open to isolate current infeed to the link. This needs to happen quickly to protect the parallel diodes from overheating and failing critically [32].

Should any of these steps fail (converter controller failure, trip signal failure, maloperation of ac-side CB), the converter could be left in a damaging (e.g. freewheeling diodes are typically not rated for continuous operation) uncontrolled rectifying state as described previously.

## III. STUDY METHODOLOGY

This section will present the modelling methodology for the study. A two-stage simulation process will be employed. Sampled voltages and currents are generated via detailed transient simulation. These waveforms are then used as inputs to a software-based distance protection relay simulator.

### A. Network Modelling

The network section modelled is based upon an area of the 33 kV network in the south of Scotland where the benefits of introducing embedded MVDC links has been shown via simulation [33].

**B. Converter Modelling**

The converter has a power rating of 35 MVA operating under a two-level symmetrical monopole configuration with dc voltage ( $V_{dc}$ ) of  $\pm 27$  kV, which is broadly equivalent to 33 kV rms line-line voltage from an insulation perspective.

The per-unit impedance of the converter filter is 10% with a wye-delta transformer of 20% impedance (referred to the converter base impedance) connected to the ac network to limit fault current during dc-side faults and improve power quality during normal operation.

Fig. 3 presents a control overview diagram of the MVDC link. The converter control deploys a standard inner current control strategy [34]. Station A is set to maintain a  $P$ - $Q$  reference while Station B deploys a  $V_{dc}$ - $Q$  control under normal pre-fault operation [35]. Note that the pre-fault operating control strategy does not affect the proposed protection solution introduced later in the paper and is only used to create a pre-fault power transfer across the dc link.

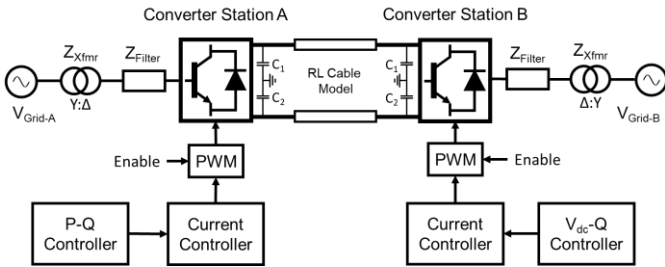


Fig. 3. MVDC link control topology

When a fault is detected, the converter would behave as outlined in Section II.B. In summary, switching of the IGBTs would be blocked and the bypass thyristors enabled. The bypass thyristors are activated when the absolute value of an instantaneous current entering the converter is equal to or greater than the user-definable current threshold ( $I_{max}$ ). (In this model  $I_{max}$  is set as 1.1 times the rated current of the converter). Should an overcurrent be detected on any phase, the bypass thyristors will be enabled to create a parallel conduction path across the IGBT diodes. The freewheeling diodes and bypass thyristors conduct fault current until the converter is isolated via ac-side breakers. Fig. 4 presents an overview of the bypass controller.

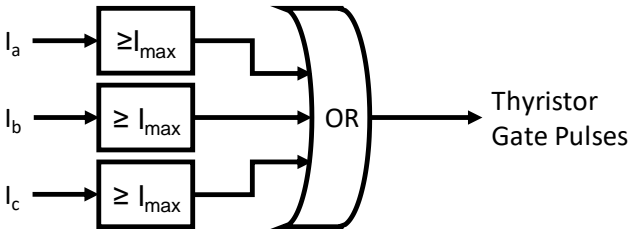


Fig. 4. Overview of bypass thyristor controller

**C. Distance Relay Modelling**

The Dynamic Protection Modelling Controller (DPMC) [22] software package is used to determine the impedance of the network measured during steady state and fault conditions. This software was developed to assist the local Transmission System Operator (TSO), National Grid, in the post-fault investigation of power system performance under various transient faults and

events. The relay models within DPMC are based upon and validated against commercially available devices.

The software uses sampled three phase voltages and current values along with a relay setting file as inputs. Tripping times of the relay are calculated according to the setting file logic. An R-X locus diagram is also generated for visual interrogation of how the apparent impedance changes as the event progresses.

**D. Simulation Strategy**

Fig. 5 summarizes the developed simulation approach.

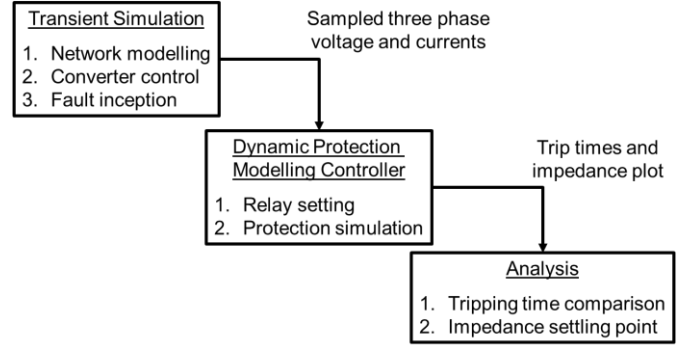


Fig. 5. Simulation flow diagram

**IV. CASE STUDIES**

Two main studies are presented in this paper. The first determines how a dc-side fault would be observed by ac-side distance protection devices. The second establishes if the relay can be configured to react to such a fault in an appropriate manner without compromising operation of the existing ac system protection zones.

**A. Test Network**

The test network used for modelling is presented in Fig. 6 where the characteristic impedance of all lines is  $(0.17 + j0.3) \Omega/\text{km}$ . AC conductors are modelled as lumped element R-L components while the dc cable is implemented as a single  $\pi$ -section with the shunt capacitance modelled via the dc capacitors [36]. The network is a three-bus system where a 10 km 35 MW MVDC link has been introduced at busbar B. Key network and converter parameters used in the model are summarized in the appendix.

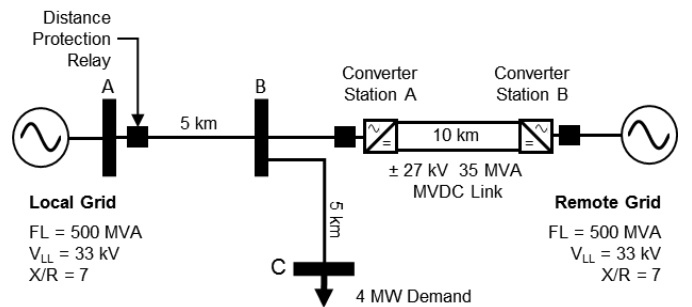


Fig. 6. Test network section

**B. Relay Setting Parameters**

Relay settings are assigned in accordance with [37] [38] with trip times selected to ensure that fault clearance occurs inside of the maximum permissible clearance times outlined in Table

I. The angle ( $\theta$ ) of the mho characteristic is established using the line impedance ( $0.17 + j0.3 \Omega/km$ ) and by considering the ratio between the downstream line reactance and resistance as specified by equation (1) where  $X_{line}$  is the reactance of the protected line and  $R_{line}$  is the resistance. The apparent impedance of the conductor ( $Z_{line}$ ) is calculated in equation (2). Equations (3) - (5) calculate the standard ac protection zone settings.

$$\theta = \tan^{-1}\left(\frac{X_{line}}{R_{line}}\right) = \tan^{-1}\left(\frac{0.3}{0.17}\right) = 60.5^\circ \quad (1)$$

$$Z_{line} = \sqrt{(R_{line}^2 + X_{line}^2)} = \sqrt{0.17^2 + 0.3^2} = 0.345 \Omega/km \quad (2)$$

$$Z1 = 0.8 \times (Z_{line} \times 5 km) = 1.4 \Omega \quad (3)$$

$$Z2 = 1.2 \times (Z_{line} \times 5 km) = 2.1 \Omega \quad (4)$$

$$Z3 = 1.2 \times (Z_{line} \times (5 km + 10 km)) = 6.2 \Omega \quad (5)$$

Key distance zone setting parameters are outlined in Table II (note that the impedances are primary referred and do not take current transformers (CT) or voltage transformers (VT) ratios into account).

Parameter	Value
Mho angle ( $^\circ$ )	60.5
Z1 ( $\Omega$ )	1.4
Z2 ( $\Omega$ )	2.1
Z3 ( $\Omega$ )	6.2
Z2 fault delay [phase & earth] (s)	0.3
Z3 fault delay [phase & earth] (s)	1.1

### C. Fault Analysis

It is assumed that the MVDC converter does not provide any fault current contribution for ac-side faults (i.e. switching of IGBTs is paused), although it is recognized that significant fault infeed from a converter could influence relays' impedance measurements for ac faults [39].

The behavior and performance of the distance protection relay will now be outlined, initially for ac-side faults and then for pole-to-pole-to-ground (PPG) dc-side faults.

#### 1) AC-side zone verification

To verify the correct setting of the distance protection relay for standard ac system faults (i.e. Z1, Z2 and Z3), six fault studies were conducted, covering a wide range of fault locations and types. Transient simulations were carried out using a model implemented in Matlab's Simulink (SimPowerSystems). Faults are simulated at 0.5 s in all cases.

Table III (where G indicates the detection of a fault to ground and P signifies a phase fault) presents which of the internal comparators of the distance protection relay leads to the tripping of the CB for each fault study. For the scenarios conducted the relay operates within the required tripping times defined previously in Table I.

TABLE III  
SIMPLIFIED RELAY LOG FOR Z1, Z2, Z3 AC-SIDE TEST FAULTS

Scenario		Time of Trip (s)	Tripping Element						
Fault Location	Fault Type		Z1		Z2		Z3		
			G	P	G	P	G	P	
Line AB midpoint (Z1)	A-B-C-G	0.5144		♦					
Busbar B (Z2)	A-B-C-G	0.8229				♦			
Busbar C (Z3)	A-B-C-G	1.6221							♦
Line AB midpoint (Z1)	A-G	0.5178	♦						
Busbar B (Z2)	B-C	0.8183				♦			
Busbar C (Z3)	C-A-G	1.6254							♦

#### 2) DC-side faults

For dc-side faults, it is assumed that the converter's ac-side CB at Station A (ac-side) fails to operate for the event. Both Station A and Station B block commutation upon the instigation of the fault with the remote converter station (Converter Station B) disconnecting from the ac network within four ac cycles (80 ms). Fault inception is at 0.5 s and occurs at the midpoint of the dc link.

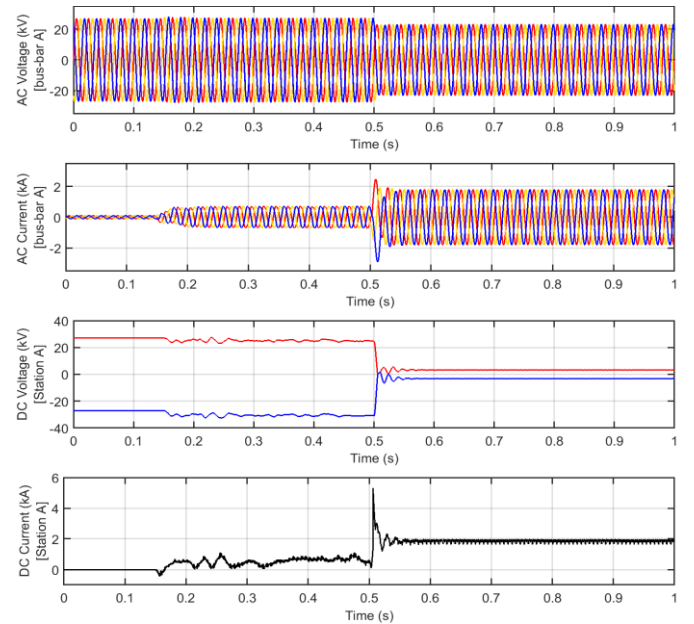


Fig. 7. Voltage and currents for ac and dc systems during pole-pole-ground fault at the midpoint of the dc link

Fig. 7 presents the ac-side voltage and current as measured at the distance relay in addition to the dc-side voltage and current measured at Station A. In the simulation, converter initialization occurs up to 0.15 s, then a pre-fault transfer of 30 MW from converter A to B is established.

The impedance locus diagrams in Fig. 8 represents the time-varying apparent impedances calculated by the distance protection relay during the event. Each subplot shows a different calculation of the impedance based on phase to phase and phase to ground measurements (i.e. A-G, B-G, C-G, A-B, B-C, C-A). These subplots also show the standard distance protection zones (i.e. Z1 - Z3). The "trails" (shown in green in Fig. 8) represent the "history" of the calculated impedance over time starting from the time of fault inception – for example for AG, one can see that the measured impedance has "entered" from the right and transited (via an approximately circular loci) to its "steady state" – this evolution of measured impedance is

due to the initial fault, any arcing, and the measurement algorithm (e.g. discrete Fourier transform etc.) within the relay stabilising during and after the initial transient/discontinuity in the measured voltages and currents.

The impedance loci during the fault reside significantly outside the standard ac protection zones of the distance protection relay. The loci do however evolve towards a sustained value of approximately  $(2.5 + j10.8) \Omega$  in the case of a PPG fault at the midpoint of the dc link.

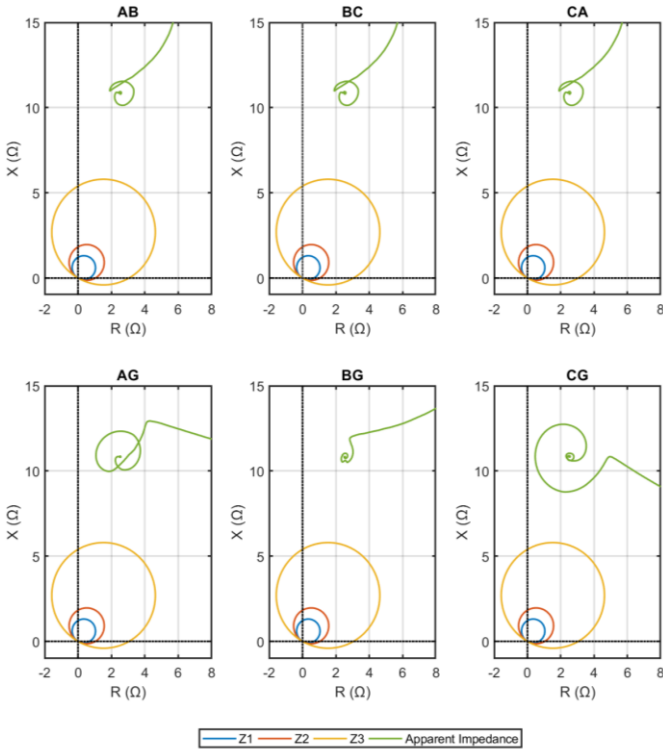


Fig. 8. R-X diagram for a PPG fault at the mid-point of the dc link

### 3) Proposed Protection Scheme

The highly inductive nature of the fault outlined in Fig. 8 creates a degree of confidence as to where the impedance loci of a dc-side PPG event settles. This settling point resides significantly outside of the resistive, load-serving, operational area of conventional distribution networks. The inductive nature of the fault is associated with significant inductance (i.e. converter filters and transformer) placed between the distance protection relay and the converter (for purposes outlined previously). This highly inductive characteristic could allow a distance protection relay to detect dc-side PPG/PP faults and trip quickly, using appropriate logic, should primary protection fail.

Conventional overcurrent protection is unlikely to provide reliable and fast backup disconnection as the current infeed to the fault is expected to be very low (again due to the significant inductance in the system). In the case of the simulated waveform for a PPG fault (Fig. 7), the fault current is only approximately 2.5 to 3 times greater than the load current.

Many modern digital distance protection relays include customizable zones whose characteristics can be manipulated as per network requirements. It is hypothesized that a fast-trip quadrilateral characteristic, shown as zone 4 (Z4) in Fig. 9,

could potentially provide sufficiently quick backup protection to an embedded MVDC link for dc-side PPG and PP faults.

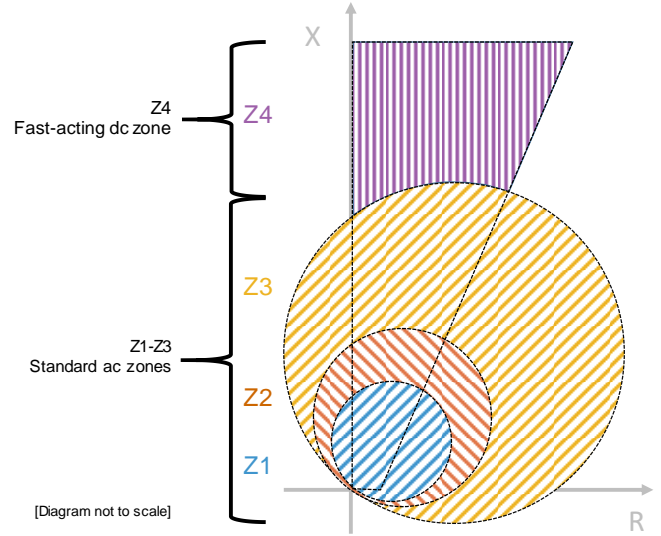


Fig. 9. R-X diagram showing conventional Z1-Z3 tripping zones and fast-acting Z4 quadrilateral area for detection of dc-side faults.

For a dc-side fault, it is important that the relay only trips for the area outside of the Z3 Mho characteristic but within the bounds of the Z4 quadrilateral (the area shaded with purple vertical lines - Fig. 9). Under conventional relay setting this would not be an issue as the time grading (i.e. tripping delay) increases with increasing zone number. However, with the solution proposed a fault detected in Z4 should react much quicker than both Z2 and Z3. A fault which resides in either Z2 or Z3 but also within the quadrilateral Z4 characteristic should trip with the time delays associated with Z2 and Z3 to ensure correct discrimination. The logic tripping expression for the fast-acting Z4 area ( $Z4_{TRIP}$ ) is therefore presented in equation (6). Note that since Z2 is a subset of Z3, a Z2 condition is not required in the tripping logic.

$$Z4_{TRIP} = \overline{Z3} \cdot Z4 \quad (6)$$

From the R-X plots outlined previously (Fig. 8), it is established that dc-side faults appear as symmetrical events to the distance relay for PPG faults. With the majority of ac faults on the distribution system being non-symmetrical events [40], it is proposed that all six Z4 comparators (i.e. AB, BC, CA, AG, BG, CG) must be active to permit the fast dc trip zone. The introduction of this additional check increases the certainty of a dc-side fault being detected rather than a remote ac fault. Confidence in such a backup scheme is vital due to the wider geographical area and larger number of customers liable to be disconnected should the Z4 scheme trip.

### D. Proposed Relay Setting Parameters

The maximum (primary referred) resistance and reactance of the system, as measured by the distance protection relay, for a fault on the dc link is calculated via equations (7) and (8). A 20% grading margin is added to the apparent impedance to calculate the reach of Z4 ( $Z4_{Reach}$ ) as in equation (9). This uncertainty allows for errors in line length and transducers but

also considers the nonlinearities associated with the ac to dc conversion system.

$$R_{Z4} = \Sigma[R_{L_{ac}}, R_{X_{fmr}}, R_F, R_{L_{dc}}] \quad (7)$$

$$X_{Z4} = \Sigma[X_{L_{ac}}, X_{X_{fmr}}, X_F] \quad (8)$$

$$Z4_{reach} = 1.2 \times \sqrt{R_{Z4}^2 + X_{Z4}^2} \quad (9)$$

$R_{Z4}$  and  $X_{Z4}$  are the maximum expected resistance and reactance associated with a dc-side fault.  $R_{L_{ac}}$  and  $X_{L_{ac}}$  represent the resistance and reactance of the ac conductors between the distance relay and the converter's ac-side CB.  $R_{L_{dc}}$  is the resistance of the dc conductors between converter Station A and Station B.  $R_F$  and  $X_F$  are the resistance and reactance of the ac filters at Station A. Finally,  $R_{X_{fmr}}$  and  $X_{X_{fmr}}$  are the resistance and reactance of the converter transformer. Note that the reactance of the dc line is not included in the calculation of  $X_{Z4}$  as there should be no ac component within a dc system.

The quadrilateral line setting angle for the proposed solution employs equation (1) but using the resistance and reactance values determined by equations (7) and (8). The angle of the Z4 reach can be computed as per equation (10).

$$\theta = \tan^{-1}\left(\frac{X_{Z4}}{R_{Z4}}\right) \quad (10)$$

For the network presented previously in Fig. 6, setting parameters are calculated as follows. Note that the transformer winding resistance has been assumed to be negligible for these case studies.

$$R_{Z4} = [(0.17 \times 5) + 0.3 + (0.17 \times 10)] = 2.9 \Omega$$

$$X_{Z4} = [(0.3 \times 5) + 6.28 + 3.14] = 10.9 \Omega$$

$$Z4_{reach} = 1.2 \times \sqrt{2.9^2 + 10.9^2} = 13.5 \Omega$$

$$\theta = \tan^{-1}\left(\frac{10.9}{2.9}\right) = 75.1^\circ$$

A summary of all the proposed relay setting parameters is outlined in Table IV. A tripping delay of 100 ms is proposed for Z4 to provide time for the converter's main protection scheme to detect and block (assumed maximum 20 ms) and isolate the fault through opening the ac-side breaker (assumed maximum 60 ms), with a discrimination margin of 20 ms (in effect, clearance would be after a further 80 ms or so due to circuit breaker action).

TABLE IV  
SUMMARY OF RELAY SETTING PARAMETERS

Parameter	Value
Mho line angle (°)	60.5
Quadrilateral line angle (°)	75.1
Z1 reach(Ω)	1.4
Z2 reach (Ω)	2.1
Z3 reach (Ω)	6.2
Z4 reach (Ω)	13.5
Z4 resistive reach (Ω)	2.9
Z2 delay (s)	0.300
Z3 delay (s)	1.100
Z4 delay (s)	0.100

E. DC Fault Simulation Studies

To validate the proposed relay setting configuration, a series of simulated PPG and PP events were applied at three locations on the dc line: at the terminals of converter A (0 km), the middle of the dc line (5 km) and at the terminals of converter B (10 km). Faults are applied at 0.5 s with a fault resistance of 0.1 Ω. The tripping times and associated detection zones for these dc-side faults are presented in Table V. Fig. 10 presents the associated R-X locus diagrams for a PPG fault at the midpoint of the dc link.

TABLE V  
RELAY LOG DC-SIDE FAULTS

Scenario		Time of Trip (s)	Tripping Zone			
Location	Fault Type		Z1	Z2	Z3	Z4
0 km	PPG	0.634				◆
5 km	PPG	0.637				◆
10 km	PPG	0.639				◆
0 km	PP	0.635				◆
5 km	PP	0.637				◆
10 km	PP	0.638				◆

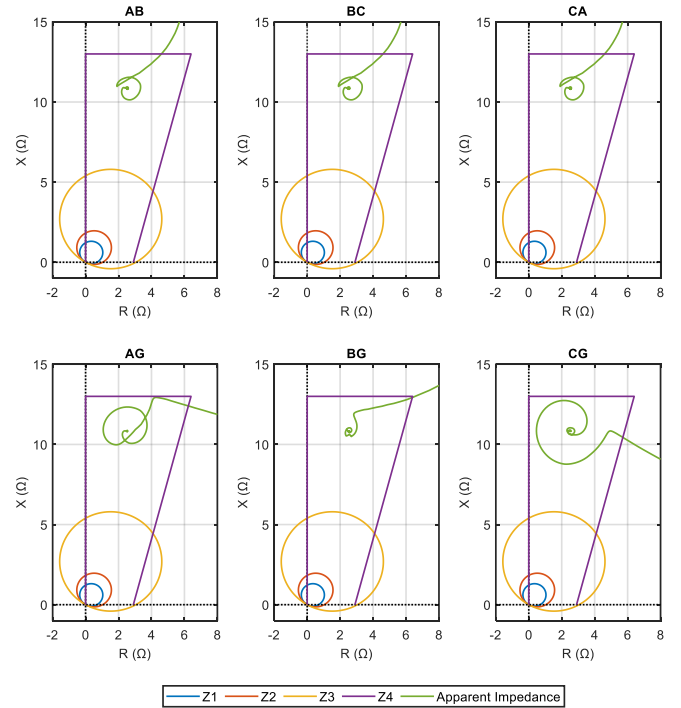


Fig. 10. R-X diagram for a PPG fault at dc link midpoint (5 km)

It is observed from Table V that the relay correctly identifies all PPG and PP faults with a maximum tripping time of 0.639 s, 0.139 s after fault inception in the case of the PPG fault at 10 km. This time incorporates the (user-configurable) 100 ms

Z4 delay outlined previously to allow for grading with the embedded MVDC link’s main converter protection scheme. Effectively, the relay detects the presence of the fault residing inside Z4 in 40 ms. The tripping time could potentially be shorter if desired, through specifying a shorter delay time.

The final impedance settling points are presented in Fig. 11 for PPG faults and in Fig. 12 for PP events. It is shown that the relay is sensitive to all PPG and PP faults along the MVDC link. These plots confirm that the measured ac-side impedance only increases horizontally along the resistive axis proportionally to the increasing distance between the measurement point and fault location for dc-side PPG and PP faults. Accordingly, for any resistive fault, the locus of the apparent impedance would be shifted to the right with the magnitude of shift being proportional to the fault resistance. There is no possibility of a resistive fault on the link encroaching on other zones, so to detect faults with significant fault resistance would simply require an extension of the Z4 boundaries.

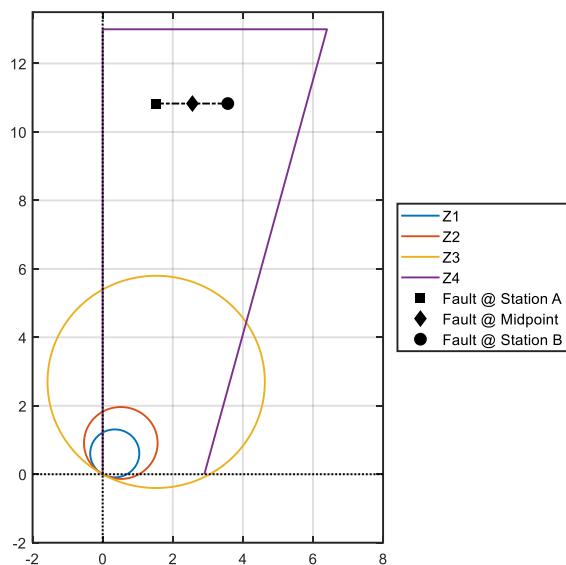


Fig. 11. Final apparent fault impedance settling points for PPG events at Station A, mid-point and Station B

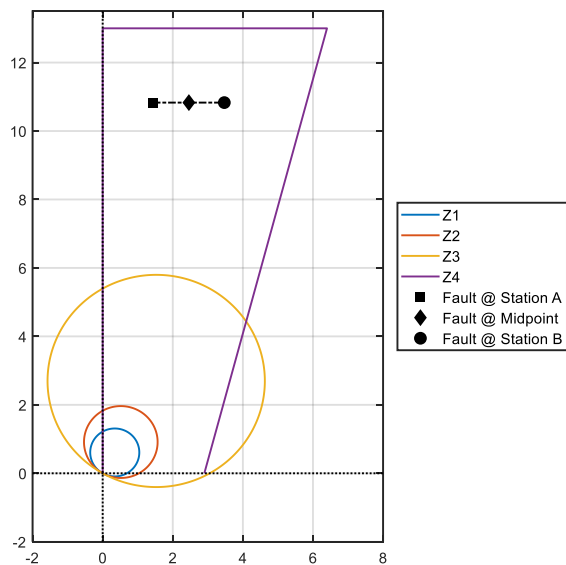


Fig. 12. Final apparent fault impedance settling points for PP events at Station A, mid-point and Station B

## V. DISCUSSION

Fast-acting distance protection offers an innovative method of backing up main converter protection for dc-side PPG and PP faults under a symmetrical monopole operating topology. Remote backup protection of an MVDC link is likely to give system operators higher confidence in deploying and protecting dc solutions within their networks. Fast backup protection may allow simpler and cheaper converter topologies to be deployed on distribution networks with the DSO remaining confident that assets are secured by both main protection at converter stations and through remote network protection schemes.

The back-up protection reported here would not be capable of detecting single pole to ground (PG) faults for a symmetrical monopole. When a PG fault occurs, the link effectively becomes a monopole with a metallic/earth return. The transfer current remains constant, as the system is still controlled, while the dc voltage tends towards twice the nominal pole to ground voltage (i.e. 54 kV in the case of a  $\pm 27$  kV system) [41]. From the ac-side, there is no change in the ac voltage or currents and therefore the distance relay would not be sensitive to such faults.

Depending upon network topology, it may be undesirable to permanently trip the CB at the ac distance relay (i.e. at busbar A in Fig. 6) in the event of a dc-side fault, however the approach outlined could also provide a transfer trip to a more local CB or potentially a load-interrupting switch (which may be usable as fault currents are relatively low) to minimize the isolation of healthy ac circuits in an interconnected network. If the fault is successfully cleared by more the local switchgear, the CB at busbar A could be automatically reclosed.

A quadrilateral impedance characteristic has been used for these studies, as it is easy to implement on standard commercially available relays. It may be preferable to define a more customized shape (i.e. via specific R-X co-ordinates to create a specific boundary) to improve performance. This could readily be achieved via modifications to distance protection relay configuration and setting software.

## VI. CONCLUSION

The role and effectiveness of existing network protection strategies must be validated for situations where dc interconnectors, with converter interfaces, are introduced within existing networks. This paper has presented the results of an investigation into the impacts on power system protection performance from the introduction of embedded MVDC links into distribution networks and has shown how distance protection can be modified to provide backup protection, should a fault on the dc link not be cleared by the main protection for that link.

Through simulations presented in this paper the impedance of dc-side PPG and PP faults, as measured remotely from the ac network, are determined. Using this characteristic, a fast-acting backup protection strategy has been established. The developed solution is demonstrated for faults across the length of the dc link with the protection performing as expected.

Studies have shown that existing ac distance protection relays have the flexibility and responsiveness to discriminate and react quickly to faults upon a dc link in a backup mode.

## APPENDIX

TABLE VI  
SUMMARY OF MODELLED ELECTRICAL PARAMETERS

Parameter	Value
Local & remote grid voltage (line-line)	33 kV
Local & remote X/R ratio	7
Local & remote grid fault level	500 MVA
A-B line length	5 km
B-C line length	5 km
MVDC line length	10 km
Conductor impedance	$(0.17 + j0.3) \Omega/\text{km}$
MVDC link voltage	$\pm 27 \text{ kV}$
MVDC link power rating	35 MW
DC link capacitor value ( $C_1, C_2$ )	1 mF [42]
Filter impedance ( $Z_{\text{Filter}}$ ) <sup>a</sup>	10 %
Transformer impedance ( $Z_{\text{xfmr}}$ ) <sup>a</sup>	20 %
Transformer vector group	Yd11
Transformer rated power	35 MVA
Transformer rated voltage	33:33 kV

<sup>a</sup>Impedances referred to 35 MVA base

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