

Fault Ride-Through Enhancement of PMSG Wind Turbines with DC Microgrids Using Resistive-Type SFCL

Doaa M. Yehia, Diao-Eldin A. Mansour, *Senior Member, IEEE*, and Weijia Yuan

Abstract— Integrating permanent magnet synchronous generator (PMSG) wind turbines with DC microgrids have attracted a great attention due to the inherent merits of DC systems. However, under fault conditions, the excessive energy during fault will be reflected on PMSG rotating parts causing over-speed, mechanical stresses and fast aging. Also, large fault currents will force generator-side converter to disconnect making it not complied with grid codes. Accordingly, this paper aims to apply superconducting fault current limiter (SFCL) to enhance the performance of PMSG wind turbines with DC microgrids under fault conditions. PSCAD/EMTDC software is used to build the system under study that includes PMSG, controlled AC/DC converter, SFCL and DC bus. The behavior of SFCL in enhancing the DC output of the converter is first studied. Then, the impact of using SFCL on the speed, torque, and output current of PMSG is investigated and discussed. Finally, the suitable current limiting resistance is adopted considering DC output as well as PMSG speed.

Index Terms—PMSG wind turbines, DC microgrids, superconducting fault current limiter, current limiting resistance.

I. INTRODUCTION

IN recent years, wind energy has been expanded extensively worldwide and became one of the fastest growing energy sources due to its cleanness and sustainability. This accelerated the development and integration of wind turbine generators with the grid. Wind energy is the second contributor to the increment in renewable energy generation after hydro energy. By 2040, wind energy is projected to account for about 23% of total renewable energy generation all over the world [1]. Coupled with this, the concept of microgrid has been proposed to incorporate different renewable energy sources in a single framework capable of operation either in isolated mode or in grid connected mode [2]. In this regard, DC microgrids have attracted a great attention due to the DC nature of most renewable energy sources and energy storage devices [3-5]. Even with wind energy, connection to a DC microgrid will save one DC-AC conversion stage and will eliminate the requirements of frequency control and reactive power control.

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(Corresponding author: Doaa M. Yehia.)

Doaa M. Yehia and Diao-Eldin A. Mansour are with the Department of Electrical Power and Machines Engineering, Faculty of Engineering, Tanta University, Tanta 31521, Egypt (e-mail: dmyehia@f-eng.tanta.edu.eg and mansour@f-eng.tanta.edu.eg).

Weijia Yuan is with the Department of Electronic and Electrical Engineering, University of Bath, Bath BA2 7AY, U.K. (e-mail: W.Yuan@bath.ac.uk).

There are different types of wind turbine generators. Among them, permanent magnet synchronous generator (PMSG) has gained popularity with large wind farms due to its brushless construction, high reliability, high efficiency, and so on [6]. With DC microgrids, the usage of PMSG becomes more feasible due to the saving of inverter stage as mentioned above. However, integration of PMSG with DC microgrids faces some technical problems under grid disturbances. The major problem of integrating PMSG with DC microgrids is fault response. In DC microgrids, DC faults cause a relatively large voltage drop with high levels of fault currents [7]. This results in a large contribution of fault current from PMSG with a negative impact on fault ride-through (FRT) capability and failure probability of power electronic switches.

Several studies have been carried out for performance assessment and FRT enhancement of PMSG under faults when interfaced with AC grids [8, 9]. Most of these studies focus on dissipation of excessive energy in the DC-link. On the other hand, superconducting fault current limiter (SFCL) was used in [10] to improve FRT capability of PMSG connected to AC grids through minimizing the mismatch between generator power and grid-side power and subsequent decreasing the overvoltage on the DC-link capacitor. In [11], it was proposed to store the excessive energy in the rotating parts of the generator. But, this results in mechanical stresses and faster aging due to over-speeding of the generator. Also, at high wind speeds, the over-speed relay can be triggered due to violation of its allowed range. Unlike AC grids, fault behavior of PMSG interfaced with DC microgrids is different. In this case, the DC bus itself becomes faulty and the excessive energy will be reflected directly on the rotating parts of the generator. In addition, the large fault currents will force generator-side converter to disconnect.

This paper proposes the usage of resistive-type SFCL for FRT enhancement of PMSG wind turbines with DC microgrids. Resistive-type SFCL is considered because any inductance will affect the operation during normal conditions, and also, will have no beneficial impact during fault conditions, where inertia of PMSG and control of rectifier limits the high rising rate of current. This is different than that studied in [12] and [13]. Regarding FRT enhancement of wind turbine generators, SFCL has been used effectively for this purpose with AC grids [14, 15]. Also, SFCL has been succeeded in reducing fault currents in DC systems [16]. In this study, PMSG and SFCL are built on PSCAD/EMTDC software, and then,

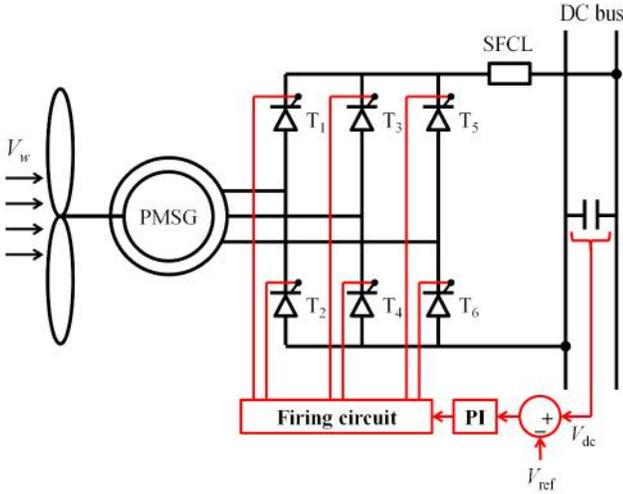


Fig. 1. Scheme of considered PMSG with a DC microgrid.

were interfaced with a DC bus through controlled AC-DC converter. First, the current limiting behavior of SFCL under DC fault is investigated. Then, the effect of using SFCL on PMSG performance is evaluated considering generator speed and torque as well as output 3-phase current. Finally, the performance is evaluated with different values of limiting resistances in order to adopt the suitable value.

II. SYSTEM MODELING AND DESCRIPTION

The wind energy system shown in Fig. 1 is composed of wind turbine, PMSG, and the rectifier stage that converts the variable frequency output of the generator to a controlled DC output at the desired value.

The extracted mechanical power from wind turbine can be described by the following equation:

$$P_w = \frac{1}{2} \rho A C_p(\lambda, \beta) v_w^3 \quad (1)$$

where ρ denotes to air density, A denotes to swept area, C_p is the power coefficient for a certain tip speed ratio and pitch angle β , and v_w is the wind speed. The developed torque is transferred to the rotor of the generator through the coupling shaft. The dynamic model of PMSG is described by the following equations in the dq reference frame [17]:

$$v_d = -i_d R_s - \omega \lambda_q - \frac{d\lambda_d}{dt} \quad (2)$$

$$v_q = -i_q R_s + \omega \lambda_d - \frac{d\lambda_q}{dt} \quad (3)$$

$$\lambda_d = L_d i_d + \lambda_m \quad (4)$$

$$\lambda_q = L_q i_q \quad (5)$$

where v_d and v_q are the dq components of stator voltage, R_s is stator winding resistance, ω is the rotational speed of PMSG, λ_d and λ_q are the dq components of stator flux, L_d and L_q are the dq components of stator winding inductance, and λ_m is the permanent magnet flux linkage. The PMSG was rated at 50 kVA and 400 V.

The rectifier is a 3-phase fully controlled Graetz bridge [18]. The output DC voltage is compared to the reference one,

and then, the error is fed to PI controller and firing circuit as shown in Fig. 1. The gating signals for each switch are generated based on ramp comparison control. The firing angle is then used to control the output DC voltage V_{dc} as follows:

$$V_{dc} = 1.35 V_L \cos \alpha \quad (1)$$

where V_L is the line input voltage.

The SFCL is represented by a current controlled switch paralleled with a nonlinear resistance [19]. The current controlled switch is normally closed to represent superconducting state. Once the current exceeds the critical value the switch changed to off state. The critical value was set at 150 A corresponding to 125% of DC rated current, whose value is 120 A. The considered tape for developing SFCL is YBCO coated conductor [20]. The nonlinear resistance is operated into two modes; flux flow mode and resistive mode. The flux flow resistivity (ρ_f) is expressed as follows:

$$\rho_f = \frac{E_c}{J_c} \frac{J}{J_c}^n \quad (2)$$

where E_c is the critical electric field intensity and n is a constant value. Usually n is higher than 20 [20]; thus, the current limiting resistance after quenching will be governed mainly by the stabilizer resistance. The recovery time was neglected, since overcurrent ratio is small due to buffering action of the controlled rectifier.

Usually DC microgrids include other sources in addition to wind turbine generators, such as photovoltaic (PV) panels and battery energy storage systems (BESS). But, PV panels have a self-limited fault current. Moreover, BESS is a static source and come into operation directly after fault clearance. So, the focus in this study will be on FRT enhancement of PMSG connected to the DC microgrid with neglecting the contribution of other sources. Accordingly, the DC microgrid model is simplified as a DC bus incorporating a capacitor of 4700 μF . A resistive load of 45 kW was connected on the DC bus.

III. RESULTS AND DISCUSSION

A. SFCL behavior

To assess the performance of PMSG with DC microgrids when integrating SFCL, results were obtained considering SFCL resistance of 3 Ω . This value lies within the common resistance range used for current limitation in DC systems [13]. Assuming that a solidly grounded DC fault occurs on the DC bus at $t = 2$ s, the DC current and power at the converter terminals are depicted in Fig. 2.

When the fault occurred, the generator was loaded by about 46 kW corresponding to a DC current of about 115 A. This loading condition is close to the full load of the generator that equals to 50 kVA. It is considered that protection devices isolate the fault after 0.1 s. Upon the fault occurrence, the DC current increased instantaneously to 215 A, and after about 13 ms it dropped gradually to zero. This is attributed to loosing controllability of the bridge rectifier after firing angles went to their limits. Also, the DC power dropped to zero during fault. On the other hand, the DC voltage depicted in Fig. 3 dropped to zero during fault and was kept at zero for about 0.4 s after

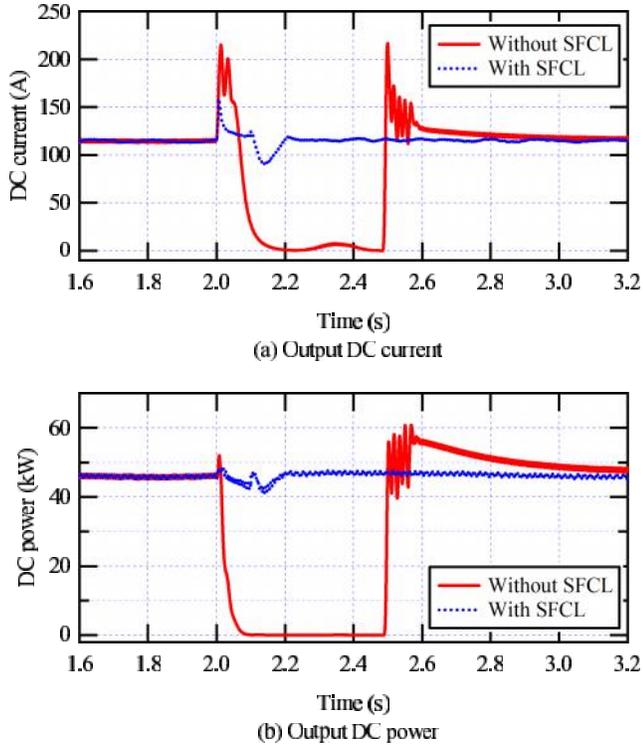


Fig. 2. Behavior of SFCL in enhancing the DC output of PMSG converter.

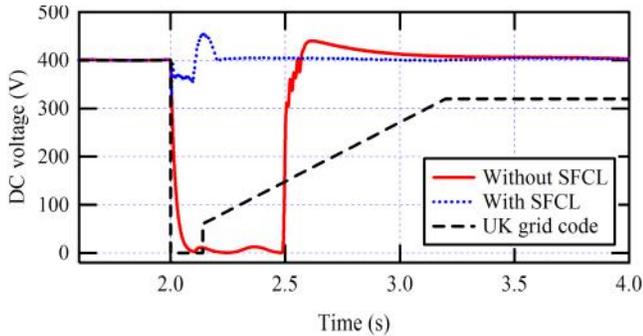


Fig. 3. Output voltage of PMSG converter in comparison of UK grid code.

the fault clearance at 2.1 s, until PMSG could rebuild up the voltage. This makes the system not complied with various grid codes [21]. Fig. 3 compares the obtained voltage profiles with UK grid code as an example.

When SFCL was applied, the DC current was limited to 159 A with a significant reduction in the DC voltage drop that became only about 20%. This action preserved the operation of PMSG in delivering power to the DC side as shown in Fig. 2(b), and made the system complied with the grid code. After fault clearance at 2.1 s, the remaining charges on the DC-link capacitor cause a slight overvoltage on the DC bus.

B. Impact of SFCL on PMSG performance

The PMSG performance is evaluated by investigating both electrical and mechanical parameters. Electrical parameters include output 3-phase current and electrical torque, while mechanical parameters include mechanical torque and generator speed.

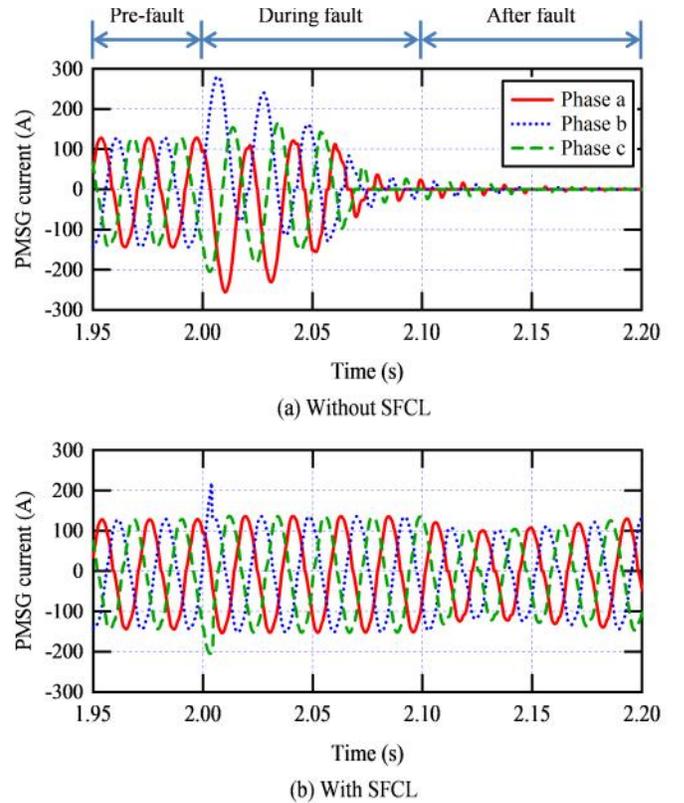


Fig. 4. Impact of SFCL on the output 3-phase current of PMSG.

Fig. 4 illustrates the output 3-phase current of PMSG before and after applying SFCL. Before applying SFCL, the current increased instantaneously on all phases with different values depending on the instant of fault for each phase [22]. The maximum fault current occurred on phase b with instantaneous value of 282 A, which represents 220% of the nominal peak current. It is noteworthy that the nominal loading condition in this study is close to the full load of PMSG. If loading is lower than this value, the overcurrent percentage during fault will be higher. After applying SFCL, the 3-phase current increased slightly during fault. Another important point is that using SFCL enabled PMSG to continue delivering power to the DC side during the fault and fault clearance.

Regarding electrical and mechanical torques, they are presented in Fig. 5. Without SFCL, the electrical torque falls to zero during fault and continue at zero after fault clearance for about 0.4 s. The mechanical torque falls smoothly due to the inertia of the generator. After 0.4 s, the electrical torque is rebuilt and the mechanical torque follows it, but with longer time constant. With SFCL, both torques are kept around their pre-fault values. This is reflected positively on PMSG speed shown in Fig. 6. Before using SFCL, the generator exhibited over-speeding and reached above 1.2 p.u., while SFCL usage mitigated over-speeding effectively. These results can be explained as follows. When using SFCL, the drop in DC voltage becomes small. So, the 3-phase rectifier continues under control making a small drop in DC power. Consequently, the difference between input mechanical power from the turbine and output electrical power decreases and the excessive energy in

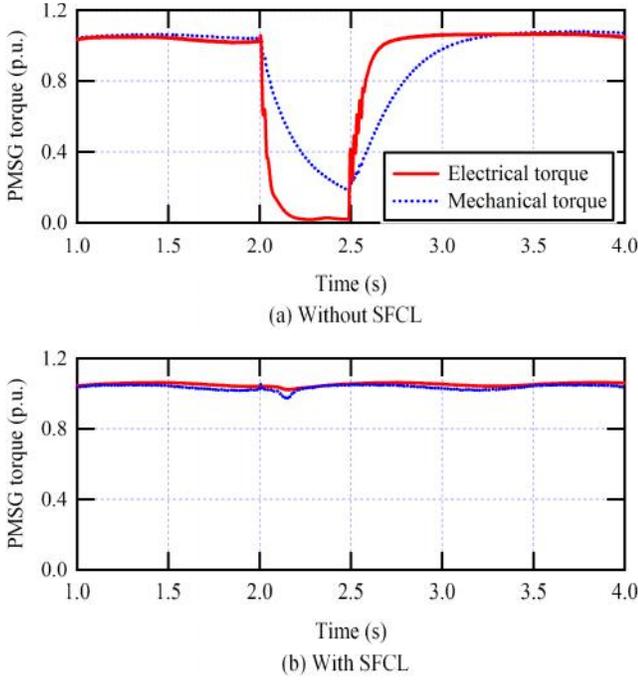


Fig. 5. Impact of SFCL on the mechanical and electrical torque of PMSG.

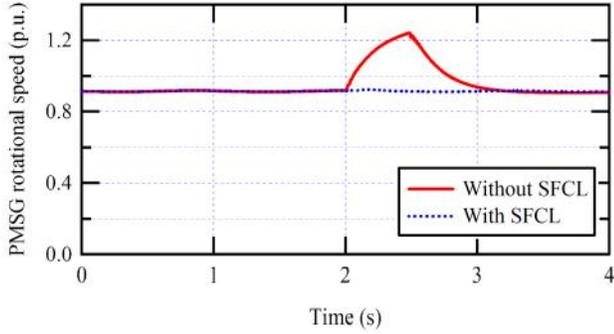


Fig. 6. Impact of SFCL on the PMSG rotational speed.

the rotating parts becomes limited. The power difference is a function of angular speed and inertia according to the following equation:

$$P_t - P_g = J_e \omega_m \frac{d\omega_m}{dt} + P_{\text{loss}} \quad (3)$$

where P_t and P_g are the turbine power and generator power, respectively; J_e is the inertia; ω_m is the rotational speed; and P_{loss} is the generator power loss. It is clear that the decrease in power difference will be followed by a decrease in overspeeding.

C. Effect of current limiting resistance

As explained above, SFCL keep the electrical output power close to its nominal value. But, this condition will not be applicable for different SFCL resistances. So, it is expected that there a certain range of SFCL resistance that can be successful in performance enhancement of PMSG under DC faults. It is aimed in this section to examine the effect of current limiting resistance and recommend the suitable range for the considered PMSG in the current study.

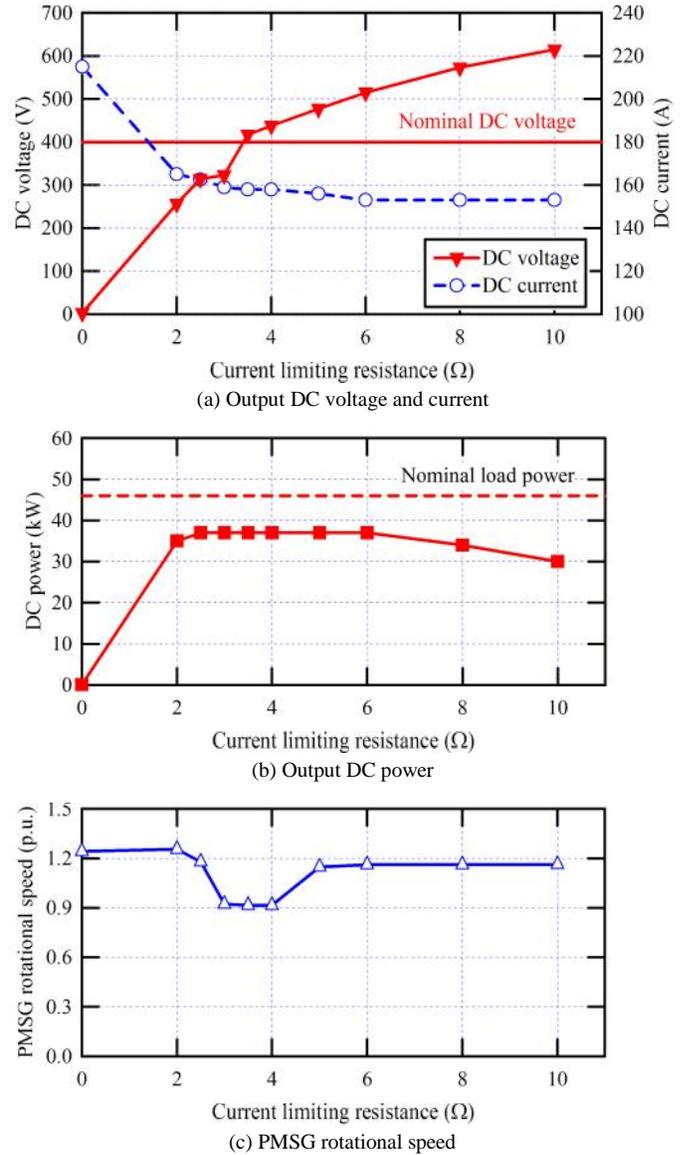


Fig. 7. Effect of current limiting resistance on PMSG performance with DC microgrids.

Fig. 7 depicts the results of output DC parameters and PMSG speed with changing SFCL resistance. For DC voltage and current in Fig. 7(a), adding SFCL with 2 Ω alleviated the voltage drop occurred without SFCL and increased the output DC voltage to 256 V. Further increase of SFCL resistance increased the output voltage until reached around its nominal value 400 V at SFCL resistance of 3.5 Ω . Additional increase of SFCL resistance above 3.5 Ω caused an overvoltage. This overvoltage continued until reached about 615 V at 10 Ω resistance. Regarding DC current, it was limited from 215 A without SFCL to 165 A at SFCL resistance of 2 Ω . The limitation in current continued, and at about 3.5 Ω limiting resistance, the current attained approximately constant value slightly above the critical current of SFCL.

This will be reflected on DC power and PMSG speed as shown in Fig. 7(b) and Fig. 7(c). The drop in DC power decreased with the increase in SFCL resistance, and above 6 Ω it increased again but with slower rate. Correspondingly, PMSG

speed decreased with increasing SFCL resistance and reached around 0.9 p.u. at 3 . Then it was kept around this value until limiting resistance of 4 . After that, PMSG was accelerated again and its speed reached about 1.16 p.u. for the remaining values of SFCL resistances. Based on these results, it can be claimed that the suitable range of SFCL resistance is 3~4 .

IV. CONCLUSION

In this study, SFCL was used for performance enhancement of PMSG wind turbines integrated with DC microgrids. The models of PMSG, SFCL, power converter and DC bus were built on PSCAD/EMTDC computer package. Then, detailed analysis was carried out to characterize SFCL behavior and its impact on PMSG performance. SFCL of limiting resistance 3 succeeded in limiting the DC current and decreasing the DC voltage drop making the system complied with various grid codes. This limiting action preserved the output DC power with a positive impact on PMSG developed torque and acceleration. With increasing SFCL resistance above 3.5 , adverse impact was observed on DC voltage and PMSG performance. The suitable SFCL resistance ranged between 3 and 4 .

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