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Novel Fault Location in MTDC Grids with Non-Homogeneous Transmission Lines Utilizing Distributed Current Sensing Technology

Dimitrios Tzelepis, Member, IEEE, Grzegorz Fusiek, Member, IEEE, Adam Dyško, Member, IEEE, Paweł Niewczas, Member, IEEE, Campbell Booth, and Xinzhou Dong, Fellow, IEEE

Abstract—This paper presents a new method for locating faults in multi-terminal direct current (MTDC) networks incorporating hybrid transmission media (HTMs), including segments of underground cables (UGCs) and overhead lines (OHLs). The proposed travelling wave (TW) type method uses continuous wavelet transform (CWT) applied to a series of line current measurements obtained from a network of distributed optical sensors. The technical feasibility of optically-based DC current measurement is evaluated through laboratory experiments using Fiber-Bragg Grating (FBG) sensors and other commercially available equipment. Simulation-based analysis has been used to assess the proposed technique under a variety of fault types and locations within an MTDC network. The proposed fault location scheme has been found to successfully identify the faulted segment of the transmission media as well as accurately estimating the fault position within the faulted segment. Systematic evaluation of the method is presented considering a wide range of fault resistances, mother wavelets, scaling factors and noisy inputs. Additionally, the principle of the proposed fault location scheme has been practically validated by applying a series of laboratory test sets.

Index Terms—Fault Location, Multi Terminal Direct Current, Travelling Waves, Wavelet Transform, Distributed Optical Sensing

I. INTRODUCTION

With recent rapid development of power electronics, high voltage direct current (HVDC) networks incorporating voltage source converters (VSCs) have become an attractive option for bulk power transfer from offshore wind farms [1], [2] and also for upgrading and interconnecting existing AC systems [3], [4].

When a permanent fault occurs in an HVDC transmission system, accurate estimation of its location is of major importance in order to accelerate restoration, minimize repair cost, and thus reduce the system down-time.

This paper deals with the challenges related to accurate fault location in HVDC networks with non-homogeneous transmission media including segments of UGCs and OHLs. The remainder of the paper is organized as follows: Section II presents a review of existing fault location techniques for HVDC networks. In Section III, the proposed fault location method is explained. The case studies and simulation results are presented in Section IV. The optical sensing technology together with the experimental setup are introduced in Section V. Finally, in Section VI conclusions are drawn.

II. FAULT LOCATION IN HVDC SYSTEMS

It has been demonstrated in many publications that travelling waves (TW) can be used to accurately estimate fault position on a transmission line. This estimation can be achieved using measurements either from a single end or from both ends of the faulted circuit. Single-ended methods require identification of two consecutive TW reflections measured at one terminal, while the two-ended methods use the first reflection only (captured and time-stamped at both line terminals). As the first reflection always provides the clearest signature, two-ended methods are considered more reliable [5]. Nevertheless, the selection between single-ended and two-ended methods is a trade-off between the cost, complexity and required reliability of the estimation [6].

Even though capable of providing high accuracy estimations, TW-based fault location comes with a number of challenges such as difficulty of wavefront detection, high dependency on sampling frequency, requirement for very accurate sampling synchronization (for two-ended methods), and uncertainties in estimation of propagation velocity in the transmission media.

A wavelet transform (WT) approach is utilized in [7] to locate the faults in star connected MTDC systems. The method uses continuous wavelet transform (CWT) applied to the DC line current waveforms, and is shown to be capable of completely eliminating the requirement for repeater stations at the network junctions. However, a high sampling frequency (2 MHz) and time synchronized measurements are required. Additionally, high impedance faults have not been investigated thoroughly.

Based on unsynchronized voltage and current measurements from the two terminals of the line, a mix of Bergeron time domain and TW-based fault location method is proposed in [8]. The method was found to be accurate with both metallic and high impedance faults (with impedances of up to 500 Ω).

In [9] two graph theory-based lemmas together with the basic principle of single-ended TW-based fault location is proposed to locate the faults in MTDC networks. However,
both publications [8], [9], indicate a need for high sampling frequency of 1 MHz.

A special category of fault location (which is the main focus of this paper) applies to networks which include hybrid feeders with segments of both UGC and OHL. It should be emphasized that in such networks, additional challenges for TW-based methods arise from the fact that the speed of electromagnetic wave propagation is not uniform, additional reflections are generated at the junction points, and there is an increased difficulty in identifying the faulted segment. For such networks in HVDC systems, a number of fault location approaches are presented in [5], [10].

The authors of [5] propose the application of two-ended TW-based fault location based on time-stamped measurements (voltages and capacitor currents) sampled at 2 MHz. Prior to the TW-based fault location, the faulted segment is found by solving a set of equations estimating distance to fault for each segment. The method is very accurate even with noisy inputs, however fault resistances up to 100 Ω were only taken into account. Additionally, the requirement of high sampling rate and synchronized measurements could be considered a barrier in practical applications.

An alternative approach is presented in [10] where a support vector machine (SVM) is used for faulted segment identification, while an analysis based on TWs estimates the location of the fault. The method is single-ended and requires both current and voltage measurements. However, the presented case studies consider fault resistances only up to 70 Ω, while the sampling frequency requirements are not clearly stated. Additionally, the proposed SVM-based learning algorithm can incorporate two classes of lines only, which means that the proposed algorithm can only be applied to networks with two different segments.

III. PROPOSED FAULT LOCATION SCHEME

The fault location scheme presented in this paper utilizes the TW principle applied to a series of measured waveforms obtained from current sensors distributed along the transmission line. The distributed optical measuring arrangement has been previously shown by the authors as being capable of enabling highly discriminative DC line protection. The details and rationale for utilizing distributed optical sensing can be found in [11]. It should be noted here that the key advantage of the measuring arrangement is that all sensing points are completely passive (i.e. require no power supply) and can be interrogated directly from a single piece of equipment at either end of the line, where the protective equipment is placed. In fault location applications, the immediate benefit of multiple distributed sensing is that the fault location can be successfully performed in transmission lines containing multiple segments, and is not limited to two segments as described in [10]. Additionally, when compared to [5] and several other methods, the proposed fault location approach requires neither high sampling frequency nor accurate time-stamping (i.e. GPS). As the scheme depends primarily on the utilization of TWs which can be observed in both voltage and current waveforms, the choice of specific signal is usually guided by the ease of measurement and cost. In this case, the current was considered more appropriate due to the availability of the distributed optical sensing arrangement used previously in the current-based protection described in [11]. However, in a different practical situation the use of voltage measurement could also provide similar fault location functionality.

The proposed fault location scheme consists of three main stages as depicted in Fig. 1. Those are explained in detail in the following subsections. To enhance clarity of the description an example illustration is presented in Fig. 2 which includes a hybrid circuit with 100 km OHL and 100 km UGC. A fault is shown to occur on the OHL at a distance of 70 km from T1 (where the measurements are collected).

A. Stage I: Faulted Segment Identification

In this stage the faulted segment (e.g. UGC or OHL) is identified. As seen in Fig. 1, prior to any processing the measurements are compensated for the time delay imposed by the optical fiber. Such time delay corresponds to the speed of light and the refractive index of SMF-28 fiber which can be found in [12]. The fault segment identification approach is illustrated in more detail in Fig. 4.

![Fig. 1. Fault location algorithm flow chart.](image1)

![Fig. 2. Bewley Lattice diagram incorporating OHL, UGC and distributed optical current sensors.](image2)
sensors. When a fault occurs between two sensors, the differential current derived from those sensors reaches much higher level than the current derived from any other adjacent pair. For the fault case shown in Fig. 2 differential current $I_{HF}$ is calculated for the two adjacent sensor pairs $S_A - S_B$ and $S_A - S_C$, and is illustrated in Fig. 3. The difference is almost one order of magnitude which allows for a reliable selection of the faulted section of the line using a simple instantaneous value comparison.

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The identification stage produces two outputs $S_{UP}$ and $S_{DN}$ which correspond to the two adjacent sensors, one upstream and one downstream with respect to the fault ($S_A$ and $S_B$ in the depicted case). To achieve this, the algorithm incrementally searches for the first sensor (index $r$) corresponding to the highest differential current (in this case $r = A$). This sensor becomes $S_{UP} = r$, and the adjacent sensor becomes $S_{DN} = r + 1$ (considering that highest differential current has been reached by measurements of sensors $r$ and $r + 1$).

The exact times of the waves are established by comparing the waveforms with a predefined threshold $W_{TH}$ (i.e. the time instant is recorded when the threshold is exceeded). For sensor $S_A$ those time instances correspond to $t_{1(SA)}$ and $t_{2(SA)}$ (as shown in Fig. 5), while for the sensor $S_B$ those times are depicted as $t_{1(SB)}$, $t_{2(SB)}$, $t_{3(SB)}$, $t_{4(SB)}$ and $t_{5(SB)}$.

C. Stage III: Fault location calculation

In the final stage of the algorithm, the actual fault location is calculated. Since the measurements from both ends of the faulted segment are available locally, two-ended fault location approach can be conveniently applied. It is worth reiterating that the utilized optical sensing scheme can interrogate all sensors from a single acquisition point, and thus synchronized measurements can be ensured without the need of GPS (i.e. the difference in measurement time arrival from individual sensors is known and can be easily calculated). Taking as a reference the left-hand side (i.e. where $S_{UP}$ is located) of the HTM the fault location can be estimated using equation (2).

$$D_F = \frac{L_{seg} - \Delta t(S_{UP} - S_{DN}) \cdot v_{prop}}{2}$$  (2)
where $D_F$ is the distance between $S_{UP}$ sensor and the fault (calculated using the measurements of both sensors $S_{UP}$ and $S_{DN}$), $L_{seg}$ is the total length of the faulted segment, $\Delta t_{(S_{UP}-S_{DN})}$ is the time difference of the initial TWs at sensing locations $S_{UP}$ and $S_{DN}$, and $v_{prop}$ is the propagation velocity of the faulted segment. For the studies presented in this paper the propagation velocity has been calculated according to the conductor geometry of each segment.

IV. Simulation Results

A. MTDC Study Network

A model of a five-terminal HVDC grid illustrated in Fig. 6 has been developed in Matlab/Simulink® for the purposes of evaluating the proposed fault location method. The network architecture has been adopted from the Twenties Project case study on DC grids [13]. There are five modular multi-level converters (MMCs) in the network operating at ±400 kV (in symmetric monopole configuration), current limiting inductors, and HTMs with OHLs and UGCs (both are represented by the distributed parameter line model). It should be noted that by adopting a meshed MTDC network (rather than point-to-point HVDC links), the proposed fault location scheme is further scrutinized under an extended variety of faults occurring on different types of hybrid feeders.

The line lengths of each HTM segment can be seen in Table II. The parameters used in modelling of the OHLs and UGCs can be found in Table III [14].

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>AC voltage [VAC,L-L]</td>
<td>400 kV</td>
</tr>
<tr>
<td>AC frequency [f_n]</td>
<td>50 Hz</td>
</tr>
<tr>
<td>AC short-circuit level [S_{sc}]</td>
<td>40 GVA</td>
</tr>
<tr>
<td>DC voltage [VDC]</td>
<td>800 kV</td>
</tr>
<tr>
<td>DC line external inductance [L_{DC}]</td>
<td>150 mH</td>
</tr>
<tr>
<td>MMC number of sub-modules per arm</td>
<td>400</td>
</tr>
<tr>
<td>MMC arm inductance [L_{DC}]</td>
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</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Lengths of OHLs and UGCs Included in MTDC Case Study Grid</th>
</tr>
</thead>
<tbody>
<tr>
<td>HTM-1</td>
</tr>
<tr>
<td>HTM-2</td>
</tr>
<tr>
<td>HTM-3</td>
</tr>
<tr>
<td>HTM-4</td>
</tr>
<tr>
<td>HTM-5</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Parameters of UGC and OHL</th>
</tr>
</thead>
<tbody>
<tr>
<td>Resistance [\Omega/km]</td>
</tr>
<tr>
<td>Inductance [mH/km]</td>
</tr>
<tr>
<td>Capacitance [\mu F/km]</td>
</tr>
<tr>
<td>Speed of propagation [km/s]</td>
</tr>
</tbody>
</table>

B. Case Studies and Methodology

Pole-to-pole faults (PPFs) and pole-to-ground faults (PGFs) have been simulated in all segments of the MTDC network, at varying distances and with various fault resistances ($R_f$) of up to 500 $\Omega$. The simulation of the MTDC system and DC-side faults has been carried out at 1 MHz (i.e. with 1 $\mu$s timestep), while the fault location algorithm has been executed at 135 kHz. Additionally, test cases have been expanded to include a wide range of mother wavelets $\Psi$, scaling factors $\alpha$ and the impact of noisy measurements.

Firstly, the signals have been sampled at 135 kHz (which corresponds to the resonant frequency of optical sensors) and
the CWT magnitudes have been normalized (as also shown in Fig. 5). The threshold $W_{TH}$ used for establishing the wave arrival time has been set to 0.25. As also discussed in [7] the value of the threshold is subject to the assumed safety margins, anticipated levels of noise in the measured signal and scale of CWT.

The most suitable mother wavelet is usually selected by trial-and-error studies. The two major criteria are the provision of sharp edge for wave detection and the requirement for processing resources. However the latter is not so crucial in fault location applications as they are more focused on accuracy. For HVDC fault location applications it has been found in the literature that mother wavelets with relatively good performance include the ‘Haar’ and ‘db1’ [5], [7], [10] wavelets. This will be also verified in the next subsections with the aid of simulations.

The values of fault location estimation error have been calculated according to equation (3).

$$\text{error} \ [\%] = \frac{D_F - A \cdot D_F}{L_{seg}} \cdot 100\% \ (3)$$

where $D_F$ is the calculated fault distance, $A \cdot D_F$ is the actual fault distance and $L_{seg}$ the total length of the faulted segment.

C. Fault Location Results

The results of faulted segment identification and fault location are included in Table IV for PPFs and Tables V and VI for resistive PGFs with $R_f = 100 \ \Omega$ and $R_f = 500 \ \Omega$ respectively. The presented results were obtained by utilizing ‘Haar’ mother wavelet with a scaling factor $\alpha = 2$, since (theoretically) results with increased accuracy are expected in lower scales. To facilitate easier evaluation of the large amount of presented results, a few common statistical indices, comprising minimum, maximum and average error values, are frequently reported throughout the paper.

The minimum, maximum and average errors observed for PPFs are 0%, 1.4817% and 0.3768% respectively while for PGFs ($R_f = 100 \ \Omega$) those errors were 0.0262%, 1.3389% and 0.3714% respectively. The aforementioned errors for PGFs with $R_f = 500 \ \Omega$ correspond to 0.000%, 1.4770% and 0.4170% which verify that the proposed scheme is accurate even for highly-resistive faults. By observing 4th and 5th column in Tables IV, V and VI it is evident that the faulted segment has been identified correctly in 100% of the cases for both types of faults. This has been achieved regardless the position of the fault (i.e. OHL or UGC) or the number of series-connected segments, which verifies the robustness of the proposed algorithm. What is also interesting to note is that the proposed fault location scheme achieves high accuracy even in the case of close-up faults (i.e. faults occurring close to the head or end of each line segment).

D. Effect of Mother Wavelet $\Psi$ and Scaling Factor $\alpha$

In order to investigate the effect of mother wavelet $\Psi$ and scaling factor $\alpha$, a section of UGC at HTM-3 has been tested. In particular, PPFs from 90 km to 120 km with steps of 250 meters have been generated. For this range of faults the minimum (min), maximum (max) and average (avg) values have been calculated for different mother wavelets $\Psi$ and scaling factors $\alpha$, as shown in Table VII. The scaling factor values $\alpha$ have been selected using a power-of-two series $\alpha = 2^N$ (with $N = 1, 2, \ldots$). This is a common practice adopted in WT [5], [7], [15], and it provides a common base for comparison between different methods but also for comparison between CWT and DWT (scaling factors in DWT can only assume values of the power of two).

Satisfactory results have been achieved for the majority of mother wavelets and scaling factors. However, an increase in fault location error can be observed when using the majority of mother wavelets with higher values of the scaling factor (e.g. $\alpha = 128$). The output of such study is that for the proposed fault location technique the best performance (considering minimum, maximum and average values of error and for all the scaling factors) shall be provided by mother wavelets ‘Haar’.


‘db1’, ‘db2’, ‘sym1’ and ‘sym2’. The best overall accuracy in terms of average error has been achieved for mother wavelet ‘coif2’ and scaling factor $\alpha = 4$. Predominantly, high performance is achieved to lower scales, as they correspond to higher frequency components and the accuracy is expected to be theoretically greater [5], [16].

**E. Impact of Noisy Measurements.**

In order to assess and further scrutinize the performance of the proposed fault location scheme, the studies presented in Section IV-D have been repeated considering noisy inputs. In particular, the current measurements have been subjected to artificial noise with increasing amplitude up to 28 dB Signal to Noise Ratio (SNR). Excessive noise may result at the transimpedance amplifier stage, particularly when spectral signals from Fiber Bragg Grating (FBG) sensors need to travel relatively long sections of optical fiber and require significant amplification. Due to space limitations only results for mother wavelet ‘Haar’ are presented in Table VIII. Compared to the noise-less signal (infinite SNR), the increase in noise level (lower dB values correspond to higher levels of noise) has inevitably a degrading effect on the fault location accuracy. It can be concluded that the proposed fault location scheme is relatively robust to the additive noise, systematic of the fiber section lengths and sampling rates (hence photodetector bandwidth) considered in this simulation. In terms of average error it has been found that the error rises by 0.0369% for the SNR dropping to 28 dB.

**F. Impact of Fault Current Limiters.**

To accelerate practical feasibility of MTDC grids various types of fault current limiters are often utilized, installed either on the DC or AC side of the system [17]–[19]. It should be highlighted though, that the proposed fault location scheme,
TABLE VII

<table>
<thead>
<tr>
<th>SNR (dB)</th>
<th>α</th>
<th>0</th>
<th>1</th>
<th>2</th>
<th>4</th>
<th>8</th>
<th>16</th>
<th>32</th>
<th>64</th>
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<tr>
<td></td>
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<td>max</td>
<td>avg</td>
<td>σ</td>
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<tr>
<td>00</td>
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<td>0.2056</td>
<td>0.0621</td>
<td>0.0000</td>
</tr>
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</table>

should be immune against any practical fault current limiter, installed either on the AC or DC side. This will be better explained with the aid of Fig. 7.

A transition point is a point on a line where there is a change in surge impedance. When an electromagnetic wave passes through a transition point, a part of it is reflected, and a part continues to travel in the same direction [20, 21]. As indicated in Fig. 7, the initial wave is termed ‘incident wave’, and the remaining two at the transition point are termed ‘reflected wave’ and ‘refracted wave’. Any fault current limiting or interruption device is expected to increase the impedance (i.e. \( Z_{\text{grfd}} \)) and consequently affect the amplitude of reflected and refracted waves. However, no impact is expected on the time of arrival of the TW which is determined by the line impedance \( Z_{\text{OHL}} \) and distance to fault. Considering this and the fact that the sensors of the proposed scheme are installed on the line side of any potential fault limiting element (see Fig. 7) the performance of the scheme should not be compromised.

It should be noted that typical terminating inductors of 150 mH are already included in all the simulation results presented in previous sections. In order to further validate the above reasoning, a fault occurring at 78.5 km on the UGC section of HTM-1 has been simulated with different fault current limiting and interruption technologies.

![Fig. 7. Explanation of travelling waves at a transition point.](image_url)

In particular, inductive current limiters (with different inductance values), DC-CBs, AC-CBs, and AC resistive fault current limiters (R-FCLs) have been considered in the studies. DC-CBs represent a hybrid design as introduced in [11], R-FCL have been modelled according to [22], while the AC-CBs are represented by mechanical disconnectors.

The normalized CWT magnitudes (for wave detection) of these cases are depicted in Fig. 8, and the corresponding fault location errors have been reported as \(-0.0858\%\) for all cases. The results in Fig. 8 have been generated by utilizing current measurements from sensors \( S_A \) and \( S_B \) as shown in Fig. 7. The results demonstrate that fault current limiters and breakers (either on DC or AC side) do not distort the time response of TWs at the specific point of measurement and hence the fault location accuracy is not affected.

G. Effect of Sampling Time and Small Increments of Fault Distance

In order to investigate the effect of sampling time, a small incremental change in fault distance has been applied to the
UGC section of HTM-3. The results for PPFs at positions of 99 km to 101 km with steps of 100 meters have been generated. For this range of faults the error is shown in Fig. 9. The presented results were obtained by utilizing 'Haar' mother wavelet with a scaling factor $\alpha = 2$.

![Fig. 9. Fault location error with respect to small distance increment.](image)

By moving fault position in short increments of 100 meters, an effect of randomly changing sampling instant is emulated. It has been established that the error can fluctuate between $-0.2489\%$ and $0.2489\%$. This result has been achieved at very moderate sampling rate of 135kHz (used in all simulations presented in the paper). To reduce the sampling-time-related error, the sampling rate would have to be increased. Similar effect can be expected from all TW-based methods.

V. OPTICAL SENSING TECHNOLOGY

A. Transition Joint Pits

Optical sensing technology for HVDC applications is a growing area of research and development. However, there are only a handful of field trials reported in open literature [23]–[25]. The purpose of such sensing schemes is to assist the implementation of protection, control, power quality and other power-system-related functions.

![Fig. 10. Typical representation of a transition joint pit.](image)

Optical sensing schemes (i.e. examples of optical current transformers) for HVDC applications are also reported in IEC-61689 [26] (Part 9: Standards for Digital Interface for Instrument Transformers). From the technical point of view, the connection between overhead lines and cables, is taking place at “transition joint pits” (see Fig. 10), and the actual conductor connection is established with ‘transition joints’ [27], [28]. Such pits are actual onshore installations with other protective, measuring and control components. Considering this, current optical sensors can be attached and installed around the transition joint and hence current measurements can be realized at transmission junctions.

B. Testing Methodology and Hardware Setup

In order to prove the principle of the proposed fault location scheme, the optical sensor system previously developed by the authors to enable distributed DC line monitoring was utilized in this study [11].

The optical voltage sensor is formed by attaching an FBG to a piezoelectric transducer and measure strain generated as a result of a voltage applied across the transducer. The strain exerted on the FBG produces a corresponding shift in its peak wavelength which can be calibrated in terms of voltage. An analogous function can be achieved by utilizing a magnetostrictive transducer that responds to magnetic field generated around a conductor experiencing a fault current.

![Fig. 11. Experimental setup schematic diagram.](image)

Due to the working range of the utilized data acquisition card, the generated voltages were scaled to remain within a range of $\pm 10$ V. The voltage traces representing the DC line currents were then applied to the optical sensors while the corresponding measurement data obtained from the optical interrogation system was recorded on a PC for further processing by the fault location system algorithm developed in Matlab/Simulink®.

A schematic diagram of the experimental setup employed for the practical validation of the proposed fault location scheme and its physical arrangement are shown in Fig. 11 and Fig. 12, respectively. The experimental setup depicted in Fig. 12 is applicable to any HTM regardless of the number of segments and shall be installed independently for each HTM. As a result, each fault location scheme operates independently and is not affected by the operation of any other distributed sensing networks. This facilitates high flexibility under various operating modes. For example, when an HTM is out of service, only the scheme corresponding to the specific HTM needs to be deactivated, permitting the remaining fault location schemes to continue operating. It should be noted that the HTM-3
was selected for demonstration as the most challenging case considering the number of segments and the length of lines.

Fig. 12. Laboratory experimental setup.

In the presented case, the HTM-3 section of the MTDC network (see Fig. 6) consisting of three segments and four optical sensors ($S_A, S_B, S_C, S_D$ in Fig. 12) were considered. Pre-simulated fault currents at the corresponding four sensing locations along HTM-3 were used to provide replica voltage waveforms generated directly from a multi-function data acquisition card (installed on the National Instruments® PXI unit). Prior to testing, the sensors were characterized and calibrated by applying a DC voltage across the piezoelectric transducers (in 1 V steps within the range of ±10 V). For all the corresponding voltages, the FBG peak wavelengths were recorded and the inverted function was used to calibrate wavelength shifts. The sampled data were stored on a PC for further analysis (e.g. signal processing, plotting, etc.)

The FBG peak wavelength shifts were monitored by a dedicated commercial FBG interrogation system (‘Sensors interrogator’ in Fig. 12) capable of acquiring the sensors spectra at 5 kHz. As such, the proposed fault location algorithm could only be demonstrated and practically validated at this relatively low sampling frequency. Nevertheless, the principle of operation and robustness of the proposed scheme has been fully validated (for all the three stages of the algorithm) even though with slightly lower accuracy of fault location. It should be noted, however, that the acquisition frequency limit of 5 kHz is strictly due to the FBG interrogator currently available for the experiments. Higher sampling rates can be achieved when alternative interrogators are employed, such as a solid state interrogator based on an Arrayed Waveguide Grating (AWG) previously developed by the authors [29], [30]. In such a case, the limiting factor for high speed operation would be the performance of the employed data acquisition and signal processing electronics, but scanning frequencies greater than 100 kHz can readily be achieved and the accuracy of the developed fault location prototype could be improved significantly.

C. Experimental Results

Time-domain waveforms recorded during the laboratory validation experiment are presented for fault scenario $F_2$ only. The summarized reposed to all the fault scenarios is presented in Table IX.

1) Measured response: The measured response from the sensors corresponding to fault scenario $F_2$ depicted in Fig. 11 is illustrated in Fig. 13. For the ease of comparison both simulation and experimental results are combined in the same figure.

Fig. 13. Simulated and experimental DC voltages corresponding to DC scaled fault currents for fault case $F_2$ depicted in Fig. 11: a) $S_3-A$, b) $S_3-B$, c) $S_3-C$, d) $S_3-D$.

The DC voltage traces shown above correspond to scaled-down replicas of the fault currents. It can be seen that all the dynamic features of the simulated currents are captured with some inevitable level of noise. However, it has been demonstrated in Section IV-E that the scheme is robust to noise originating from optical signal acquisition. It should be noted that due to the fault occurrence on the UGC there is a current reversal taking place between sensors $S_{3-B}$ and $S_{3-C}$. The shape of TWs in terms of frequency of reflections and waveform damping effect is a function of distance to fault and properties of the transmission media. The measurements $S_{3-B}$ and $S_{3-C}$ are the closest to the fault which occurs at UGC (cable segment), and therefore, the TWs appear less damped and with higher frequency of reflection, while the sensors $S_{3-A}$ and $S_{3-D}$ are at the far ends of the OHL lines which results in longer travelling times and higher damping (i.e. travelling wave fronts appear much less ‘sharp’). As it is difficult by visual inspection to assess the difference between the simulated and measured response recorded from the sensors, the values and their impact on the performance of the proposed scheme are better appreciated by investigating the results of Stage I, II and III of the algorithm:
2) Stage I - Faulted segment identification: For the experimental voltage traces illustrated in Fig. 13, the differential voltage \( V_{diff} \) has been derived (corresponding to \( I_{diff} \) as explained in Section III-A) individually for every pair of adjacent sensors and is depicted in Fig. 14. The differential voltage calculated for the pair of sensors (i.e. \( S_{3-B} \) and \( S_{3-C} \)) adjacent to the faulted segment reaches much higher values compared to those related to healthy segments (i.e. \( S_{3-A} \), \( S_{3-B} \) and \( S_{3-C}, S_{3-D} \)). Therefore Stage I (faulted segment identification) of the proposed fault location algorithm is also verified experimentally.

![Fig. 14. Differential voltages calculated from experimental voltage traces for fault scenario F2.](image)

3) Stage II - Wave detection & fault location calculation: The measurements from the sensors \( S_{3-B} \) and \( S_{3-C} \) have been utilized to perform CWT and calculate the arrival time of the TWs. The CWT for such measurements is depicted in Fig. 15. The time indices for fault location calculation correspond to \( t_1(S_{3-B}) = 0.760 \) ms and \( t_1(S_{3-C}) = 0.660 \) ms for the sensors \( S_{3-B} \) and \( S_{3-C} \) respectively.

![Fig. 15. CWT calculated from experimental voltage traces for sensors \( S_{3-B} \) and \( S_{3-C} \) for fault scenario F2.](image)

VI. CONCLUSIONS

A new fault location scheme suitable for hybrid networks with segments of overhead lines and underground cables has been proposed. The scheme relies on the measurements obtained from a network of distributed current optical sensors, and it uses travelling wave principle to estimate the fault position.

The proposed algorithm has been found to successfully identify the faulted segment of the line in all cases regardless the position of the fault (i.e. OHL or UGC) or the number of series-connected segments, which verify and demonstrate its robustness. The scheme has also been found to consistently maintaining high accuracy of the fault location estimation across a wide range of fault scenarios including both pole-to-pole and pole-to-ground faults with resistances up to 500 \( \Omega \). Assuming a sampling rate of 135 kHz, and using the mother wavelet ‘\textit{coif2}’ with scaling factor \( \alpha = 4 \), the maximum fault location error has been found to be 0.1677\% while the average error is 0.0784\%. Additionally the proposed scheme has been found to be robust against noisy inputs for a wide range of mother wavelets and scaling factors. Studies have included the impact of fault current limiting and interruption devices installed both on AC and DC side and the accuracy of the proposed scheme was found to be immune when such devices are utilized. A hardware prototype based on FBG optical sensors has been employed to conduct a series of laboratory tests which confirm the practical feasibility of the proposed scheme, while demonstrating the robustness for faulted segment identification. A few considerations regarding the installation of the sensors at the transition points of transmission lines and underground cables have also been reported.

REFERENCES


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