

# A VSM (Virtual Synchronous Machine) Converter Control Model Suitable for RMS Studies for Resolving System Operator / Owner Challenges

Andrew J Roscoe, Mengran Yu, Adam Dyśko,  
Campbell Booth  
Department of Electronic and Electrical Engineering  
University of Strathclyde  
Glasgow, UK

Richard Ierna, Jiebei Zhu  
National Grid  
Warwick, UK

Helge Urdal  
Urdal Power Solutions, UK

**Abstract**— In recent years, it has become clear that reaching the targeted levels of renewable power generation poses problems, not only for basic infrastructure and generation/load balancing, but also in terms of fundamental network stability. In Ireland, the contribution from converter-connected generation is already constrained to 50-55%, while recent studies of other networks suggest that any “penetration” of converters above 65% could lead to instability. The phenomena have been observed both in RMS and high-fidelity EMT simulations of converter-dominated power systems, and appears to be unavoidable when using the dq-axis current-source controllers within conventional grid-connected converters. The high control bandwidth (>50 Hz) of these converters also means that they cannot be effectively included within RMS type large-scale network models. The idea of “synthetic inertia” has been proposed in some publications as a mitigating solution but needs to be considered carefully, since if implemented incorrectly it has been shown to further destabilise the network at the critical small timescales and high frequencies. In this paper we present simple versions of a Virtual Synchronous Machine (VSM) model which is implemented and demonstrated in both transient and RMS based simulations. An important aspect of the VSM is that the controller’s bandwidth is low ( $\ll 50$  Hz). This means that it can be modelled with reasonable accuracy in RMS simulation with time steps of the order of 2ms. From a system operator perspective, large-scale RMS simulations of entire countries or regions containing hundreds of VSM generators can be carried out with reasonable accuracy.

**Keywords**- NSG (Non Synchronous Generation), Virtual Synchronous Machine (VSM), Converter Control, Penetration Level Limit, Power System Stability

## I. INTRODUCTION

Today all practical models for installed plant presented to the GB (Great Britain) SO (System Operator) and TO (Transmission Operator) for converter-based generation consist of a dq-axis current-injection (DQCI) mode converter which references the local system voltages and frequency through a Phase Lock Loop (PLL). For manufacturers and generators this technology offers significant advantages as it is well understood, allows all harvested energy to be exported on to the grid and provides good control of the current flow

which helps with the management of DC rail voltage and current, as well as protection of the various hardware components.

However, with ever growing penetrations of DQCI converters, for system operators there are various significant areas of concern [1, 2], most notably:

- Increased RoCoF (Rate of Change of Frequency)
- Loss of synchronising torque/power and reference voltage
- Possibility of high frequency instability and controller interaction [3]
- Difficulties associated with modelling the electricity systems dynamic behaviour
- Reduced and possibly delayed fault in feed
- Possibility of voltage instability during or post fault e.g. collapse, blocking or over voltage post fault
- Potential for sub-synchronous oscillations and interaction with conventional machines
- Potentially increased sensitivity to load imbalance and harmonics

Typically for penetration levels below 50% [1] the remaining conventional synchronous plant provide the appropriate response, mitigating against these effects and allowing normal system operation and modelling. However it is anticipated at some point between 50 and 100% (various estimates exist [4, 5]) one or more of these effects will adversely affect operation and / or modelling.

The first adverse effects have been observed on the GB system and relate to RoCoF. As synchronous generation is displaced with NSG (Non-Synchronous Generation) the overall system inertia drops and increased Rates of Change of Frequency are observed, in response to system events. This is of particular concern as many embedded generators use RoCoF based protection to identify islanding and initiate disconnection, consequently increased RoCoF had the potential to instigate cascade tripping. This phenomenon is widely understood and can (or has generally been) mitigated through modification of relay settings and potentially through

extremely fast load-side response. Another proposal is to add (or retrofit) “synthetic inertia” to DQCI converters, which has been termed SEBIR (Swing Equation Based Inertial Response) by the authors in [6]. Effectively the converter controller attempts to measure frequency, and provide a power modulation proportional to RoCoF and a particular chosen value of synthetic inertia constant  $H$  [7-9]. This technique provides what could best be described as a “fast frequency response”, but it does not have the same nature as inertia naturally provided by the synchronous generator (SG). Understanding of this critical difference can be gained by studying the Network Frequency Perturbation (NFP) plots described in [4]. In fact, the authors found in [6] that adding such control loops destabilized the system even more. In addition, it was shown that DQCI (and DQCI with SEBIR) have critical control loop frequencies and oscillatory modes in excess of 50 Hz (i.e. significantly greater than 5 Hz). Instabilities have also been observed in reality in various locations/installations, although [3] is perhaps the only/best example existing in the public domain. Also, some of the lower-frequency modes have been seen by National Grid in their RMS system models [1], and the results have been corroborated with high-fidelity models using time steps of approximately 50 $\mu$ s [4, 5].

It is known that the problem could be avoided by adding synchronous compensators (SC), but there may be questions over whether the use of SCs is an economic long-term solution. However, it has been implemented in conjunction with some HVDC projects, e.g. for Skagerak. An installation was made in 1975 due to a weak AC connection in Norway, and another more recently added in Denmark due to loss of system strength, where conventional generation has been retired and wind output at times exceeds 100% of demand in that part of the much larger Continental European System since 2008.

Although, with the exception of effects on RoCoF, the “synthetic inertia” results using SEBIR were not promising [6]. Much more positive results were obtained by using a converter control technique the authors refer to as VSM0H (Virtual Synchronous Machine Zero Inertia). This converter controller resolves many of the pertinent issues, particularly those relating to the loss of synchronising torque/power, voltage reference, and the associated wideband-frequency instabilities [10, 11]. VSM0H has been shown to potentially allow NSG penetration to achieve 100% both in simulation studies [11] and practical systems [12].

As the converters with VSM0H type controller do not provide an inertial response, further strategies are required to manage the initial value of RoCoF which occurs in the first few cycles following a large generation/load imbalance. This apparent lack of inertia also has to be resolved for the benefit of the remaining synchronous machines connected to the system. However, in terms of frequency excursion containment, the VSM0H strategy has fast-acting frequency droop slope, so active power dispatch is quickly adjusted by VSM0H as the frequency deviation from the set-point increases. In this respect, although it is “inertia-less”, it provides a very good “fast frequency response” and high degree of damping to the existing synchronous machine rotors.

Use of a converter which is controlled by an algorithm which mimics the dynamic behaviour of a synchronous machine, i.e. a VSM (Virtual Synchronous Machine) [13-17],

is expected to provide many of the benefits of the VSM0H algorithm, plus the ability to deliver “true” inertial support, via choice of an appropriate per-unit inertia constant  $H$ . The purpose of this paper is to present potential architectures for such VSM controllers, which can be used in real converters, and also modelled accurately, using both high fidelity EMT simulation as well as within large RMS type simulation programs.

Two models of the VSM are presented, and the results compared under similar system conditions. The first model is developed in Simulink, using multi-rate EMT simulation (250  $\mu$ s for the controller frame time; 4 kHz switching, and 50  $\mu$ s for the average-value power-system model). The second model is an RMS model created for in PowerFactory, and suitable for application with (for example) the National Grid transmission system model. The high-fidelity model is useful when it comes to understanding aspects of power quality and high-frequency device-device interactions, but is also used to corroborate with the RMS model. The RMS model is useful for analysing the large systems. Whilst it is conceivable that improvements in computing technology would allow EMT studies to be performed in detail on a complete power system in the future, it is unlikely that a system operator could ascertain the values of the many thousands of device parameters and software behaviour for the many distributed generators and converters. Considering these limitations, it is in practice impossible to model the entire system dynamics at bandwidths  $>5$  Hz.

VSM does in many respects behave in a similar manner to a SG (Synchronous Generator) but can potentially deliver some characteristics impossible to achieve in conventional rotating machines due to physical limits. For example, there is nothing to be gained from replicating some of the undesirable characteristics that might otherwise be avoided. VSM may, therefore, outperform SG in some areas, but as converters are typically subject to stricter design limits such as fault current, harmonics, load imbalance, etc., they may be outperformed by SG in other areas.

Despite these differences, the authors believe it is possible to achieve significant improvements in all 8 areas of concern listed in the introduction, by adopting VSM for at least a proportion of the converters in a power network.

## II. VSM0H CONVERTORS

Whilst VSM0H technology is not the key solution proposed in this paper, as previously stated, it provides essential understanding of the problems being encountered with DQCI, and is therefore, briefly discussed here. It is beyond the scope of this paper to provide a detailed description of the VSM0H technology. However, the key differences between VSM0H, DQCI, and VSM are explained in this section.

A high-level overview of the VSM0H controller concept is shown in Figure 1. VSM0H unlike DQCI (which behaves like a current source), is a true VSC (Voltage Source Converter). Furthermore it does not use a PLL (Phase Lock Loop). Instead it has a “free-running” oscillator which is used to produce a balanced 3-phase AC voltage output waveform. The magnitude and frequency of the voltage waveform are controlled by two parallel response loops which respond to the active and reactive power outputs of the converter. The loops are not conventional “control” loops in that there is no choice of  $k_p$  or  $k_i$ . Instead the loops are simply responses to

the measured powers, based on frequency and voltage droop slopes, set-points of frequency and voltage, and set-points of active and reactive power. If necessary any of these could be changed in real time.

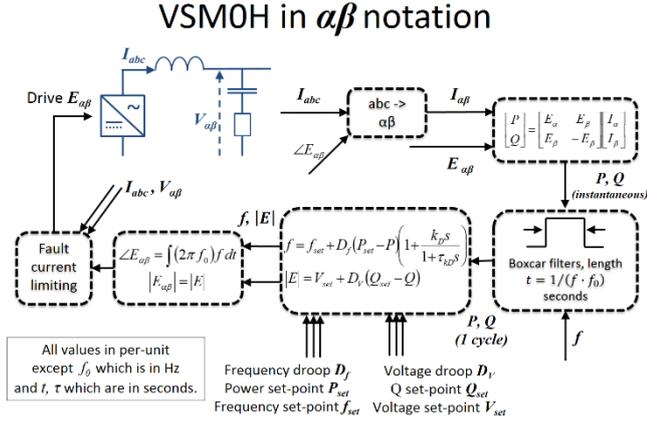


Figure 1 : High-level control diagram for VSM0H converter controller

The measurements of active and reactive power are filtered using boxcar filters, limiting the system dynamics to have a 3 dB bandwidth of 22 Hz (which is less than 50 Hz). In particular, the filtering places zeros at every multiple of the system fundamental frequency. As a result the voltage source does not react at all to unbalance or harmonics. Even in the presence of harmonics and unbalance, the resulting feedback signals are unaffected and remain clean and true representations of the measured quantities. While no inertia is emulated, the device performs as an “infinite-inertia” device from the perspective of harmonics and unbalance, and it provides a robust, predictable, fast-acting droop-slope response to (for example) under-frequency and/or under-voltage events.

Because no inertia is emulated, the device makes no attempt to output increased power when frequency first begins to fall - i.e. when there is a finite RoCoF but not yet a finite frequency deviation. However, as soon as the proportional frequency deviation becomes significant, the power output can increase dramatically if the droop-slope is configured to an appropriate value. The fact that no rotor mass is simulated means that there is no damped rotor oscillation. The transfer function is 1<sup>st</sup>-order and so includes damping but no tendency to resonate. In fact, this means that VSM0H can be used to increase the natural damping within SG and VSM-dominated networks.

As an example of how VSM0H works consider a system only comprising of VSM0H converters, which experiences an instantaneous 5% load increase. Because all the converters have frequency droop the system frequency will drop accordingly, however unlike a conventional power system this change will be extremely quick with the majority taking place in one AC cycle. The system is stable, and there is enough “synchronizing torque” for all VSM0H converters to remain synchronized. Also, power quality is controlled as all VSM0H-controlled converters mitigate voltage unbalance and harmonics in proportion to their ratings (which define filter impedances).

### III. VSM CONVERTOR MODELS

The output stage of a VSM converter is very similar to that of a VSM0H. It also is essentially a “true” VSC with the waveform being derived from a model of a rotor with

associated mass and damping. The electrical angle across the per-unit filter impedance  $X$  can be written as  $\delta$  and considered to be equivalent to a rotor angle. Using this analogy, the VSM0H response contains only “spring” (power output is proportional to  $\delta$ ) and “damper” (power output proportional to  $d\delta/dt$ ) terms. The VSM response adds an extra “mass” term - via a finite value of inertia constant  $H$ . This term adds a response proportional to  $d^2\delta/dt^2$ . This changes the response of the device from a purely damped response, to one with a 2<sup>nd</sup>-order transfer function with an associated natural resonant frequency determined by:

$$f_n = \frac{1}{2\pi} \sqrt{\frac{2\pi f_0}{2HX}} \quad (1)$$

A simplified high-level view of the control architecture is shown in Figure 2.

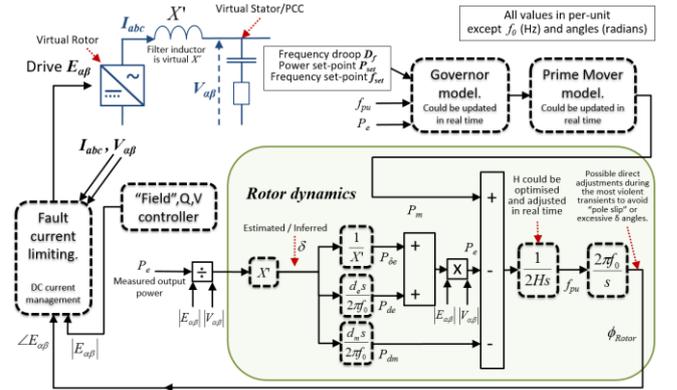


Figure 2 : High-level control diagram for a “true” VSM converter controller

A general statement can be made that any “true” VSM which accurately represents inertia must contain such a tendency to resonate, since it must by definition, have all three of the 2<sup>nd</sup>-order transfer response terms. The resonance in the VSM rotor is the exact analogy of rotor oscillation in real machines, and is generally in the 2-5 Hz region for normal values of  $H$  (2-10 s) and  $X$  ( $\approx 0.1$  pu). Some literature considers damping to be a mechanical or droop-related effect, but in reality any real machine with significant mechanical damping (i.e. friction or viscous windage) would be rejected on efficiency grounds. By far the dominant damping effect in a real machine (by a factor of  $>1000$ ), is the electrical damping. In a real machine the amount of damping is limited by physical constraints on the damper windings, and also on electrical losses associated with them. These operate as an induction machine to provide a torque proportional to slip (power proportional to  $d\delta/dt$ ), with the associated inefficiency and reactive power losses inherent in an induced-rotor-current machine. Real SGs have damping ratios  $\zeta$  of the order of 0.2 and hence the decaying post-transient rotor oscillations observed after system events and changes in operating point.

In a VSM, therefore, electrical damping should really be dominant, as a function of the rotor slip angle derived from the measured active power. But also, in a VSM there are no efficiency or mechanical design constraints on the damping ratio  $\zeta$  and the resulting damping term magnitude. Essentially  $\zeta$  is a freely variable parameter, settable in software. It is therefore possible to choose  $\zeta=1$  which will lead to critical damping of the rotor, and this would be a highly desirable network stabilisation tool and potentially omits the need for complex PSS (Power System Stabilisers) as found on SG. However, damping the rotor resonance right down too much

might detract from some of the beneficial inertial response. Further work is required to investigate this.

Therefore, the whole VSM response replicates true VSM performance: it has a bandwidth  $\ll 50$  Hz, configurable droop settings via a virtual governor, and a damped rotor resonance which provides inertia. The rotor inertia, and the fact that bandwidth is  $\ll 50$  Hz with a 2<sup>nd</sup>-order transfer function response, means that the voltage source “induced” by the rotor filters out unbalance (which appears at  $2f \approx 100$  Hz and largely does not react to it) and harmonics which appear at higher frequencies. Like the VSM0H, the true VSM produces a robust balanced 3-phase voltage set which mitigates voltage power quality problems in a manner proportionate to its rating and filter reactance  $X$ .

The fact that virtual-rotor resonance occurs at frequencies  $\leq 5$  Hz is important, because it means the control bandwidth is limited to  $\leq 5$  Hz. This 5 Hz figure was chosen as it is the current GB Grid Code limit for SG [2] and exists largely to protect synchronous plant from exciting torsional shaft oscillations.

Some care needs to be exercised when referring to literature concerning VSM. In the same way that the term “synthetic inertia” is ambiguous in its detail, some “VSM” implementations actually have inner dq-axis current control loops (e.g. [18]) and may not provide many of the benefits of VSM listed in this paper.

During close-in faults, action needs to be taken to avoid converter destruction, as it is operated as a voltage source. This intervention is described in [19] and has the advantage that it can service unbalanced, resistive and non-linear fault currents, rather than the “balanced reactive current” prescribed by existing grid codes. The fault current limiting does not affect the behaviour of the underlying VSM model, which has bandwidth  $< 5$  Hz. Further work is needed to provide practical limits of NPS and harmonics performance to preserve enough of the converter’s capability in order to provide the prime purpose of power conversion.

Modification’s such as the inclusion of dynamic braking, which on synchronous machines involves the addition of expensive plant such as resistor banks, are simply implemented in software. This reduces the post fault power oscillations, increasing system stability, minimizing increases to the converter rating requirements (see Figure 9 and Figure 10), and allows the converter to survive distribution fault durations in excess of 500 ms (see Figure 10).

It should also be noted that any renewable energy source connected to a VSM would have to be capable of supplying the necessary changes in power and would require an increased converter rating. In practice, due to lack of control of the common energy sources such as wind and solar, it is likely that some storage would be necessary, or for a generation source to operate at reduced power output in order to provide headroom for any additional energy demanded by the converter. Both of these solutions could have significant commercial implications, although the amount of storage required is relatively small.

The broader system based advantages with early widespread application of VSM as well as the additional equipment implications are discussed further in section VI and also in [20].

#### IV. IMPLEMENTATION OF VSM MODELS

Many aspects of the VSM model implementation in MATLAB and PowerFactory are the same. Both models consist of 6 major subsystems: power droop governor, reactive power droop voltage controller, rotor inertia emulation, dynamic braking, output current limiter and waveform generator. The first 4 of these subsystems are implemented identically in both models and described in this section. The current limiter and waveform generators differ and are described individually in the following sections.

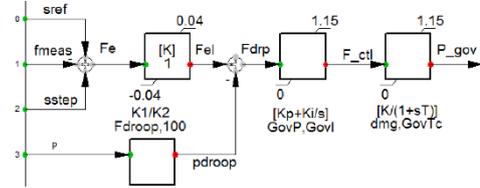


Figure 3. Power droop governor model for the VSM

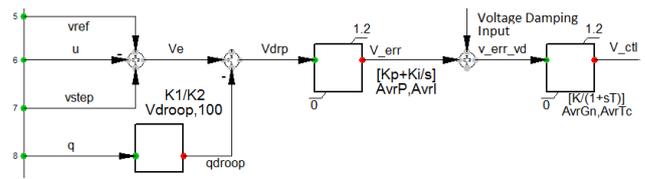


Figure 4. Voltage regulator model for the VSM

The power droop governor is shown in Figure 3 and uses a PI (Proportional and Integral) controller and low pass filter (LPF) with a gain which represents the gain and fuel/control system delay of a mechanical governor. The MATLAB model is essentially the same but includes an optional  $1/(1+s.Td)$  term in the path of *pdroop* to improve the response during large load steps.

The voltage controller is configured as shown in

Figure 4 with similar design as the governor controller. Reactive power output is measured and multiplied by the voltage droop gain and then subtracted along with the feedback voltage (taken after the output filter) from the voltage reference. The resulting error signal is processed by a PI controller and mixed with an optional power system stabilising signal. The resulting summed signal is passed through a LPF and gain to simulate the gain and field time constant of a SG. This signal is passed to the current limiter which normally doesn’t change it and then to the converter output oscillator to set the output voltage on the converter side of the filter reactance.

The rotor dynamics and inertia simulation are shown in Figure 2 and Figure 5 consisting of a  $1/2Hs$  element used to simulate the inertia of the rotor. The input to the block is derived from the output of the governor, which under steady state conditions is largely balanced by a power feedback signal  $P$ . This measure’s the electrical power observed at the converter output. If  $P$  was the only feedback signal, disturbances would result in critically damped power oscillations.

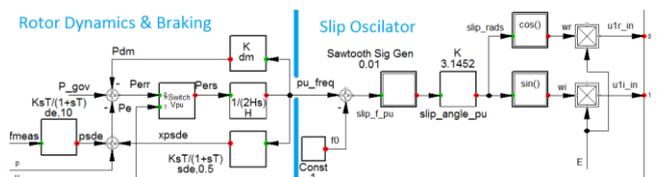


Figure 5. Emulation of rotor dynamics

Various damping signals were therefore added and these can be used individually or in combination. The  $d_m$  term is the equivalent of mechanical damping in SGs but unlike the SG there are no real losses incurred from increasing it. However, doing so adversely affects the steady state gain. The  $d_e$  term has a similar affect but has the benefit of leaving the steady state gain intact. It is derived from the speed feedback signal. The  $sde$  term performs the same function as  $d_e$  but in practice would not experience measurement noise and is the main damping signal applied in the results. Finally, the model provides a  $vd_e$  option which modifies the voltage in similar fashion to a PSS on an SG (not shown). This is derived in the same way as the  $psde$  signal but is added to  $V\_err$  in the voltage controller (see

Figure 4). It also works very well and unlike PSS on SG is applied at a point where no phase compensation was required. However, unlike the other options it affects the terminal voltage and was switched off for the results presented here.

Dynamic braking is applied in both models which consists of a switch placed before the  $1/2Hs$  term. This opens for converter terminal voltages of less than 0.85 pu. This prevents the integrator changing the frequency of the converter and advancing the operating angle when close up faults are applied. The 0.85 pu was chosen as the GB grid code allows generators to trip if this is exceeded for more than 1.5 seconds [2].

#### A. Implementation of VSM within MATLAB Simulink

The MATLAB EMT model runs at 50  $\mu s$  (250 $\mu s$  for the controller frame time) where 3-phase AC waveforms are generated by the power system and the various components within it. The model is therefore capable of simulating the effects of imbalance, harmonics and better represents the hardware than the equivalent RMS model.

An average value model for a VSC converter with controllable sources is utilised to represent the converter interface, where the switching effect is replaced by its average effect over the PWM switching cycle. The switching inter-harmonics generated by the switching process are neglected. The final drive voltages  $E_{abc}$  computed from the VSM controller are sent to the controllable voltage sources for 3-phase waveform generation at the output. For the operation of VSC convertor and its control system, the following delays (at its corresponding sampling time step) are simulated in the MATLAB model: an 0.5 frame delay for the simulated averaging of multiple ADC (Analogue to Digital Converter) reads across the whole of the preceding frame, a 1 frame delay in due to real-time computation, and a 0.5 frame delay due to the use of PWM (Pulse Width Modulation) switching pulses. For the VSM algorithm proposed in this paper, it is crucial to ensure the correct amount of phase advances are applied on the derived rotor angle to compensate for the delays, i.e. 1 frame phase advance for feedback signals in the controller and 2 frames for calculation of the final drive voltages.

Single-cycle boxcar filters are implemented to the measured rotor angle (from the actual electrical power output of the VSM) and after rotor angle derivation for better signal quality. The current limiting block described in [19] is applied in the MATLAB model to avoid damage to the convertor, which is fast acting and capable of providing balanced or unbalanced fault current.

#### B. Implementation of VSM within PowerFactory

The diagram in Figure 5 shows the output stage for the PowerFactory VSM model. This model differs from the MATLAB version as it uses the ‘‘Statgen’’ element type in PowerFactory as the basis of the converter interface to the network. This is the same element used for DQCI models, however for a VSM convertor the ‘‘Statgen’’ element is configured in Voltage source mode. DQCI models, by contrast, configure it as current source and use a PLL to provide a phase reference to the network.

PowerFactory RMS studies operate with fixed reference frame. PowerFactory provides two signals,  $u1r\_in$  (v real) and  $u1i\_in$  (v imaginary), for controlling the voltage source which under nominal frequency and steady state conditions are static. Unlike the MATLAB model, which has a three phase voltage reference generator operating at the system frequency, the 50Hz signal is effectively removed from the PowerFactory simulation and replaced by quadrature RMS voltages. Consequently in the PowerFactory the simulation of the 50 Hz oscillator is replaced with a slip frequency oscillator which operates at the difference between the actual and nominal system frequencies. 1 pu system frequency is subtracted from the input signal taken from the rotor simulation to provide the slip frequency which then rotates the quadrature voltages at the slip frequency.

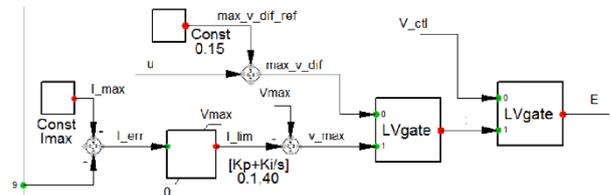


Figure 6. Current limitation applied in the PowerFactory model

The PowerFactory current limit is shown above in Figure 6 and consists of two LV gates which connect the lowest, of the two input signals to their outputs. Normally the voltage regulator signal is selected and used to determine the convertor output voltage. Under short circuit conditions the output voltage is initially prevented from exceeding the terminal voltage by more than 15% which limits the current to about 1.5 times rating as the convertor output reactance is 10%. After a short delay a PI reactive current controller becomes active and takes further action to limit the current to a specified value of 1.25 pu rated current.

## V. ELECTRICAL NETWORK AND TESTS

Figure 7 shows the electrical network used for the studies. It consists of 200 MVA SG and VSM convertor connected to a 15 GVA system and 15 GW load. The transformers step up the voltage from 33 kV (distribution level) to 400 kV (transmission level). The transformers are both rated at 200 MVA and 15% and 17% impedance respectively. The lines have an impedance of 0.04 pu reactance and 0.005 pu resistance (X/R ratio of 8) for total line length rated at 200MVA. The 15GVA system is represented either by a Static Generator, SG or External Grid in the PowerFactory model. This was done to compare results. It was found that the External Grid and Static Generator gave highly comparable results but the Static Generator was more flexible and therefore used for all simulations. Tests were performed to compare the response of the VSM with the SG. The results are shown in the following sections.

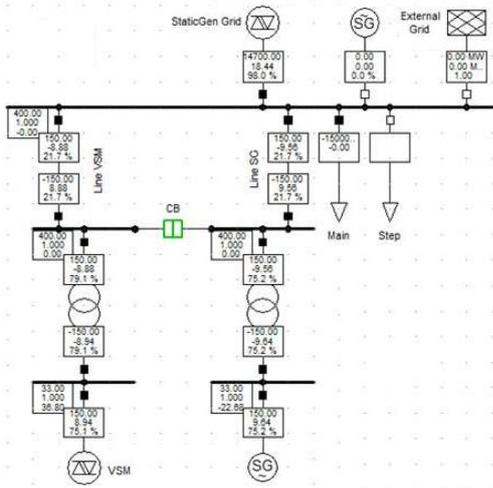


Figure 7. Network model in PowerFactory, including a VSM

**A. 5% Voltage step**

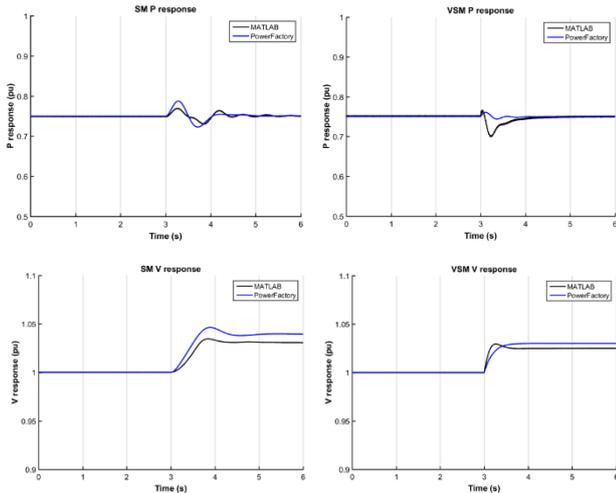


Figure 8. Test A: 5% Voltage step at SG and VSM

For this case study, a 5% voltage step is applied to the voltage reference signal in SG and VSM control system. Both SG and VSM terminal volts have a fast response with no overshoot on the VSM. Both of the generators experience power oscillations which are highly damped either by the damping mechanism in VSM and power system stabiliser (PSS) for SG. The VSM experiences slightly more power oscillations for the case shown, however these can be damped further by increasing the electrical damping factor  $s_{de}$  and there is no need for a PSS. Results from the MATLAB model are also shown for comparison with those from the PowerFactory model. The PowerFactory results were produced from an RMS simulation, whilst the MATLAB results are based on EMT simulation. Note: Because the VSM models are built in different software and use different techniques the results are generally similar but have some differences.

**B. 140ms short-circuit fault at the grid connection point**

To test performance for Type A transmission faults, a 140ms 3ph short-circuit is applied at the grid connection point on the HV side of both transformers. Both generators behave satisfactorily and can recover after the fault is removed. Regarding fault in-feed, the SG outperforms the VSM which current limits, initially at 1.5pu. After a short time, current reduces to an average of 1.25pu, as can be seen from the trace

it's very quick and hunts about 0.1pu. The SG by contrast produces four times the reactive power. However, the retained voltage is twice as high on the SG, result in in double the fault current for a close up fault. Because the SG is a voltage source behind an impedance and the VSM is essentially in constant current mode the difference will reduce the more distant the fault. Both currents reduce with time. On the VSM this is due to the PI controller and on the SG the transient and sub-transient reactance. Although the retained voltage on the VSM is very low, the fault in feed current was closer to the SG than expected. The SG current is limited at 2.5 pu naturally by the impedance of the transformer. Dynamically the VSM outperforms the SG as the dynamic braking is applied to the  $1/2H_s$  term for voltages below 0.85 pu, which is not only beneficial for the power system as a whole but it is also necessary for practical and economic converter design as limits the power required post fault.

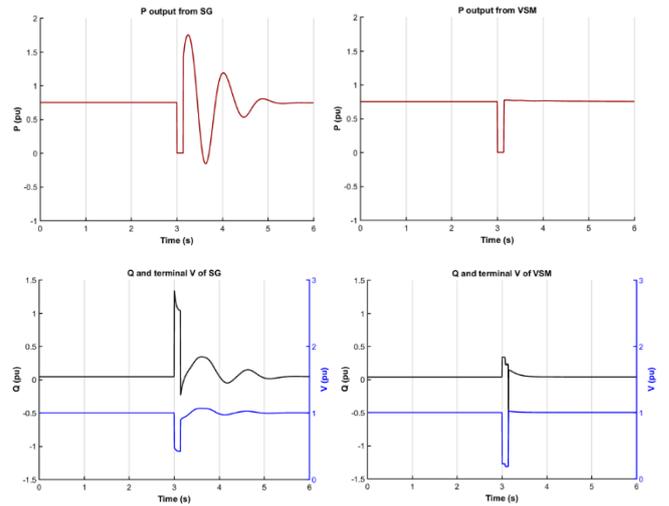


Figure 9. Test B: 140ms TSO fault at the grid connection point

**C. 500ms short-circuit fault at the low voltage (LV) side of the transformers**

As a more severe event locally, 500ms short-circuit (equivalent to distribution fault) fault was applied to SG and VSM busbars at the HV side of the transformers. The fault was applied to the HV as the VSM will probably have similar transformer impedance between it and the fault. The VSM performs better than the SG because its operating angle is prevented from accelerating by the braking system described earlier. By contrast, the SG pole slips.

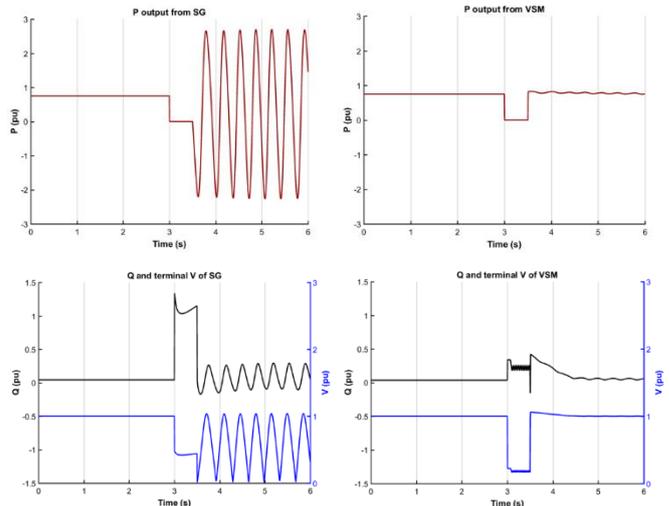


Figure 10. Test C: 500ms TSO fault at LV side of the transformers

#### D. 1 Hz/s frequency ramp from 50Hz to 45 Hz

With the inertia simulated, i.e. the  $1/2H$ s term, the response of the VSM to the 1 Hz/s frequency ramp is similar to that of the SG. At the instance when the frequency starts to ramp, the  $P$  outputs from the two generators ramp up. The increased power largely results from the real or simulated inertia. The height of the pulse being proportional to  $df/dt$ . The slope on the top of the power pulses and increased settling values on completion of the ramp are due to governor action. The settled  $P$  levels are different due to the different limit settings in the VSM governor. For the SG, the voltage fluctuates due to the PSS response to power changes, whereas the VSM is less affected and incurs less voltage disturbance.

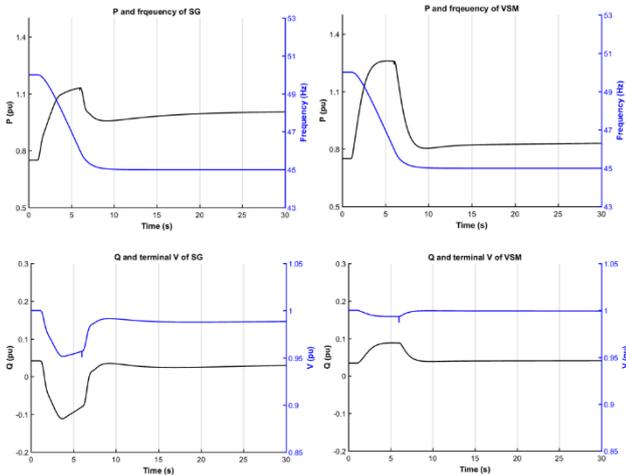


Figure 11. Test D: 1 Hz/s frequency ramp from 50 Hz to 45 Hz

## VI. ADVANTAGES OF VSM CONVERTOR CONTROLS

Regarding the 8 areas of concern for existing DQCI convertors, which were initially raised in the introduction, the VSM controllers presented here typically out performs existing DQCI controller:

- **Increased RoCoF.** Software emulation of inertia in VSM assists with RoCoF and additionally ensures compatibility with existing SG.
- **Loss of synchronising torque/power and reference voltage.** Under normal circumstances, VSM provides synchronising power and a reference voltage for other VSM, SG & DQCI based generation but VSM may introduce classical oscillatory modes of power system instability, as experienced by SG. However, these are well understood and can be tackled using either the conventional control analysis methods, or new techniques some of which are described here.
- **Possibility of high frequency instability and controller interaction.** Limiting the bandwidth of the control signals significantly reduces the risks of high frequency instability and controller interaction. Potential risks regarding convertor protection still remains, as this must operate quickly. This is applicable to all convertors and is not VSM specific.
- **Difficulties associated with modelling the electricity systems dynamic behavior.** VSM and VSM0H convertors of the type discussed here can be modelled using RMS

simulations, using the described techniques, making large system studies possible.

- **Reduced and possibly delayed fault in feed.** In common with existing convertors, a convertor operating as a VSM will still be constrained to the maximum instantaneous current limit allowed by the solid-state devices. However, VSM technology allows faster and more appropriate fault in-feeds to be made. The currents rise within 1 cycle, and can provide unbalanced fault current at whatever power factor(s) the fault impedance(s) draw [19].
  - **Possibility of voltage instability during or post fault e.g. collapse, blocking or post-fault overvoltage.** The delivery of reactive power from VSM during a fault, unlike DQCI, is driven by the network and not the controller which largely maintains its pre-fault operating angle but reducing its voltage output in order to limit the current. This has the additional advantage that the reactive power naturally reduces on fault clearance reducing the risk of post-fault overvoltage. Many convertors currently block (i.e. produce no output) when the voltage falls below 20-40% of the nominal value. On a weaker system, where fault levels are reduced, this is of greater concern and the blocking voltage may need to be reduced or eliminated.
  - **Potential for sub-synchronous oscillations and interaction with conventional machines.** The 5 Hz bandwidth limit is in part driven by the assumptions that sub-synchronous and torsional oscillations occur between 4 and 50 Hz [21]. The examples provided here significantly attenuate all signals at and above 4 Hz.
  - **Potentially increased sensitivity to load imbalance and the harmonics.** The 5 Hz bandwidth limit effectively decouples the measurement and feedback signals from the waveform generation, allowing greater time for measurement with the potential to mitigate the effects of imbalance and harmonics on the measurements. The output stage of this type of convertor attempts to produce a three phase balanced voltage waveform. When connected to a system where external harmonic or unbalanced voltage and / or currents are present, these result in additional harmonic or unbalanced load current in the convertor. In principle, from a system perspective, the absorbing of some of the harmonic and unbalanced currents is desirable. However, the “soaking up” of voltage unbalance (fundamental or harmonic), results in a power modulation at the DC side of the convertor and may lead to higher capacitor  $dV/dt$  ratings, as for example indicated in [22]. It is foreseen that strategies might be implemented which allow for harmonic or imbalance within specific design limits and then control actions, such as modifying the phase amplitudes or output wave shape, may be permitted to reduce the adverse effects. These might be implemented so as to not impair the advantages VSM provided that a bandwidth limit on such control actions exists to prevent them interacting with other control systems both within the VSM and external to it. For example limiting the bandwidth to 5 Hz would largely maintain the integrity of the proposed solution. Further limiting to 0.05 Hz would have the advantage of placing any such actions beyond traditional power system instability.
- The commercial implications of the possibly changed converter ratings and the stored energy demanded by VSM need to be considered. Such implications would also exist for VSM0H and DQCI with SEBIR (i.e. Synthetic Inertia).

## VII. CONCLUSIONS AND FURTHER WORK

With power system portfolios changing rapidly and with increasing instantaneous penetration level of converters in the power networks, ensuring satisfactory and secure operation is becoming more challenging. Recent studies have shown instability of the power networks when the penetration of conventional DQCI converter reaches beyond ~50%, due to the reduced system strength and wide range of modes and frequency bandwidths in the converter controllers. Former work by the authors have investigated the VSMOH converter control technique with no inertia emulated, these have proved to have the great benefits on power quality as well as stability. In this paper, a VSM architecture is proposed, which provides benefits of the VSMOH converter, plus dynamic braking, inertia and damping emulation. Although the VSM behaves in a similar manor to the SG, it can outperform SG in various aspects since its parameters are not constrained by the physical design, cost or mechanical/ practical considerations and is therefore possible control and achieve specific desired behaviours, which can be advantageous for both operational and economic considerations.

The VSM converter presented here has the potential to increase the penetration level limits of converters in a power network to 100% [20]. Based on various case studies, these results demonstrate that the VSM converter response can be sufficiently accurately modelled using RMS simulation in PowerFactory. Therefore, RMS-based models, in many cases, are sufficient for studying wider power system challenges on larger networks, if they contain sufficient SG and/or VSM-equipped converters to assure high-frequency stability (which is difficult to model in the RMS simulation).

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