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Enhanced DC voltage control strategy for fault management of a VSC-HVDC connected offshore wind farm

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Abstract

This paper proposes a DC voltage control strategy for fault management taking into advantage the operation of the master controller located in the offshore AC substation platform. The issue resolved via the proposed controller relates to overvoltages caused in the HVDC links when the power transfer onshore is disrupted due to faults occurring at the AC side of the onshore grid. The control strategy presented in this paper proposes an effective way of maintaining the DC over-voltage within safety limits via reducing the connected wind farm power output. The operation of the aforementioned control strategy requires small computational power and no communication.

1. Introduction

Currently, offshore wind farms have ratings up to 500 MW with their capacities expected to reach 1 GW within the next few years [1]. The existing AC solutions, even though are well-established, have a number of disadvantages making them inappropriate for the future far offshore multi-GW applications; major issues being relatively high losses, requirements for reactive power compensation and limited transfer capability. The DC systems can overcome these limitations to a large extent. Specically, HVDC systems and especially VSC-HVDC systems are characterised by the advantages listed below when compared to relevant AC systems [2]–[5]:

- More efficient long distance power transmission.
- Interconnection of asynchronous grids.
- Independent control of active and reactive power.
- AC system support.
- Unity power factor.
- Short circuit level limitation.
- Limited visual impact.

As it has become apparent that HVDC systems will be deployed on a large scale in the future, their analysis has gained a lot of attention from many research teams worldwide. One of the issues that needs to be analysed relates to the fault ride through (FRT) capability of the offshore wind farms. Due to their large capacities offshore wind farms are required to comply with the fault ride through requirements as set out in the various grid codes; this implies that offshore wind farms need to remain connected to the grid under certain fault conditions. However the reduction of power injected into the mainland AC grid due to the fault results in increase of the DC voltage of the link as the power balance is disrupted. Significant over-voltages could potentially impact the lifetime of the cable (e.g. short duration single phase faults onshore) or even permanently damage the cable (e.g. major three-phase faults onshore that lead to extreme over-voltages). In the literature several methods are proposed for maintaining the DC voltage within acceptable limits during the faults. However, these methods require either additional equipment (e.g. DC choppers [6]), therefore they impose additional capital costs to the wind farm and waste of energy or need for communication between the HVDC converter station and the wind farm and as such they are considered slow for the given time frame of the faults. More efficient methods identify the DC over-voltage and once this is done they impose an over-frequency condition on the offshore AC grid so that the inidividual WTG controllers decrease the WTG output with no need for additional equipment or communication; however they are not the most effective as they need to interpret the fault into a certain condition at the offshore AC grid before action is initiated. The proposed method in this paper requires no communication and the response time is within the time frame of typical faults. Furthermore, under certain conditions even the use of additional equipment can be omitted so that no power is wasted (see subsection 4.3 for more details).

The paper is organised as follows: Section 2 provides the basis of modelling HVDC systems and their associated control. Section 3 presents the issue and gives a brief overview of the existing FRT methodologies while the proposed FRT approach is introduced. Section 4 shows the simulation results and finally in Section 5 conclusions are drawn.

2. Modelling and Control of VSC-HVDC Transmission System

The major components of a typical VSC-HVDC transmission system, as illustrated in Figure 1, are a AC/DC converter station, a DC transmission line and a DC/AC converter station which provides an interface with an onshore AC system. With regard to their topology VSCs are controlled using two controllers, known as Upper Level (UPC) and Lower Level (LLC) controller. The UPC generates a three-phase voltage reference signal according to the mode of VSC operation (e.g. power and/or voltage control mode).
The LLC is used to control the switching of power electronics valves, where the PWM technique is usually adopted. The control mode of the UPC is illustrated in Figure 2 and comprises of two controllers, the outer (power or voltage) controller and the inner current controller (ICC) [7]–[10].

**Active and reactive power controllers:** In the three phase system, the active and reactive power can be calculated by equations 1 and 2 respectively:

\[ p_{AC} = v_a i_a + v_b i_b + v_c i_c \]  
\[ q_{AC} = \frac{1}{\sqrt{3}} (v_{ab} i_c + v_{bc} i_a + v_{ca} i_b) \]  

When the q axis is aligned with the voltage phasor of the AC grid (i.e. \( e_d = 0 \)), the active and reactive powers in the \( dq \) frame are expressed as:

\[ p_{AC} = e_q i_d \]  
\[ q_{AC} = e_q i_d \]  

Equations (3) and (4) prove that the active and reactive powers are independently controlled by the \( q \) and \( d \) components of the converter current respectively.

- **DC voltage controller:** The DC voltage controller maintains the DC voltage at a predefined value by controlling the active power exchange with the AC grid and this is achieved by regulating the reference value of the \( q \) component of the current. The controller is set up to operate in accordance with the error of the energy stored in the capacitor, \( \Delta W_C = W_C^* - W_C \). The energy of the capacitor is proportional to the square of the DC voltage.

- **AC voltage controller:** The responsibility of the AC voltage controller is to control the amplitude of the AC voltage at the connection point which is achieved by regulating the \( i_d^* \). The main goal of the ICC is to generate the AC current by evaluating the voltage drop on the series reactance of the connected AC system. For this to be achieved, the input signals (i.e. converter currents) are transformed to a rotating \( dq \) reference frame via the Park Transformation [11]. The reference is synchronised with the AC voltage of the grid via a Phase Locked Loop (PLL). Using this technique the active and reactive power can be controlled independently. The output of the ICC is a reference value for the converter voltage, which is then used by the LLC to control the switching of the power electronic valves.

Considering the simplified equivalent model of the AC side of a VSC converter, the following equation can be written:

\[ e_s - v_C = R_T i_C + L_T d/dt(i_C) \]  

Using the Park Transformation equation (5) can be written as:

\[ e_d - v_d = R_T i_d + L_T d/dt(i_d) - L_T i_q \]  
\[ e_q - v_q = R_T i_q + L_T d/dt(i_q) - L_T i_d \]  

Where \( L_T, R_T \) are the equivalent inductance and resistance between the converter and the connection point with the AC grid.

### 3. FRT Control Strategies

Under normal AC system operation offshore wind farms (WFs) operate at their maximum export capability and the
offshore HVDC converter station injects unconstrained electrical power into the DC links to be delivered to the mainland AC transmission system. However, when faults occur on the AC system, the power injected into the AC grid from the offshore HVDC converter station is reduced significantly. Since the wind farms continue to produce the same amount of power the resulting imbalance leads to DC link over-voltages. In order to prevent the wind farm from being disconnected these over-voltages must be rapidly constrained to an acceptable level.

### 3.1. Existing FRT Methods

Several voltage limiting strategies can be found in the technical literature to facilitate FRT capability. These can be summarised as follows:

- **Use of a DC chopper:** This method utilises a fully-rated DC chopper connected in series with a braking resistor as illustrated in Figure 1 [10]. The energy excess is dissipated through the resistor to maintain the DC voltage within acceptable limits. The method is advantageous as the WF remains in operation, and is also suitable when HVDC systems are connected to weak AC networks. However, this solution comes at additional cost, an increased substation footprint and more importantly significant waste of produced energy until the fault is cleared.

- **Reduction of wind farm output utilising communication systems:** This is a de-loading control strategy which utilises the communication infrastructure of the VSC - HVDC system to transmit the appropriate signals to the WTGs for active power reduction [12]. This option comes with potential delays due to the need for transmitting the command signals through communication links.

- **Fast reduction of WF active power output at the generator level:** In this method, active power control schemes are utilised to reduce the active power output of the generators and limit the rise of DC voltage. Based on this idea many different approaches are proposed [6], [10], [13], [14]; all these methods aim to avoid the use of DC chopper, hence reducing the cost and power losses. However, there are many challenges which have to be taken into consideration, such as mechanical and electrical stresses of the equipment, need for communication signals and frequency/angular stability of the AC system.

### 3.2. Proposed FRT Method

This paper proposes a DC voltage control strategy which takes advantage of the operation of the master controller located in the offshore AC substation platform. The master controller calculates the active power reference for each wind turbine generator taking into account any measurements received from the HVDC link (e.g. status of the DC voltage). This allows the master controller to calculate appropriate reference values, even for instances when there is a fault on the HVDC link. The guiding principle for this is the fact that any fault on the AC side of the grid imposes a certain amount of over-voltage in the DC link [5]. Examples of this behaviour include faults at the AC connection point as well as voltage dips resulting from other remote AC faults. The time duration when the onshore grid is not available affects the over-voltage levels as well as the Rate of Change of Voltage (ROCOV). Therefore, a tracking curve is added into the master controller, as illustrated on Figure 3, to adjust the active power reference in relation to the severity of the fault, and specifically the ROCOV.

In addition to the master controller, a DC chopper is connected close to the offshore VSC converter station to avoid extreme values of DC voltage (e.g. in excess of 1.6 p.u. as shown in Figure 6) when the reduction of the active power export is not sufficient to mitigate it. Iterative simulations indicated that for major three-phase faults the active power export should be reduced to zero for the over-voltage to be maintained within permissible limits (i.e. 1.2 p.u.). However, this is not considered realistic within the given time frame due to the WTGs’ inertia and dynamics involved. Therefore, the controller is adjusted to reduce the active power to a minimum of 40% of the rated value.

In steady state condition the offshore WF operates under the power reference denoted by \( P_{WF}^* \). When OV Stage I threshold is exceeded, the power reference \( P_{WF(I)}^* \) will be adjusted according to the power curve illustrated in Figure 3. To avoid unnecessary continual adjustments during normal operation, the controller is enabled only when the DC link voltage exceeds a predefined limit. For the purposes of this paper this voltage threshold was set to 1.02 p.u. Within the master controller a power restoration loop is also integrated.
This allows the system to ramp-up the power reference \( P_{WF(III)} \) smoothly following the successful clearance of the fault. Such control loop provides a non-oscillatory power restoration to the system improving power quality and response of the HVDC system. During the operation of the controller under the restoration loop, the power is not tracked through the power curves, hence the derivative \( dV_{dc}/dt \) has no impact on the power reference. A secondary over-voltage element (OV Stage II) is integrated in the controller to enable the DC chopper when the reduction of the active power export is not sufficient to keep DC voltage within admissible limits. The over-voltage threshold for stage II has been set to 1.19 p.u.

The proposed controller, which benefits from the fact that no communication signals and small computational power are required, is illustrated in Figure 4. The strategy was modelled in Matlab/Simulink® and validated using transient simulation.

4. Validation Case Studies

This section presents simulation based case studies which demonstrate and quantify the key benefits of the proposed controller.

4.1. Model Description

Figure 5 illustrates the model network which includes a wind farm comprised of Permanent Magnet Synchronous Generators (PMSGs) connected by Fully Rated Converters (FRC) (Figure 5.a). The wind farm is connected to an offshore 2-level VSC converter station. Power is transferred onshore via 300 km HVDC transmission line operating at 640 kV and is injected into the AC grid through the onshore 2-level VSC converter station (Figure 5.b).

![Figure 5: Case Study Network - (a) Offshore Wind Farm (b) HVDC and HVAC Transmission Networks](image)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>WF Capacity</td>
<td>1 GVA</td>
</tr>
<tr>
<td>PMSG Transformer</td>
<td>3.3/66 kV</td>
</tr>
<tr>
<td>WF Transformer</td>
<td>66/300 kV</td>
</tr>
<tr>
<td>DC Line Length</td>
<td>300 km</td>
</tr>
<tr>
<td>DC Voltage</td>
<td>±320 kV</td>
</tr>
<tr>
<td>AC Voltage (L-L, RMS)</td>
<td>400 kV</td>
</tr>
<tr>
<td>AC Frequency</td>
<td>50 Hz</td>
</tr>
<tr>
<td>X/R Ratio of AC Network</td>
<td>20</td>
</tr>
<tr>
<td>AC Short-Circuit Level</td>
<td>10 GVA</td>
</tr>
<tr>
<td>Onshore Transformer</td>
<td>300/400 kV</td>
</tr>
</tbody>
</table>

Table 1: Wind Farm, HVDC and Onshore AC Network Parameters.

The case study network parameters are presented in detail in Table 1. The values are selected to represent typical offshore wind farm networks to be deployed in the coming years. Specifically, with regards to the utilised WTGs, the major manufacturers currently design units with capacities in excess of 8 MW. These wind turbines operate at 3.3 kV or 6.6 kV, hence the transformers’ LV winding needs to be rated accordingly. The length of DC transmission line was set to 300 km to represent likely future distances. There are already offshore wind farms being developed with distances greater than 100 km (e.g. Hornsea Project One), and it is expected that offshore wind farm locations will cover distances greater than 200 km from the coast in the future.

4.2. Case Studies

The case studies presented in this paper include single-phase to ground and three-phase faults. The selection of only these two types of faults stems from the fact that single-phase to ground faults are the most frequent faults and three-phase faults, even though rare, represent the most challenging situation from an FRT perspective. The response of the system is tested under different settings (i.e. different slopes of the power adjustment characteristic) on the master controller as illustrated in Figure 4.

4.3. Simulation Results

In Figure 6 the DC voltage natural response (i.e. without any limiting control scheme) is presented for single and three phase faults on the AC side of the onshore grid. Such response clearly illustrates the nature of the over-voltage problem. In both cases the DC voltage rise can be observed, approximately 1.2 p.u. and 1.6 p.u. for single phase and three phase faults respectively. These figures can be used as a reference to evaluate the system performance when the FRT DC voltage control scheme is enabled. An example response of the system when the proposed scheme is utilised under setting option 2, is presented in Figures 7 and 8 for single and three phase faults respectively.

A single phase fault (Figure 7) is triggered at \( t = 0.2 \) seconds. After the fault inception the power reference is reduced to 0.4 p.u. This has a desirable limiting effect on DC voltage which remains within the safe operating region (i.e. below 1.2 p.u.). After a few milliseconds, when then fault
has no longer severe impact (due to its potential clearance by onshore protection systems) on the DC voltage, the system restoration process is initiated. Power reference is ramped up to pre-fault value within less than 100 ms, while DC voltage is gradually restored to 1.0 p.u. in a slightly oscillating manner.

The master controller has been tested under all of the proposed setting options depicted in Figure 3 for both types of faults (i.e. single and three phase). In order to determine the most appropriate setting for the tracking curve, a number of iterations have been attempted, which led to the conclusion that setting 2 provides the system with a most favourable response. Figure 9 includes the response of the controller for the same fault (i.e. single-phase to ground fault) under different controller settings. Although setting 1 seems to have the best response (i.e. the shortest recovery time) it results in an abrupt power reduction which could cause unnecessary mechanical and electrical stress to the WTs (especially when an equally safe response can be achieved with other settings). Setting options 4 and 5 are not considered feasible, as a DC chopper would have to be used, which is in fact not necessary for single phase faults. Between the remaining Settings 2 and 3, the former has been selected as it provides a better safety margin (0.1 p.u). For three phase faults the response of the system indicated that operation of the DC chopper is inevitable under all setting options. However setting options 3, 4 and 5 indicated the longest operation time and consequently they were not considered as efficient options. For setting options 1 and 2 the required operation of the DC chopper was almost equal hence setting 2 was selected as it was found to be well aligned with favourable option for single phase faults.
Simulations indicated that AC-side faults in close proximity to converter stations can cause high DC over-voltages in the range of 1.2 - 1.6 p.u. that can potentially damage the DC link. The proposed DC voltage control scheme which benefits from the fact that no communication signals and small computational power are required, was found to effectively limit the impact of the aforementioned faults on the DC voltage rise. Specifically, for single-phase faults it has been observed that during the fault, and when the controller is enabled, DC voltage stays below 1.1pu and is eventually restored to the nominal value within approximately 400 ms. As far as the three-phase faults are concerned, the DC voltage stays within the acceptable limits (i.e. 1.2pu), but for this to happen operation of the DC chopper is required for a short period of time. In this case restoration to the nominal value is achieved within 200 ms. It is also worth noting that restoration of the DC voltage is significantly reduced when the proposed controller is enabled (e.g. for a single phase fault restoration periods in only a third of the time required when the controller is not enabled). Regarding the sizing of the DC chopper, the results indicate that even though its presence is required during the major three-phase faults, the size of the resistor can be established using cost benefit analysis. For such a study to be more effective it is proposed to consider extreme values of typical Contract for Difference (CID) rates for Round 3 UK offshore wind farms and eventually calculate an Operational Expenditure (OPEX) figure for each of these cases. The results could be used to draw a conclusion for a technically but also economically feasible solution.

5. Conclusions

Figure 9: DC Voltage $V_{dc}$ response under different controller settings

References