

Current-Time Characteristics of Resistive Superconducting Fault Current Limiters

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Abstract—Superconducting fault current limiters (SFCLs) may play an important role in power-dense electrical systems. Therefore, it is important to understand the dynamic characteristics of SFCLs. This will allow the behavior of multiple SFCLs in a system to be fully understood during faults and other transient conditions, which will consequently permit the coordination of the SFCL devices to ensure that only the device(s) closest to the fault location will operate. It will also allow SFCL behavior and impact to be taken into account when coordinating network protection systems.

This paper demonstrates that resistive SFCLs have an inverse current-time characteristic: they will quench (become resistive) in a time that inversely depends upon the initial fault current magnitude. The timescales are shown to be much shorter than those typical for inverse overcurrent protection. A generic equation has been derived which allows the quench time to be estimated for a given prospective fault current magnitude and initial superconductor temperature, and for various superconducting device and material properties. This information will be of value to system designers in understanding the impact of SFCLs on network protection systems during faults, and planning the relative positions of multiple SFCLs.

Index Terms—Distributed generation, fault current limitation, low-carbon, power system protection, superconducting fault current limiter (SFCL).

I. INTRODUCTION

SUPERCONDUCTING fault current limiters (SFCLs) offer an attractive solution to the problem of rising fault levels in highly power-dense electrical systems [1], [2], [3], [4].

However, for SFCLs to be effectively applied in power systems, it is important to understand their transient properties in order to coordinate their operation with power system protection devices and to ensure that, in a multiple SFCL application, that only the SFCL(s) closest to the fault location operate in order to avoid unnecessary disturbance to healthy elements of the power system. This paper analyzes a typical transient SFCL model, and determines its current-time characteristics. Section II introduces the SFCL model, and the model is analyzed mathematically in Section III.

II. SFCL MODEL

A. SFCL Model Requirements

For this study, it is important to realistically model an SFCL's resistance characteristics, in particular:

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- In a three-phase power system, each phase of the SFCL must be modeled independently because they will operate independently, particularly during unbalanced primary system faults, which represent the predominant mode of fault in power distribution systems (particularly in overhead systems) [5]. Each phase will have a dedicated superconducting wire (or several wires) which form a superconducting element. This means that within the first cycle of fault current during a three-phase to earth fault, each phase of the SFCL will develop resistance at a slightly different time, hence creating a momentary phase unbalance. Unbalanced faults may only cause a quench in only one or two phases of the SFCL. Independent operation of each phase must be represented such that the effects on the overall power system can be evaluated for all fault types at various locations.
- The SFCL model should be a reasonable approximation of transient SFCL behavior during faults, and therefore should consider thermal properties [6]. The model should be adaptable, with little or no modification, for different types of faults or other scenarios. It should be able to model the effects of different fault durations, different times of fault occurrences, different fault current magnitudes, and cater for any point of fault occurrence with respect to the voltage waveform. The model should execute without excessively long simulation times.
- The dimensions of the superconducting wires must be known. A given superconducting wire will have a full load current rating. To achieve a higher full load current rating, several wires may be connected in parallel, as described in [7], [8].

B. SFCL Model Implementation

SFCLs have been modeled using the equations from [9], which is based on a BSCCO (Bi2212) superconductor, and fits well with experimental results. Reference [10] extends this model with a simplified, yet practical, thermal sub-system. The model has been implemented in Simulink with SimPowerSystems [11]. An independent, single-phase superconductor model has been replicated to create a three-phase SFCL.

The model is based on the three possible states for a superconductor: flux-creep, flux-flow, and normal (resistive) conducting. Fig. 1 illustrates the typical relationship between electric field, E , and current density, J , for these three states. Table I defines the symbols used. Note that, after entering the flux-flow state, the superconductor heats up significantly, which in turn shifts the E - J curve to the left; i.e., E increases further.

TABLE I
SUMMARY OF SFCL MODEL PARAMETERS

Parameter description	Symbol	Value	Units
Length of the superconductor wire	l_{sc}	50	m
Diameter of the superconductor wire	d_{sc}	4.0×10^{-3}	m
Cross-sectional area of the superconductor wire	a_{sc}	$\pi \left(\frac{d_{sc}}{2}\right)^2$	m^2
Superconductor temperature	$T(t)$	—	K
Superconductor current	$i_{sc}(t)$	—	A
Ambient (starting) temperature of the superconductor and cooling reservoir	T_a	77	K
Superconductor critical temperature	T_c	95	K
Flux-creep region exponent (at 77K)	n_{77K}	6	—
Flux-flow region exponent	β	3	—
Current density	$J(t)$	$\frac{i_{sc}(t)}{a_{sc}}$	A/m^2
Critical current density (at 77K), i.e., current density where $E = 1 \mu V/cm$	J_{c77K}	1.5×10^7	A/m^2
Electric field at transition from flux-creep state to flux-flow state	E_0	0.1	V/m
Normal conducting state resistivity (at T_c)	ρ	1.0×10^{-6}	Ωm
Coefficient for heat transfer to cooling reservoir	κ	1.5×10^3	W/Km^2
Superconductor volumetric specific heat	c_v	1.0×10^6	J/Km^3
Superconductor heat capacity	c_{sc}	$l_{sc} a_{sc} c_v$	J/K
Thermal resistance from superconductor to cooling reservoir	θ_{sc}	$\frac{1}{\kappa l_{sc} \pi d_{sc}}$	K/W
Heat dissipated in the superconductor	$Q_{sc}(t)$	$\int i_{sc}(t)^2 R_{sc}(t) dt$	J
Heat removed by the cooling system	$Q_{removed}(t)$	$\int \frac{T(t) - T_a}{\theta_{sc}} dt$	J
Instantaneous superconductor resistance	$R_{sc}(t)$	$\frac{E(t, T) l_{sc}}{J(t) a_{sc}}$	Ω

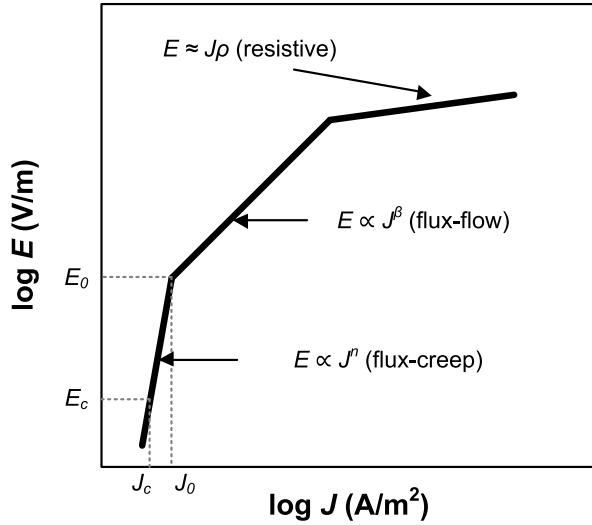


Fig. 1. Superconductor E - J characteristic, showing three states (from [9])

The thermal sub-system has been modeled as follows:

$$T(t) = T_a + \frac{1}{c_{sc}} \int_0^t [Q_{sc}(t) - Q_{removed}(t)] dt. \quad (1)$$

It is assumed that the resistivity varies linearly with temperature, when $T(t) \geq T_c$, as given by (2). This is a reasonable

assumption and is justified and supported by the experimental results in [12]. The variation of J_c with temperature is also assumed to be linear, as shown in (3). Reference [13] illustrates this property for BSCCO, and reference [14] for MgB_2 ; it is this variation of J_c (and J_0) with temperature which allows the operating current for an SFCL to be adjusted.

$$E(t, T) \approx \rho \left(\frac{T}{T_c} \right) J(t), \quad T(t) \geq T_c \quad (2)$$

$$J_c(T) \approx J_{c77K} \left(\frac{T_c - T(t)}{T_c - 77} \right) \quad (3)$$

Many of the parameter values given in Table I are taken from [10]. The resistivity at T_c , ρ , was selected as $1 \mu\Omega m$ and the length, l_{sc} , was varied to limit fault current to approximately the same root-mean-square (RMS) value as load current. This results in an SFCL phase resistance of approximately 4.0Ω at 95 K, and 12.6Ω at 300 K. The diameter, d_{sc} , was chosen such that the superconductor does not enter the flux-flow state during normal load current, but that each SFCL will enter the flux-flow state during the first cycle of fault current. In reality, the wire diameter may be fixed, but several wires may be connected in parallel to achieve a particular current rating [8]. β was reduced to 3 to reduce the rate of increase of flux-flow resistance. A cylindrical superconductor wire geometry is used, rather than the cuboid shape modeled in [10].

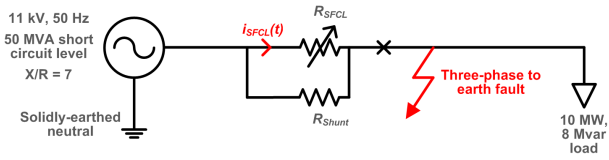


Fig. 2. SFCL fault current limitation test network

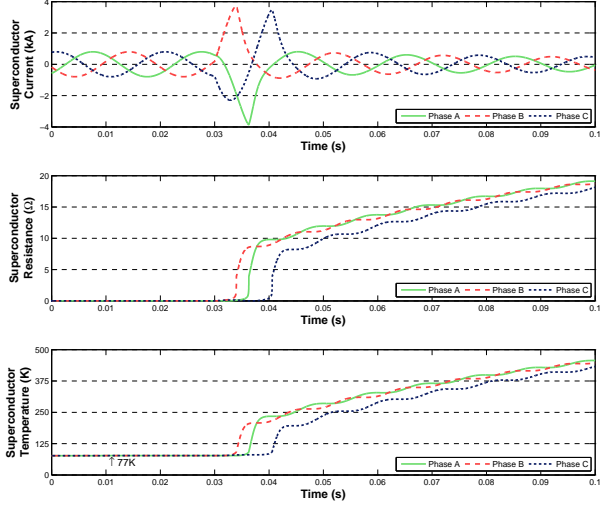


Fig. 3. SFCL model response for a three-phase to earth fault

C. SFCL Model Example

To test the fault current limitation properties of the model, the circuit in Fig. 2 was simulated. Fig. 3 illustrates the response of the model for a three-phase to earth fault, with negligible fault resistance. The SFCL quenches in each phase during the first peak of fault current. If the fault is cleared 0.1 s after occurrence, each phase of the SFCL takes between 2 s and 4 s to drop below T_c . This SFCL design corresponds to the “slow-heating” category described in [9], because the quench time is in the order of 10ms.

Resistive SFCLs typically incorporate an impedance that is connected electrically in parallel with the SFCL [1], [8], [10], as shown in Fig. 2. To simplify the analysis in this paper, it is assumed that there is no shunt impedance. The effect of the shunt impedance will be most prominent after an SFCL fully quenches to the resistive state, which does not affect the analysis in the following section.

III. ANALYSIS OF SFCL CURRENT-TIME CHARACTERISTICS

A. Analytical Solution

The SFCL model equations can be analyzed to approximate the current-time grading, i.e., the time taken to quench for a given fault current. The temperature of the superconductor is calculated using (1), where $E(t, T)$ (within $Q_{sc}(t)$) is calculated according to [9], in the flux-flow region (where $E(t, T) \geq E_0$ and $T(t) < T_c$). When $T(t) \geq T_c$, it is assumed that the superconductor quenches. Equation (1) can be differentiated and manipulated as follows:

$$\begin{aligned} \frac{dT}{dt} &= \frac{1}{c_{sc}} [Q_{sc}(t) - Q_{removed}(t)] \\ \frac{dT}{dt} &= \frac{1}{c_{sc}} \left[i(t)E(t, T)l_{sc} - \frac{T(t) - T_a}{\theta_{sc}} \right] \\ dt &= \frac{dT}{\frac{1}{c_{sc}} \left[i(t)E(t, T)l_{sc} - \frac{T(t) - T_a}{\theta_{sc}} \right]} \end{aligned}$$

To simplify the analysis, a constant dc fault current, I , is assumed. Therefore, E can be simplified to be only a function of temperature:

$$\int dt = \int \frac{dT}{\frac{1}{c_{sc}} \left[IE(T)l_{sc} - \frac{T(t) - T_a}{\theta_{sc}} \right]} \quad (4)$$

where $E(T)$ is [9]:

$$E(T) = E_0 \left(\frac{E_c}{E_0} \right)^{\frac{\beta}{n_{77K}}} \left(\frac{J_{c77K}}{J_c(T)} \right) \left(\frac{J(t)}{J_{c77K}} \right)^\beta \quad (5)$$

Substituting (3) into (5) gives:

$$\begin{aligned} E(T) &= E_0 \left(\frac{E_c}{E_0} \right)^{\frac{\beta}{n_{77K}}} \left(\frac{T_c - 77}{T_c - T} \right) \left(\frac{J(t)}{J_{c77K}} \right)^\beta \\ &= E_0 \left(\frac{E_c}{E_0} \right)^{\frac{\beta}{n_{77K}}} \left(\frac{T_c - 77}{T_c - T} \right) \left(\frac{I}{J_{c77K} a_{sc}} \right)^\beta \end{aligned}$$

For convenience, the non-temperature-dependent part of $E(T)$, along with I and l_{sc} from (4), can be combined as k as follows:

$$\begin{aligned} IE(T)l_{sc} &= k \frac{T_c - 77}{T_c - T} \\ k &= IE_0 \left(\frac{E_c}{E_0} \right)^{\frac{\beta}{n_{77K}}} \left(\frac{I}{J_{c77K} a_{sc}} \right)^\beta l_{sc} \\ k &= E_0 \left(\frac{E_c}{E_0} \right)^{\frac{\beta}{n_{77K}}} \frac{I^{\beta+1}}{(J_{c77K} a_{sc})^\beta} l_{sc} \end{aligned}$$

This provides an expression for t , the time for the superconductor to reach a particular temperature, T , as follows:

$$t = c_{sc} \int \frac{dT}{\left[k \frac{T_c - 77}{T_c - T} - \frac{T - T_a}{\theta_{sc}} \right]} \quad (6)$$

Equation (6) can be solved by (8). The constant, C , given in (9), can be calculated by substituting $T(0) = T_a$ into (8).

In the common case, using liquid nitrogen as the cryogen, where $T_a = 77$ K, $T_c = 95$ K, and $T = 95$ K, (8) can be simplified to (7) as follows:

$$t = 6c_{sc}\theta_{sc} \frac{\arctan\left(\frac{3}{\sqrt{2\theta_{sc}k-9}}\right)}{\sqrt{2\theta_{sc}k-9}} \quad (7)$$

Fig. 4 illustrates the current-time characteristics for three different values of the initial temperature, T_a . For example, for $T_a = 77$ K, a fault current of at least 1.9 kA (RMS or

$$t = c_{sc} \theta_{sc} \left(\frac{\arctan \left(\frac{T_a - 2T + T_c}{\sqrt{-(T_a - T_c)^2 + 4\theta_{sc}k(T_c - 77)}} \right) (T_a - T_c)}{\sqrt{-(T_a - T_c)^2 + 4\theta_{sc}k(T_c - 77)}} - \frac{\ln(T^2 + (-T_a - T_c)T + T_a T_c + \theta_{sc}k(T_c - 77))}{2} \right) + C \quad (8)$$

$$C = c_{sc} \theta_{sc} \left(\frac{\arctan \left(\frac{T_a - T_c}{\sqrt{-(T_a - T_c)^2 + 4\theta_{sc}k(T_c - 77)}} \right) (T_a - T_c)}{\sqrt{-(T_a - T_c)^2 + 4\theta_{sc}k(T_c - 77)}} + \frac{\ln(\theta_{sc}k(T_c - 77))}{2} \right) \quad (9)$$

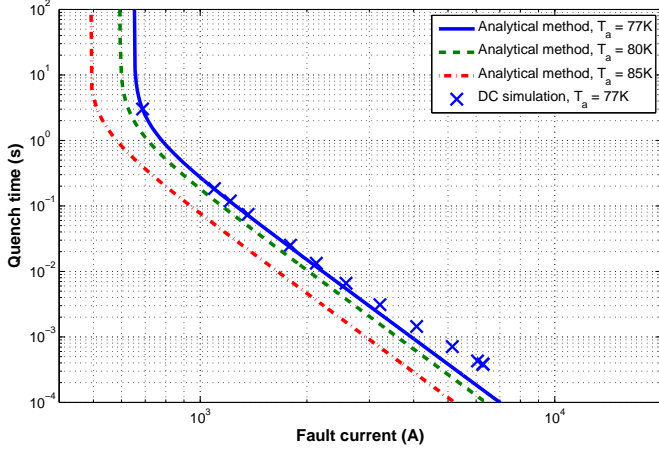


Fig. 4. SFCL model current-time characteristics (analysis and simulation). Fault current for dc simulation is the initial fault current from a constant dc voltage source.

dc) is required to ensure SFCL quenching within the first ac cycle (20 ms), compared with 1.4 kA when $T_a = 85$ K. The cut-off current for which quenching would not occur is shown by the vertical asymptotes. It can be noted that varying the initial superconductor temperature, T_a , affects both the critical current and the position with respect to the y-axis of the current-time curve, as illustrated in Fig. 4. Although this analysis excludes factors such as the instantaneous values of ac current and the point on wave of fault inception (see Section III-B), it does show that an SFCL will inherently act in a manner that is consistent with an inverse current-time graded protection system.

It should be noted that, unlike inverse current-time protection relays, the flux-flow resistance will reduce fault current before a circuit breaker opens, thereby slightly delaying quenching, compared with the quench time predicted by (7). For simplicity, this is ignored in the analysis above, but Fig. 4 also illustrates simulation results, using a single-phase constant dc voltage source. The fault current only drops by approximately 1-5%, until very close to the quench point. Furthermore, very short quench times, in the order of 1 ms or less, may be problematic in practice because of the potential for transient overvoltages due to the high rate of change of current through the circuit inductance, L , with voltage given by $L \frac{di}{dt}$ [9].

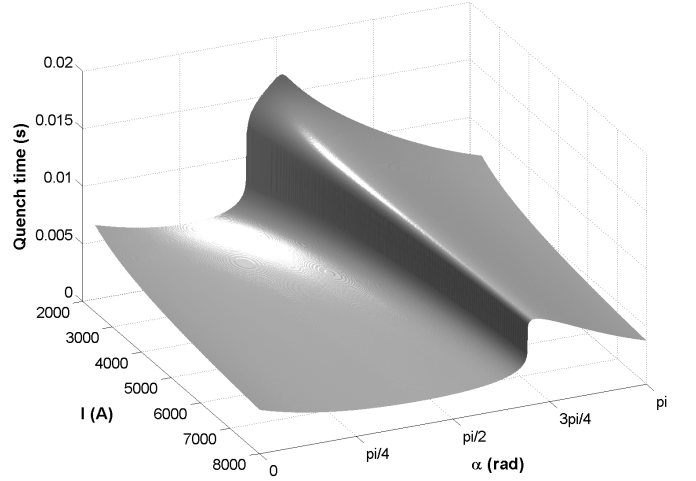


Fig. 5. Effect of current magnitude and α on quench time

B. Effect of the ac Point on Wave of Fault Inception

The instantaneous ac current during a fault can be modeled using (10) as follows [8]:

$$i(t) = I \left(\sin(\omega t + \alpha - \phi) - \sin(\alpha - \phi) e^{-\frac{Rt}{L}} \right) \quad (10)$$

where I is the current magnitude, $\omega = 2\pi f$, $f = 50$ Hz, α is the point on wave of fault inception, and $\phi = \arctan\left(\frac{\omega L}{R}\right) = \arctan\left(\frac{X}{R}\right)$. The X/R ratio is kept constant with a value of 7. Fig. 5 illustrates how I and α affect the quench time. The point of wave can delay a quench by 5-10ms, depending on the current magnitude. The sharp transition between $\pi/2$ and $3\pi/4$ is due to the combination of dc offset and current phase ($\alpha - \phi$) which results in a relatively small area under the current curve (and therefore low energy dissipation in the superconductor) during the first half cycle. If quenching does not occur within the first half-cycle of fault current, then there is a relatively long delay until quenching may occur at some point during the second half-cycle. SFCLs must therefore be carefully applied such that quenching will occur within the first cycle of fault current (at the required fault current magnitude), for any possible point on wave of fault inception.

C. Comparison of SFCL and Overcurrent Protection Current-Time Characteristics

To place the SFCL current-time curves in Fig. 4 into perspective, they can be compared with an equivalent inverse

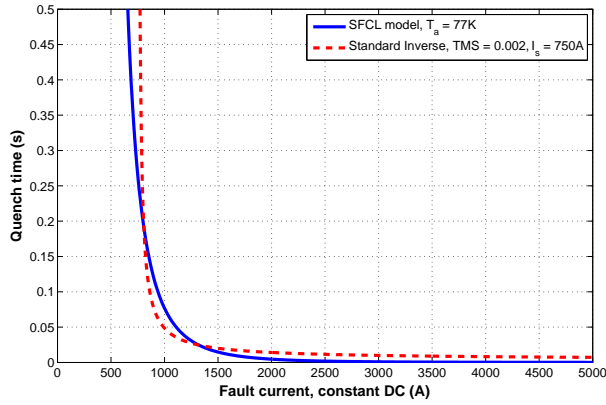


Fig. 6. SFCL model current-time characteristics vs. Standard Inverse curve

definite minimum time (IDMT) curve, commonly used for overcurrent protection. Fig. 6 compares the SFCL characteristics with the Standard Inverse curve [5], given by (11):

$$t = TMS \frac{0.14}{I_r^{0.02} - 1} \quad (11)$$

where TMS is the time multiplier setting, $I_r = \frac{I}{I_s}$, and I_s is the setting current. For Fig. 6, $TMS = 0.002$ and $I_s = 750$ A. Clearly, the curves differ, especially at higher currents, but this does demonstrate that an SFCL has similar characteristics to IDMT protection, albeit with a TMS value that would be extremely small in practice. This information could be used to coordinate the operation of multiple SFCLs in a power system in a similar fashion to the method used to coordinate overcurrent protection relays.

IV. CONCLUSIONS

This paper has established, through analysis of an existing SFCL model, that resistive SFCLs have an inverse current-time characteristic. This has been verified mathematically. The analysis approach represents a generic design tool which could help with the following:

- The coordination of multiple SFCLs, especially if they have different designs, e.g., from different manufacturers.
- Quickly investigating the effect that modifying parameters, such as superconductor length, diameter, etc., has on the current-time characteristics and the critical current value.
- As a guide for adjusting the superconductor temperature, while the SFCL is in service, to cater for different system conditions and fault current reduction requirements.
- To determine the required superconductor volume for a desired current-time characteristic (i.e., solving for k , or a particular component of k).

Note that these results are closely linked to this particular SFCL model; nevertheless, the technique is valid as long as the SFCL characteristics are well known. Further work is needed to examine the coordination of multiple SFCLs, and the implications for existing protection systems.

REFERENCES

- [1] M. Noe and M. Steurer, "High-temperature superconductor fault current limiters: concepts, applications, and development status," *Superconductor Science and Technology*, vol. 20, no. 3, pp. R15–R29, Mar. 2007.
- [2] S. Blair, N. Singh, C. Booth, and G. Burt, "Operational control and protection implications of fault current limitation in distribution networks," in *Universities Power Engineering Conference (UPEC), 2009 Proceedings of the 44th International*, 2009, pp. 1–5.
- [3] N. K. Singh, R. M. Tumilty, G. M. Burt, C. G. Bright, C. C. Brozio, D. A. Roberts, A. C. Smith, and M. Husband, "System-Level Studies of a MgB2 Superconducting Fault-Current Limiter in an Active Distribution Network," *IEEE Trans. Appl. Supercond.*, vol. 20, no. 2, pp. 54–60, Apr. 2010.
- [4] S. Blair, C. Booth, I. Elders, N. Singh, G. Burt, and J. McCarthy, "Superconducting fault current limiter application in a power-dense marine electrical system," *IET Electrical Systems in Transportation*, vol. 1, no. 3, p. 93, 2011.
- [5] Alstom Grid, *Network Protection & Automation Guide*. Alstom Grid, 2011.
- [6] I. N. Dul'kin, D. V. Yeysin, L. M. Fisher, V. P. Ivanov, A. V. Kalinov, and V. A. Sidorov, "Modeling Thermal Process in a Resistive Element of a Fault Current Limiter," *IEEE Trans. Appl. Supercond.*, vol. 18, no. 1, pp. 7–13, Mar. 2008.
- [7] R. Dommerque, S. Krämer, A. Hobl, R. Böhm, M. Bludau, J. Bock, D. Klaus, H. Piereder, A. Wilson, T. Krüger, G. Pfeiffer, K. Pfeiffer, and S. Elschner, "First commercial medium voltage superconducting fault-current limiters: production, test and installation," *Superconductor Science and Technology*, vol. 23, no. 3, p. 034020, Mar. 2010.
- [8] S. M. Blair, C. D. Booth, N. K. Singh, G. M. Burt, and C. G. Bright, "Analysis of Energy Dissipation in Resistive Superconducting Fault-Current Limiters for Optimal Power System Performance," *IEEE Trans. Appl. Supercond.*, vol. 21, no. 4, pp. 3452–3457, Aug. 2011.
- [9] W. Paul, M. Chen, M. Lakner, J. Rhyner, D. Braun, W. Lanz, and M. Kleimaier, "Superconducting fault current limiter: applications, technical and economical benefits, simulations and test results," CIGRE SC 13, Tech. Rep., 2000.
- [10] J. Langston, M. Steurer, S. Woodruff, T. Baldwin, and J. Tang, "A generic real-time computer Simulation model for Superconducting fault current limiters and its application in system protection studies," *IEEE Trans. Appl. Supercond.*, vol. 15, no. 2, pp. 2090–2093, 2005.
- [11] MathWorks, "Real-Time Workshop - Generate C code from Simulink models and MATLAB code - Simulink," 2011. [Online]. Available: <http://www.mathworks.com/products/rtw/>
- [12] S. L. Liu, G. Longyan, B. Gang, W. Haiyun, and L. Yongtao, "The temperature dependence of the resistivity in Ba 1 - x K x Fe 2 As 2 superconductors," *Superconductor Science and Technology*, vol. 24, no. 7, p. 075005, Jul. 2011.
- [13] S. Kozak, T. Janowski, B. Kondratowicz-Kucewicz, J. Kozak, and G. Wojtasiewicz, "Experimental and Numerical Analysis of Energy Losses in Resistive SFCL," *IEEE Trans. Appl. Supercond.*, vol. 15, no. 2, pp. 2098–2101, Jun. 2005.
- [14] A. Oliver, A. Smith, M. Husband, M. Bailey, and Y. Feng, "Assessment of Small Bend Diameter Magnesium Diboride Wire for a Superconducting Fault Current Limiter Application," *IEEE Trans. Appl. Supercond.*, vol. 19, no. 3, pp. 1942–1945, 2009.

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