

# Scalable Real-Time Controller Hardware-In-the-Loop Testing for Multiple Interconnected Converters

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**Abstract** -This project is aiming to make the first steps in investigating and pushing the boundaries of real-time simulation. To that end it will focus on real-time representation of converter devices on different platforms, enabling the future coupling of prototyping controllers to power system simulation tools. The small time-step, high fidelity representation of the converter devices and the large time-step model of the grid will be carried out on RTDS Technologies, RTDS and perhaps expanding the attempt, on OPAL-RT Technologies, OPAL-RT Simulator in the future. The controller prototyping, including the converter switching strategy will be implemented on ADI's rtX and the use of other rapid controller prototyping systems will also be evaluated. This will effectively allow the exploration of the scalability and limits of such schemes. Namely, how many converters can be simulated on real-time simulation devices? How many controllers can be implemented on a prototyping platform using modern microprocessors?

**Index Terms**—Hardware-In-the-Loop, prototype controllers, power electronic converters, real-time simulation.

## I. INTRODUCTION

The increased complexity of power networks and the large scale integration of intermittent energy sources, paired with the proliferation of power electronics, present many challenges in terms of quality and security of supply.

The aforementioned recent and on-going rapid growth of the wind and renewable industry means that more and more converter-connected devices are present within the national electricity grid. The behavior of these converters is highly dependent on the mix of other devices connected to the network along with the topology and the resulting system dynamics at the minute, second, millisecond, and even micro-second timescales. Traditional simulations of overall system behavior, utilized steady-state or “pseudo steady state” techniques and this was adequate for power networks dominated by generation connected via rotating plant. For emerging power networks, this is no longer true. Converters have complex response at timescales right down to the switching frequency of the power electronics. The way that converter-connected devices, including High Voltage DC (HVDC) links, provide inertial response is entirely dependent on their high-bandwidth control software and power electronic filter responses.

These facts make the accurate design, analysis and simulation of modern power networks an extremely difficult and complex procedure in terms of computational time and resources. To that end this project will investigate the real-time capabilities of prototyping controllers and power system simulation tools. This will enable further exploration of the scalability and limits of such schemes by identifying the most crucial elements of this type of network analysis and representation.

First, a brief overview of the different simulation techniques used today along with comments on their respective shortcomings and benefits is given. Next the proposed simulation configuration is presented, followed by the theoretical description of the STATCOM model. Last is the showcase of the practical implementation of the model along with preliminary results and conclusions.

## II. OVERVIEW OF SIMULATION TECHNIQUES

Simulation is a crucial element in the design and development of power generation and transmission systems. Simulation analysis is conducted to verify the system's stability and response over time under different scenarios.

The most commonly applied simulation technique is the discrete-time, software simulation. It makes use of scaled-down models of real power system components, based on fundamental frequency responses and mathematical representations of system dynamics. In these steady-state simulations, time moves in fixed steps. At the end of each step the variables and states of the system are computed. The time it takes for these functions and equations to be resolved can be shorter or longer than the actual time-step of the simulation [1], a fact that does not interfere with the accuracy of the results. This technique is useful during the early stages of design in order to analyse distinct components rather than a complex system in its entirety. The models are usually implemented on high end, in terms of computational power, PCs [2].

As the modelled cases are becoming more complex the computational burden increases to a degree where it requires an impractical amount of time to compute the solutions for the different events of a system [3]. It is of use to consider the addition of unconventional distributed generation to the energy mix. Wind and solar farms, fuel-cell storage devices and

micro-turbines present the challenge of more sensitive industrial loads in terms of power quality [4]. The widespread use of the microprocessor-based controls of fast-acting power electronic devices along with the benefits of faster reaction times of the power networks, is introducing an extended spectrum of harmonics to the system.

To get in context with the time-step and computing requirements of present day applications a comprehensive graph is provided below in fig. 1 reproduced from [1].

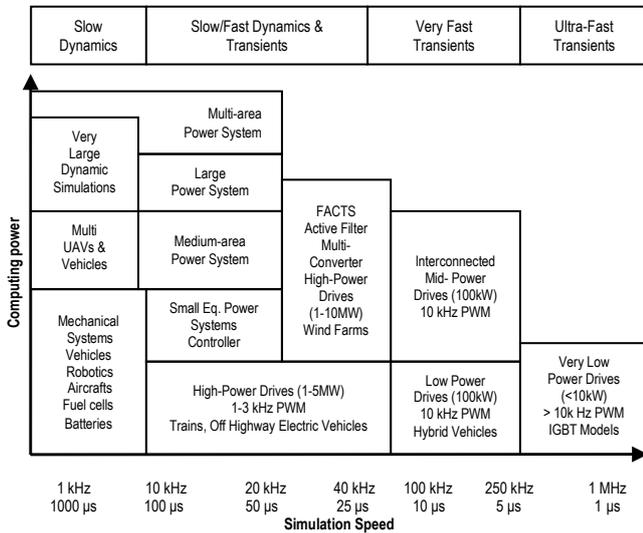


Fig. 1: Simulation speed - power relationship

Moving from left to right are applications that require increasingly smaller time-step and from bottom to top are the applications with increasing demand in computational power.

A way to accurately predict and plan ahead for the mitigation of disturbance and instability factors is real-time simulation. In this case though, the accuracy of the results is relevant to the time demanded to produce them [1] i.e. the simulator must be faster than the clock duration of the time-step. There are different approaches to realising a real-time simulation environment.

The software-based real-time approach, involves the simulation of the whole system on a real time digital simulator. Depending on the architecture, the simulator might have a single or many parallel digital processors. This allows the option of either using a single processor to carry out all the calculations or of dividing the system under test into zones and allocating each zone to a dedicated processor [5]. Another variation is the utilisation of more than one real-time simulators carrying out the necessary computations. The latter concept, because of the intercommunication through physical inputs and outputs, introduces the burden of ensuring and preserving the integrity of the signals [1]. The frequency bandwidth of the results is up to 10 kHz which might not be accurate enough for the switching operation of the power converters.

The hardware-based real-time approach or Hardware-In-the-Loop (HIL) simulation, involves the modelling representation of only a part of the system while the rest is actual hardware. There are two distinct cases of this concept. Power-Hardware-In-the-Loop (PHIL) refers to the implementation of

a controller on a real time simulator or a Rapid Control Prototyping (RPC) unit, connected to a physical power network. This method offers easier debugging and implementation of controllers. Controller-Hardware-In-the-Loop (CHIL) refers to the connection of an actual controller to a simulated network [1]. CHIL is used for controller testing at the early stages of design offering high experimental repeatability of events. Real-time simulation also has its limitations as the highly accurate simulation of the switching events of the converters, requires high frequency bandwidth. Increasing the frequency bandwidth leads to the implementation of smaller time-steps and more accurate models. Finally increasing the representation accuracy corresponds to an increase in computational time [5].

### III. PROPOSED SIMULATION APPROACH

The importance of numerically accurate solutions is made apparent by taking into account the various shortcomings discussed in II and considering them in context with the very high precision demands of the power electronic converter simulations. To that end the parallel implementation approach of a real-time digital simulator in conjunction with a high performance, rapid prototyping controller unit is proposed. The hardware configuration is illustrated in fig. 2 below.

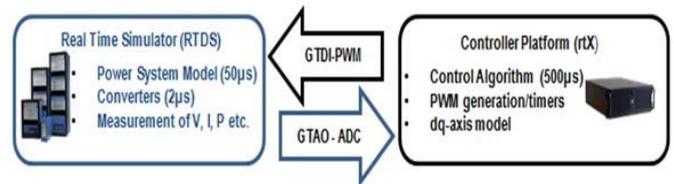


Fig. 2: Simulation configuration

The real-time digital simulator, RTDS (RTDSTM) combines real-time operation and accurate digital simulation. The models can be built in two resolution environments of 50 μs and 2 μs accuracy on a host PC and then downloaded to the simulator to be executed on one or more parallel digital signal processors. The monitor and control of the models is achieved through a dedicated software suite, RSCAD.

The rapid control prototyping unit is selected to be the rtX real-time simulator (ADITM) due to the familiarity with MATLAB/Simulink and the relative ease of controller insertion. A controller realized on rtX is flexible, faster in its implementation and easier to debug [4]. The rtX uses standard personal computer (PC) technology with a 6core Intel Xeon processor for fast, highly accurate, assessing of control strategies and real-time simulation. Mathematical models of the controller under test are imported to the dedicated software suite of the host PC called ADvantage Framework, in the native C programming language. After their upload on the rtX, the user has the choice of using the available ADC and DAC cards or even the Dynamically Programmable Gate Array (DPGA) card to interface with the other parts of the distributed simulation.

For this first interconnected modeling attempt the model of a Static Synchronous Compensator (STATCOM) will be used. Its simplified circuit is illustrated in fig. 3.

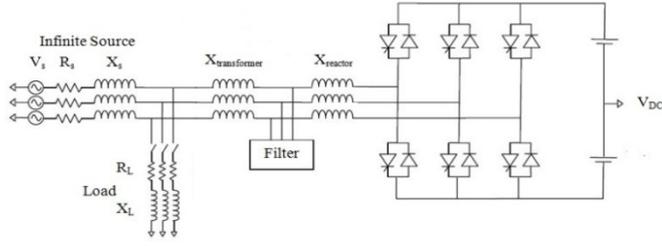


Fig. 3: Simplified STATCOM diagram

The electrical part will be implemented on RTDS. The grid side of the network will be represented in the large time-step and the STATCOM on the small time-step. The dq control and PWM switching calculation will be stripped from RSCAD and modelled on the rtX

Measurements of voltages and currents required will be gathered in real-time and transmitted to the rtX. The interface can be realised by the Digital to Analogue Converter (DAC) GTA0 I/O channel. The signals are carried by an optically shielded cable and received by the Analogue to Digital Converter (ADC) I/O card of the rtX. There the dq and PWM algorithms will make the appropriate decisions based on the transformed \$i\_d\$, \$i\_q\$ and \$u\_{dc}\$ loops [6]. The gate drive pulses will be transmitted from the DPGA I/O card of the rtX. It will be useful at this point to consider that the PWM signal is not constant. Its main parameter is the duty cycle, D which describes the proportion of "on" time of the pulses for a specified interval [7]. Equation (1) describes D.

$$D = \frac{\tau_{on}}{T} \quad (1)$$

Where \$\tau\_{on}\$ is the time of the on signal, T is the period and \$0 \le D \le 1\$. This parameter along with the switching frequency is the signal that will be sent through the rtX's DPGA card and received by the Analogue to Digital Converter (ADC) GTA1 I/O channel of the RTDS, effectively closing the loop of the simulation.

#### IV. MODEL THEORITICAL DESCRIPTION

STATCOMs provide support to electricity networks for a variety of power system stability conditions such as voltage regulation and power factor correction. The device is comprised of a Voltage Source Converter (VSC) with a DC-link capacitor, a coupling reactor, and a harmonic filter. The connection to the rest of the network is achieved by a step-down transformer. Its operating principle is based on the controllable voltage of the VSC and the voltage drop at the reactor that dictates the reactive power absorption from or injection to the system as shown by (2).

$$Q_{inj} = \frac{V_s \cdot V_c \cdot \cos \delta - V_s^2}{X} \quad (2)$$

Where \$V\_s\$ is the voltage at the grid side of the coupling transformer and \$V\_c\$ is the voltage at the VSC side of the transformer. \$\delta\$ is the angle between the grid and VSC bus and X is the leakage reactance of the transformer. When the current at the AC side of the STATCOM is leading the voltage by \$90^\circ\$, then reactive power is absorbed while when the current is lagging

the voltage by \$90^\circ\$, reactive power is injected to the power system in the form of reactive current [8] as seen in (3).

$$I_{qinj} = \frac{V_s - V_c}{X} \quad (3)$$

To ensure this kind of operation d-q vector control is employed. This control strategy involves the transformation of three phase voltages and currents into the dq reference frame and is illustrated in fig. 4 below.

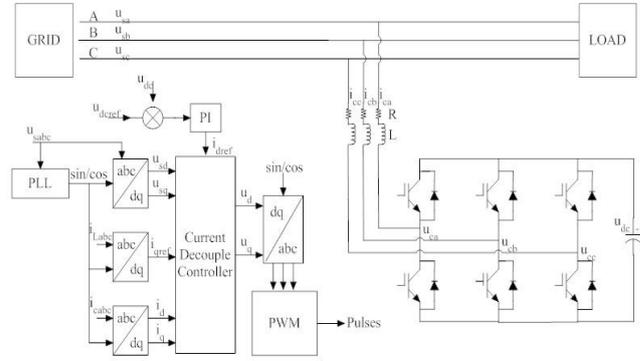


Fig. 4: dq STATCOM control.

The angle of the transformation is obtained from the three phase voltages of the grid side with a Phase Locked Loop (PLL). The values that are being transformed are:

- The voltage of the grid (\$u\_{sabc}\$) is converted to \$u\_{sd}\$, \$u\_{sq}\$.
- The current through the load (\$i\_{Labc}\$) is converted to the reactive current component, \$i\_{qref}\$.
- The compensation current of the converter (\$i\_c\$) is transformed to the active component \$i\_d\$ and reactive component \$i\_q\$.
- Additionally the voltage of the dc-link capacitor \$u\_{dc}\$ is monitored and kept stable by a PI controller and an external reference \$u\_{dcref}\$ in order to extract the active current reference, \$i\_{dref}\$.

These signals are then fed to the current decouple controller seen in fig. 5, to generate the output voltage vectors \$u\_d\$ and \$u\_q\$. One last inverse Park's transformation from dq to abc and the reference voltages, \$u\_a\$, \$u\_b\$, \$u\_c\$ are obtained. By adjusting the magnitude and the phase of the VSC voltage, the three phase reference signals are fed to the PWM scheme to generate the switching pattern for the VSC. From there, the six pulse signals are extracted for driving the power electronic switches of the VSC and in turn generate the desired reactive power.

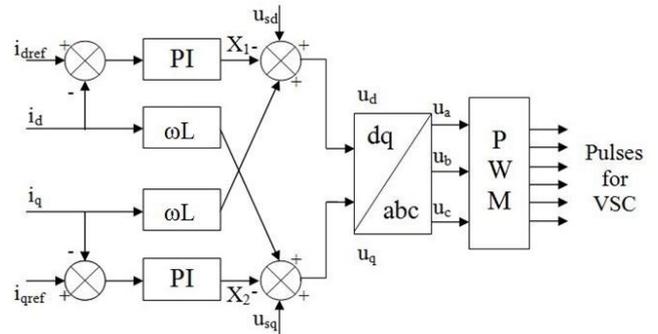


Fig. 5: Current decouple controller

## V. PRACTICAL IMPLEMENTATION OF THE MODEL

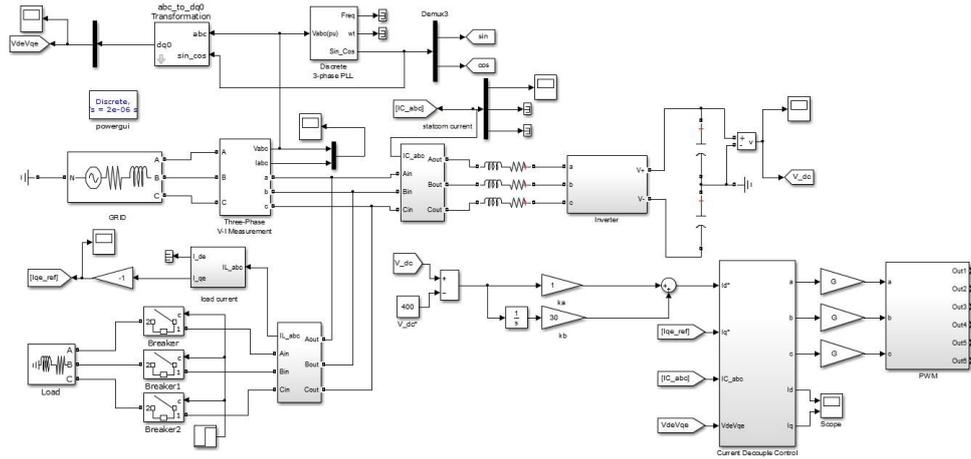


Fig. 6: MATLAB/Simulink STATCOM model

The STATCOM is first modeled in MATLAB/Simulink environment to verify its electrical and control behavior. The model is depicted in Fig. 6 above.

The time-step of the simulation was set to  $2 \mu\text{s}$ . The load is connected at 0.6 s. The orderly operation of the model can be deduced from fig. 7 that shows the active and reactive current components, fig. 8 shows the DC-link voltage and fig. 9 shows the peak, line to line, rms voltage of the network and the current of the load.

When the load connects, it draws active and reactive power. This causes a disturbance at the DC-link voltage that is compensated within 0.1 s thus regulating the active power. Reactive current is being injected from the STATCOM to regulate the reactive power. The model's behavior appears to be representative of the operation of the real equipment.

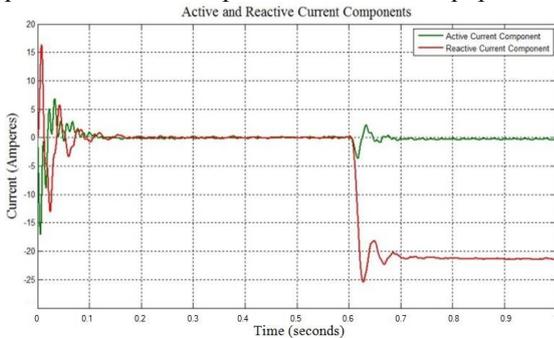


Fig. 7: Active and reactive current component

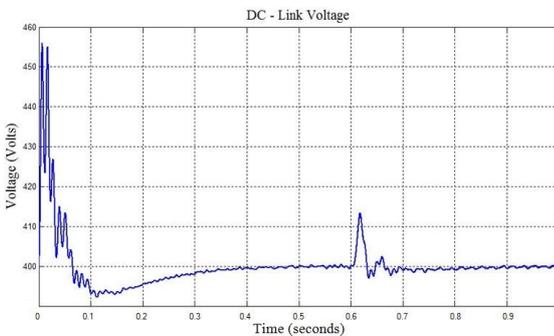


Fig. 8: DC-link voltage

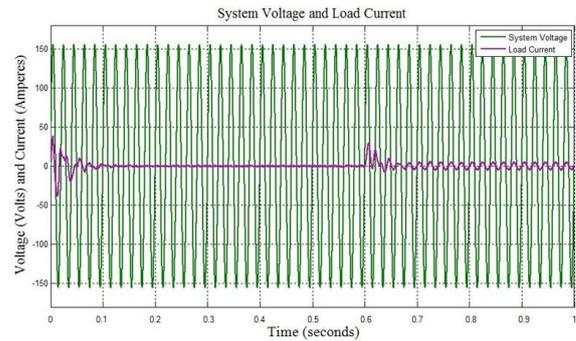


Fig. 9: System voltage and load current

The next step is the implementation and real-time simulation on the RTDS through a host PC equipped with the RSCAD. The grid side of the network was developed in the large time-step environment ( $50 \mu\text{s}$ ) illustrated in fig. 10. The STATCOM device and the control are carried out at the small time-step domain ( $2 \mu\text{s}$ ), fig. 11, since the converter and its switching scheme require a high fidelity simulation environment [9]. The switching frequency is 3 kHz.

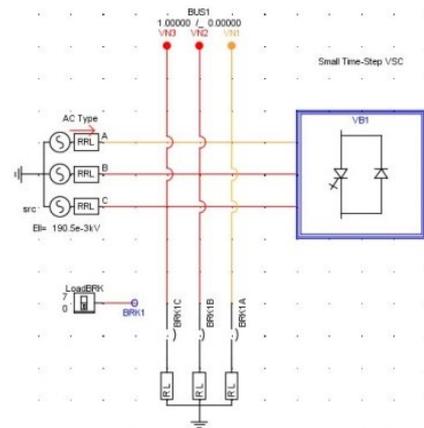


Fig. 10: Grid model in large time-step

Since the scope of the project is to utilize the RTDS real-time simulation capabilities only for the electrical part of the model a more simplified control method was used. The amount of injected reactive power is given by:

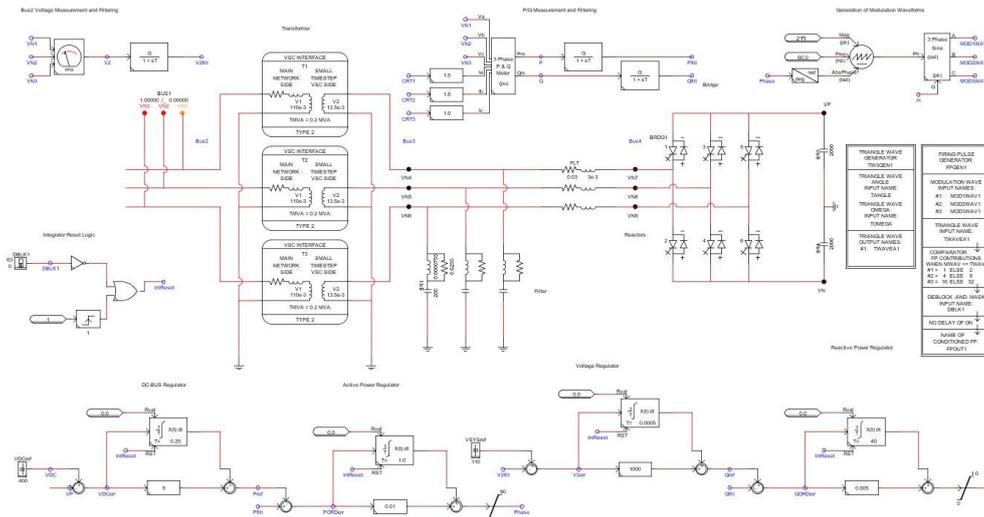


Fig. 11: STATCOM and control model in small time-step

$$Q_{inj} = \frac{V_s V_c \cos \delta - V_s^2}{x} \quad (4)$$

Where  $V_s$  is the voltage of the grid,  $V_c$  is the voltage of the converter,  $\delta$  is the angle between  $V_s$  and  $V_c$ . So the amount of injected reactive power is controlled by  $V_s$ .

The way the various system values are represented in the RSCAD is different from the MATLAB/Simulink model. Bearing in mind that in this case the load is manually switched on and the VSC valves are manually de-blocked, explains the difference in the time scale of the figures and is the reason of the depiction of different measurements. The calculations for system voltage and reactive power are carried out in the per unit system. The carried out simulation demonstrates that the design behaves accurately and as desired. A further addition to the model is the harmonic filter tuned to compensate the harmonics at the switching frequency of 3 kHz that is inserted between the coupling transformer and the VSC reactor. The load draws  $Q_L=5000\text{Var}$  close to 8.3pu in the 600 kVA system base. When the load is connected through the three phase breaker there is a voltage dip on the grid side of the network that corresponds to 0.021pu, but is quickly dealt with by reactive power injection while the dc side voltage is maintained at a preset value of 400 V. These are illustrated at figs. 12 - 14.

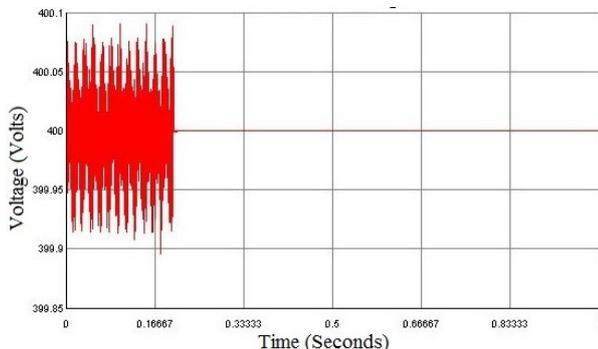


Fig. 12: DC-link voltage

It is worth noticing that there are a lot more spikes in the DC-link voltage compared to the previous simulations. One

possible explanation could be the time delay in the interface of the two different time-step environments.

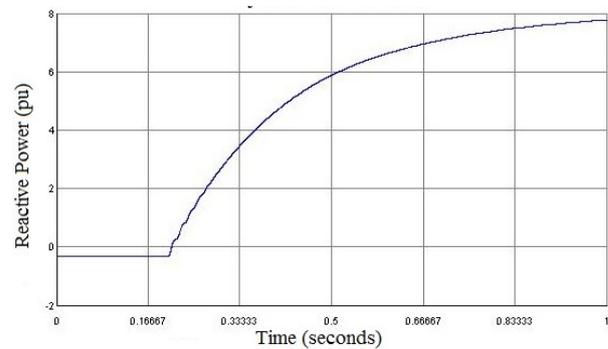


Fig. 13: Injected reactive power

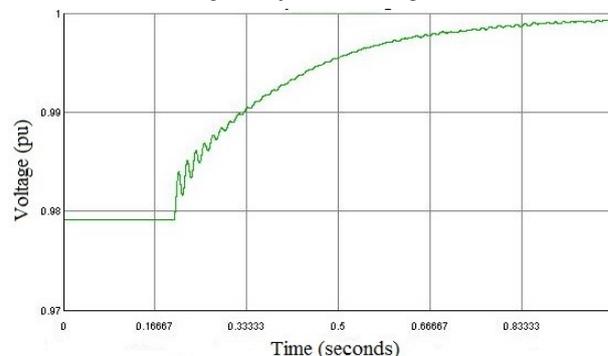


Fig. 14: System voltage

The last step is the real-time simulation of the model on the selected RPC, the rtX. The ADvantage Framework software in conjunction with the Real Time Workshop is responsible for the MATLAB code translation. Once the model is uploaded, its ports need to be specified and configured and only then is the project ready to be built. Advantage VI is launched charged with the task of attaching the project to the preferred simulation target. The results of the real-time simulation show a strong relationship and are consistent with the offline MATLAB simulation see fig. 15-17. This is to be expected since the coding of the model is essentially the same.

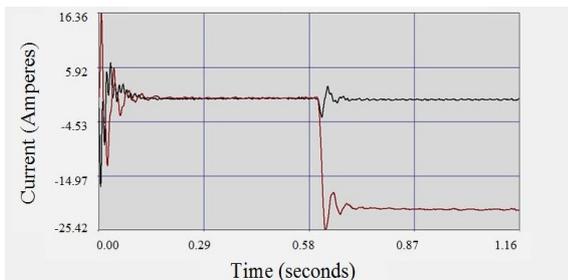


Fig. 15: Active and reactive current component

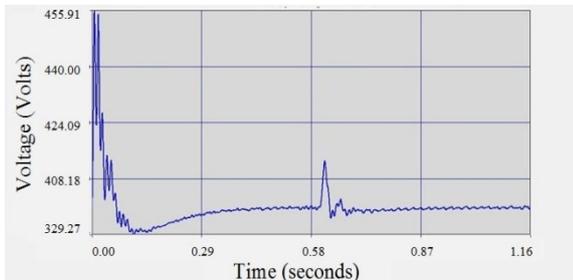


Fig. 16: DC-link voltage

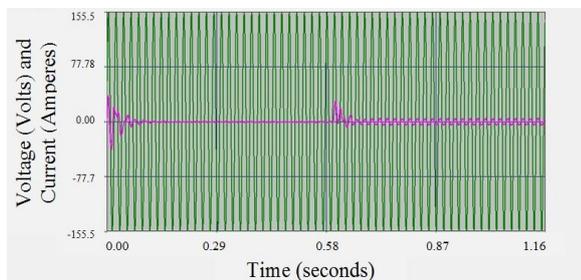


Fig. 17: System voltage and load current

## VI. CONCLUSIONS AND FUTURE WORK

This paper explored the procedure of implementing a model for real-time simulation and transient analysis. The STATCOM was selected as the modelling case due to the high computational accuracy, necessary for the calculation of the control of its converters' switching scheme. It was demonstrated that both the RTDS and the rtX are capable of individually fulfilling their real-time tasks.

Since their respective efficiency has been verified the models will be more accurately represented and this ongoing project will focus on the actual coupling architecture of the RTDS and rtX. The intention is to design and configure an optimal testing environment for networks with high penetration on converter controlled and connected devices.

The simulation process will be extended by simulating an increasing number of converters and control schemes on the RTDS and rtX effectively taking the simulation to the next level by exploring the limits and scalability of such a scheme. At this stage the optimization of loop and input/output delays will be of great importance and new techniques will have to be developed for their compensation. The selection of the sampling frequency is equally important as it can lead to unstable performance of the system. It should be selected at least two orders of magnitude larger than the maximum switching frequency of the converters [10].

Capitalizing on the knowledge and data collected from the creation, evaluation and improvement of this simulation approach, real-time Controller and Power hardware-in-the-loop testing boundaries can be pushed further and novel concepts safely and explored. The industrial sectors where the proposed simulation environment can be utilized for testing are:

1. Power networks, incorporating power quality improvement and protection devices that are converter-connected and software controlled, having complex high bandwidth responses.
2. More-Electric ships and Aircrafts, where power sources and propulsion loads are transitioning from rotary and constant impedance devices to complex converter-connected devices, allowing variable-frequency, variable-voltage and bidirectional flow operation.
3. HVDC multiple interconnected converters and multi-level converter applications, modeled at switching-level fidelity.
4. Novel concepts of future power grids, utilizing distributed generation power sources and synthetic inertial responses.
5. Hybrid electric vehicles with hydrogen based power sources that comprise of batteries, DC/DC converters, fuel-cells and permanent magnet motor drives [1].

With real-time simulation, the interactions of the mentioned power systems with other control and protection schemes can be tested and evaluated before commissioning. This will provide useful and in depth data for the limits, optimization and further improvement without the constraints that computational time and power as well as mathematical model complexity, puts on traditional steady-state simulations.

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